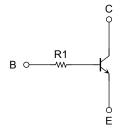
TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

# **RN1110FT,RN1111FT**

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

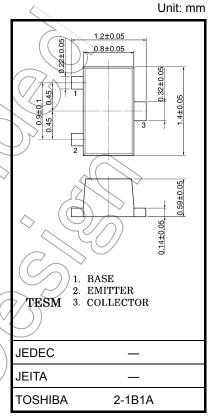
- High-density mount is possible because of devices housed in very thin TESM packages.
- Incorporating a bias resistor into a transistor reduces parts count.
   Reducing the parts count enable the manufacture of ever more compact equipment and save assembly cost.
- Wide range of resistor values are available to use in various circuitdesigns.
- Complementary to RN2110FT, RN2111FT

#### **Equivalent Circuit and Bias Resistor Values**



### Absolute Maximum Ratings (Ta = 25°C)

	$\sim$		
Characteristics	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub> <	(50//)	V
Collector-emitter voltage	V <sub>CEO</sub>	50	<b>V</b>
Emitter-base voltage	VEBO	5	>
Collector current	Ic	100	mA
Collector power dissipation	Pc	100	mW
Junction temperature	<1/	150	°C
Storage temperature range	T <sub>stg</sub>	-55~150	°C



Weight: 0.0022 g (typ.)

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

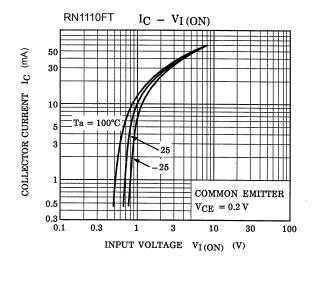
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

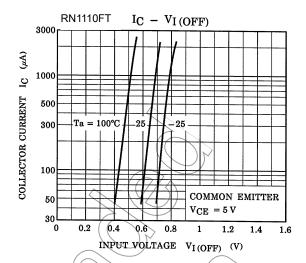
## **Electrical Characteristics (Ta = 25°C)**

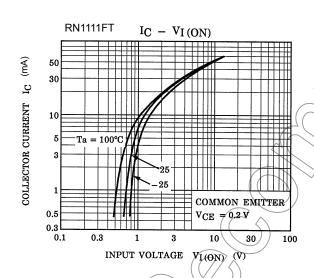
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off curre	ent	I <sub>CBO</sub>	$V_{CB} = 50 \text{ V}, I_{E} = 0$	_	_	100	nA
Emitter cut-off curren	t	I <sub>EBO</sub>	V <sub>EB</sub> = 5 V, I <sub>C</sub> = 0	/_	_	100	nA
DC current gain		h <sub>FE</sub>	$V_{CE} = 5 \text{ V}, I_{C} = 1 \text{ mA}$	120	_	700	
Collector-emitter satu	ration voltage	V <sub>CE</sub> (sat)	$I_C = 5 \text{ mA}, I_B = 0.25 \text{ mA}$	1	0.1	0.3	V
Transition frequency		f <sub>T</sub>	V <sub>CE</sub> = 10 V, I <sub>C</sub> = 5 mA	\ \	250	_	MHz
Collector output capacitance		C <sub>ob</sub>	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz	$\mathcal{A}$	3	6	pF
Input resistor	RN1110FT	- R1	- Or	3.29	4.7	6.11	kΩ
	RN1111FT			7	10	13	

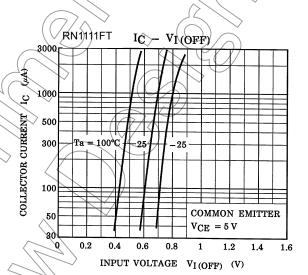


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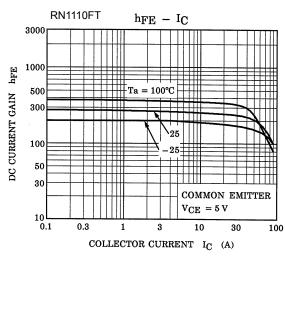


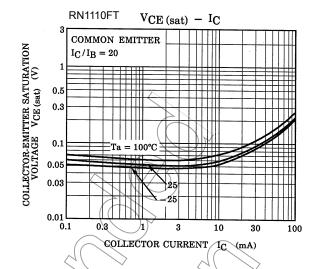


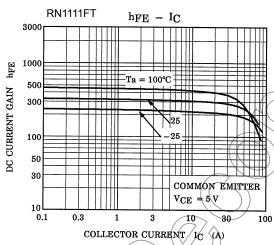


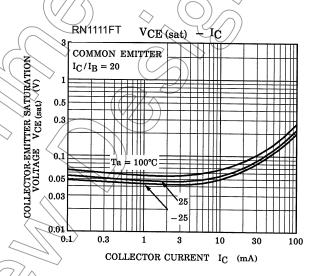


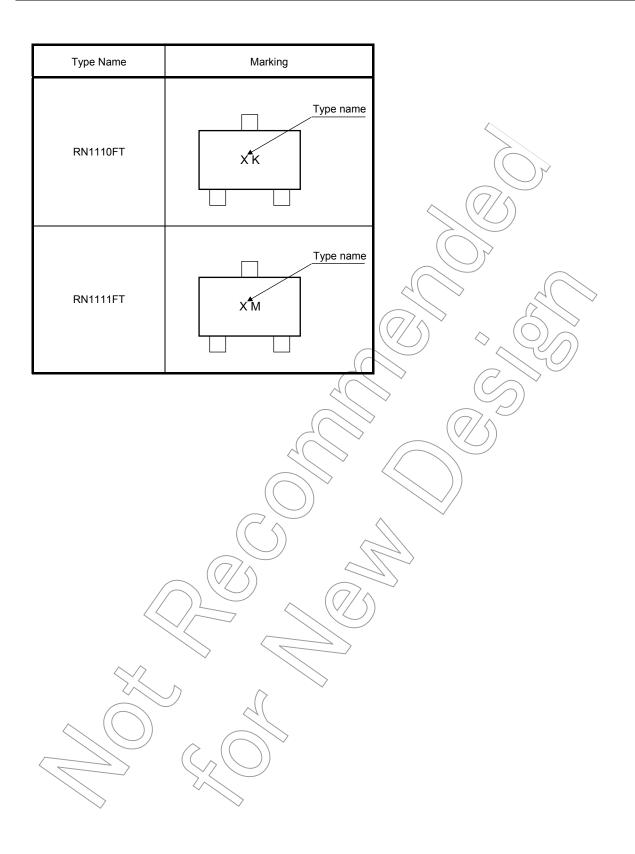
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