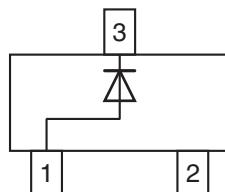


Small Signal Switching Diode



FEATURES

- Silicon epitaxial planar diode
- Fast switching diode in case SOT-23, especially suited for automatic insertion.
- These diodes are also available in other case styles including: the DO-35 case with the type designation 1N4448, the MiniMELF case with the type designation LL4448, and the SOD-123 case with the type designation 1N4448W-V.
- AEC-Q101 qualified
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT

MECHANICAL DATA

Case: SOT-23

Weight: approx. 8.8 mg

Packaging codes/options:

GS18/10K per 13" reel (8 mm tape), 10K/box

GS08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE				
PART	ORDERING CODE	TYPE MARKING	INTERNAL CONSTRUCTION	REMARKS
IMBD4448-V	IMBD4448-V-GS18 or IMBD4448-V-GS08	A3	Single diode	Tape and reel

ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25^{\circ}\text{C}$, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V_R	75	V
Peak reverse voltage		V_{RM}	100	V
Rectified current (average) half wave rectification with resistive load (1)	$f \geq 50 \text{ Hz}$	$I_{F(AV)}$	150	mA
Surge forward current	$t < 1 \text{ s}$ and $T_J = 25^{\circ}\text{C}$	I_{FSM}	500	mA
Power dissipation (1)		P_{tot}	350	mW

THERMAL CHARACTERISTICS ($T_{amb} = 25^{\circ}\text{C}$, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Thermal resistance junction to ambient air (1)		R_{thJA}	450	K/W
Junction temperature		T_j	150	°C
Storage temperature range		T_{stg}	- 65 to + 150	°C

Note
⁽¹⁾ Device on fiberglass substrate, see layout (SOT-23).

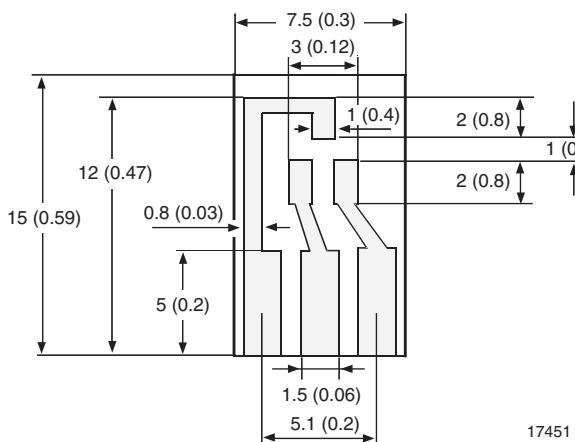
ELECTRICAL CHARACTERISTICS ($T_{amb} = 25^\circ C$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 5 \text{ mA}$	V_F	0.62		0.72	V
	$I_F = 100 \text{ mA}$	V_F			1	V
Leakage current	$V_R = 70 \text{ V}$	I_R			2.5	μA
	$V_R = 70 \text{ V}, T_j = 150^\circ C$	I_R			50	μA
	$V_R = 25 \text{ V}, T_j = 150^\circ C$	I_R			30	μA
Diode capacitance	$V_F = V_R = 0 \text{ V}$	C_D			4	pF
Reverse recovery time (see figures)	$I_F = 10 \text{ mA}$ to $i_R = 1 \text{ mA}$, $V_R = 6 \text{ V}$, $R_L = 100 \Omega$	t_{rr}			4	ns

Layout For R_{thJA} test

Thickness:

Fiberglass 1.5 mm (0.059 in.)

Copper leads 0.3 mm (0.012 in.)



TYPICAL CHARACTERISTICS ($T_{amb} = 25^\circ C$, unless otherwise specified)

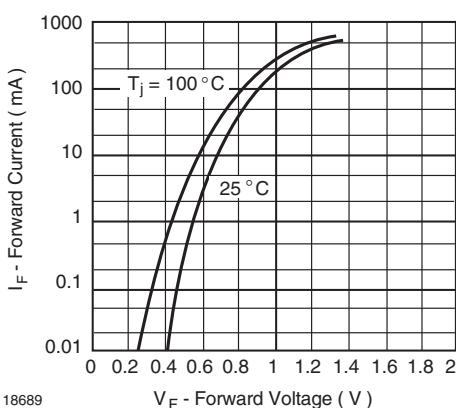


Fig. 1 - Forward Current vs. Forward Voltage

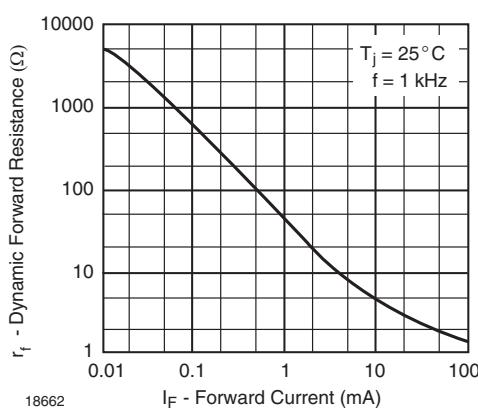


Fig. 2 - Dynamic Forward Resistance vs. Forward Current

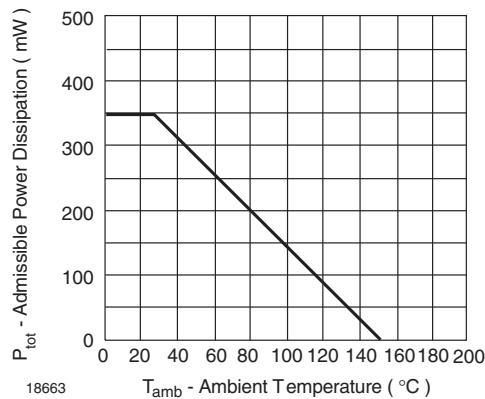


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

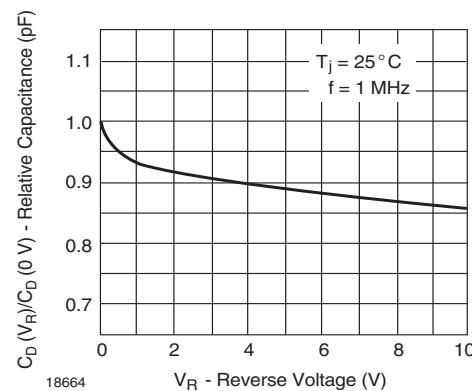


Fig. 4 - Relative Capacitance vs. Reverse Voltage

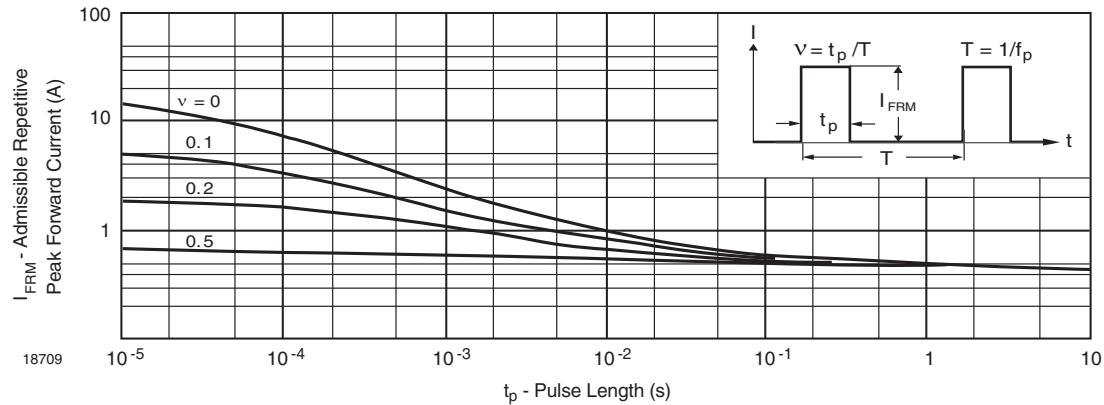
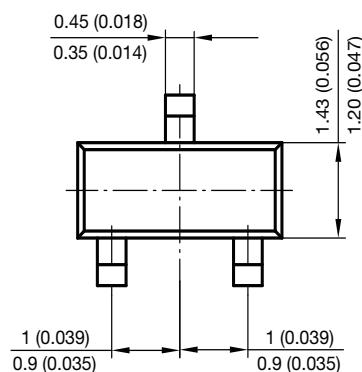
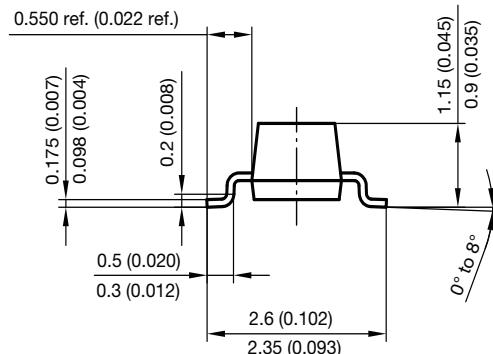
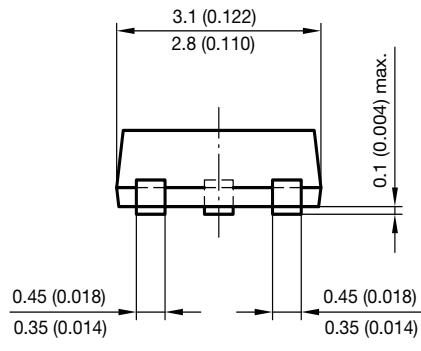
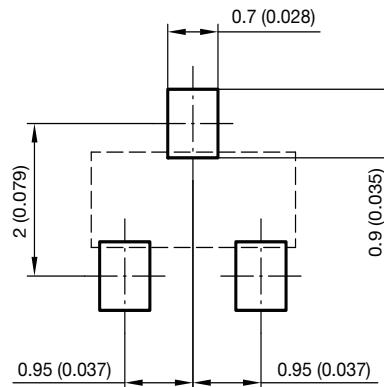


Fig. 5 - Admissible Repetitive Peak Forward Current vs. Pulse Duration

PACKAGE DIMENSIONS in millimeters (inches): **SOT-23**


Foot print recommendation:



Document no.: 6.541-5014.01-4
 Rev. 8 - Date: 23.Sept.2009
 17418

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