# 74AUP1G885

# Low-power dual function gate

Rev. 13 — 12 August 2024

**Product data sheet** 

## 1. General description

The 74AUP1G885 is a dual function gate. The output state of the outputs (1Y, 2Y) is determined by the inputs (A, B and C). The output 1Y provides the Boolean function:  $1Y = A \times C$ . The output 2Y provides the Boolean function:  $2Y = \overline{A} \times B + A \times \overline{C}$ .

Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times.

This device ensures very low static and dynamic power consumption across the entire  $V_{\text{CC}}$  range from 0.8 V to 3.6 V.

This device is fully specified for partial power down applications using I<sub>OFF</sub>. The I<sub>OFF</sub> circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

### 2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- CMOS low power dissipation
- High noise immunity
- Low static power consumption; I<sub>CC</sub> = 0.9 μA (maximum)
- Overvoltage tolerant inputs to 3.6 V
- Low noise overshoot and undershoot < 10 % of V<sub>CC</sub>
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 100 mA per JESD78 Class II
- · Complies with JEDEC standards:
  - JESD8-12 (0.8 V to 1.3 V)
  - JESD8-11 (0.9 V to 1.65 V)
  - JESD8-7 (1.2 V to 1.95 V)
  - JESD8-5 (1.8 V to 2.7 V)
  - JESD8-B (2.7 V to 3.6 V)
- ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 3A exceeds 5000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C



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# 3. Ordering information

**Table 1. Ordering information** 

Type number	Package									
	Temperature range	Name	Description	Version						
74AUP1G885DC	-40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package; 8 leads; body width 2.3 mm	SOT765-1						
74AUP1G885GT	-40 °C to +125 °C	XSON8	plastic extremely thin small outline package; no leads; 8 terminals; body 1 × 1.95 × 0.5 mm	SOT833-1						
74AUP1G885GN	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.2 × 1.0 × 0.35 mm	SOT1116						
74AUP1G885GS	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.35 × 1.0 × 0.35 mm	SOT1203						

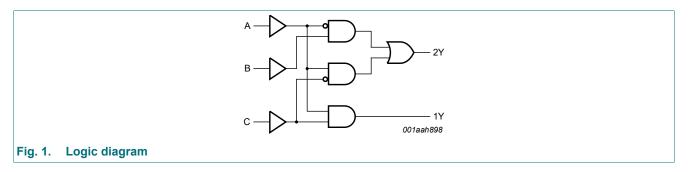
# 4. Marking

Table 2. Marking codes

Type number	Marking code [1]
74AUP1G885DC	pS8
74AUP1G885GT	pS8
74AUP1G885GN	58
74AUP1G885GS	58

<sup>[1]</sup> The pin 1 indicator is located on the lower left corner of the device, below the marking code.

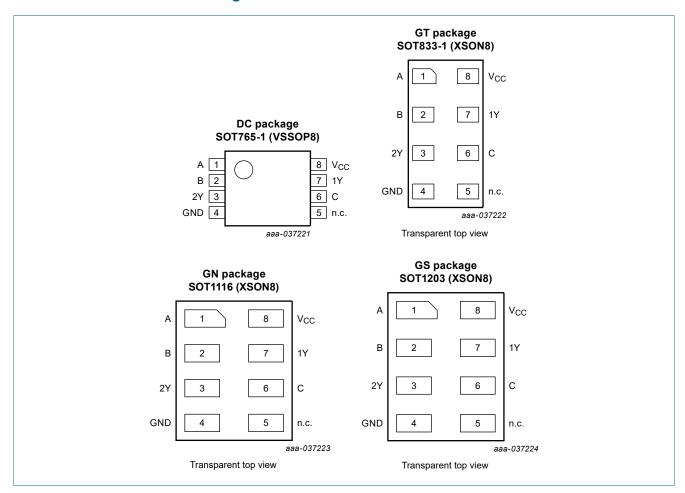
# 5. Functional diagram



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# 6. Pinning information

### 6.1. Pinning



## 6.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
A, B, C	1, 2, 6	data input
GND	4	ground (0 V)
n.c.	5	not connected
1Y, 2Y	7, 3	data output
V <sub>CC</sub>	8	supply voltage

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# 7. Functional description

#### **Table 4. Function table**

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$ 

Input			Output	Output		
A	В	С	1Y	2Y		
L	L	L	L	L		
Н	L	L	L	Н		
L	Н	L	L	Н		
Н	Н	L	L	Н		
L	L	Н	L	L		
Н	L	Н	Н	L		
L	Н	Н	L	Н		
Н	Н	Н	Н	L		

## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-0.5	+4.6	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V	-50	-	mA
VI	input voltage	[1]	-0.5	+4.6	V
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < 0 V	-50	-	mA
Vo	output voltage	Active mode and Power-down mode [1]	-0.5	+4.6	V
Io	output current	$V_O = 0 V \text{ to } V_{CC}$	-	±20	mA
I <sub>CC</sub>	supply current		-	50	mA
$I_{GND}$	ground current		-50	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40  ^{\circ}\text{C} \text{ to } +125  ^{\circ}\text{C}$ [2]	-	250	mW

<sup>[1]</sup> The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

# 9. Recommended operating conditions

**Table 6. Operating conditions** 

Symbol	Parameter Conditions		Min	Max	Unit
V <sub>CC</sub>	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V <sub>CC</sub>	V
		Power-down mode; V <sub>CC</sub> = 0 V	0	3.6	V
T <sub>amb</sub>	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 0.8 V to 3.6 V	-	200	ns/V

<sup>[2]</sup> For SOT765-1 (VSSOP8) package: Ptot derates linearly with 4.9 mW/K above 99 °C.

For SOT833-1 (XSON8) package:  $P_{tot}$  derates linearly with 3.1 mW/K above 68 °C.

For SOT1116 (XSON8) package: Ptot derates linearly with 4.2 mW/K above 90 °C.

For SOT1203 (XSON8) package: Ptot derates linearly with 3.6 mW/K above 81 °C.

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# 10. Static characteristics

#### **Table 7. Static characteristics**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = 2	25 °C		-			
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.70V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.65V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.30V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.35V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output	$V_I = V_{IH}$ or $V_{IL}$				
	voltage	$I_{O}$ = -20 $\mu$ A; $V_{CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.75V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.11	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.32	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	2.05	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.9	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.72	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.6	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.31	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.31	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.31	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.44	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.31	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.44	V
l <sub>l</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.1	μΑ
I <sub>OFF</sub>	power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V	-	-	±0.2	μΑ
Δl <sub>OFF</sub>	additional power-off leakage current	$V_1$ or $V_0 = 0 V$ to 3.6 V; $V_{CC} = 0 V$ to 0.2 V	-	-	±0.2	μΑ
I <sub>CC</sub>	supply current	$V_I$ = GND or $V_{CC}$ ; $I_O$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.5	μA
ΔI <sub>CC</sub>	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$ [1]	-	-	40	μΑ
Cı	input capacitance	$V_{CC}$ = 0 V to 3.6 V; $V_I$ = GND or $V_{CC}$	-	0.6	-	pF
Co	output capacitance	$V_O = GND; V_{CC} = 0 V$	-	1.3	-	pF

**Product data sheet** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = -	40 °C to +85 °C				-	•
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.70V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.65V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.30V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.35V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output	$V_I = V_{IH}$ or $V_{IL}$				
	voltage	$I_{O}$ = -20 $\mu$ A; $V_{CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.7V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.03	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.30	-	-	V
		$I_{O}$ = -2.3 mA; $V_{CC}$ = 2.3 V	1.97	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.85	-	-	V
		$I_{O}$ = -2.7 mA; $V_{CC}$ = 3.0 V	2.67	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.55	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		$I_{O}$ = 20 $\mu$ A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.33	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.45	V
I <sub>I</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.5	μA
I <sub>OFF</sub>	power-off leakage current	$V_1$ or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.5	μA
ΔI <sub>OFF</sub>	additional power-off leakage current	$V_1$ or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.6	μΑ
I <sub>CC</sub>	supply current	$V_I$ = GND or $V_{CC}$ ; $I_O$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.9	μΑ
ΔI <sub>CC</sub>	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$ [1]	-	-	50	μA

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = -	40 °C to +125 °C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.75V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.70V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.25V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.30V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output	$V_I = V_{IH}$ or $V_{IL}$				
	voltage	$I_{O}$ = -20 $\mu$ A; $V_{CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.11	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.6V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	0.93	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.17	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.77	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.67	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.40	-	-	V
		$I_{O}$ = -4.0 mA; $V_{CC}$ = 3.0 V	2.30	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		$I_{O}$ = 20 $\mu$ A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.11	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.33V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.41	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.39	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.36	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.50	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.50	V
l <sub>l</sub>	input leakage current	$V_I$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.75	μΑ
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O} = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.75	μΑ
ΔI <sub>OFF</sub>	additional power-off leakage current	$V_1$ or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.75	μA
I <sub>CC</sub>	supply current	$V_1$ = GND or $V_{CC}$ ; $I_0$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	1.4	μA
ΔI <sub>CC</sub>	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$ [1]	-	-	75	μA

<sup>[1]</sup> One input at  $V_{CC}$  - 0.6 V, other inputs at  $V_{CC}$  or GND.

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# 11. Dynamic characteristics

### **Table 8. Dynamic characteristics**

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 3.

Symbol	Parameter	Conditions		25 °C			°C to 5 °C	-40 °C to +125 °C		Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
C <sub>L</sub> = 5 p	F									
t <sub>pd</sub>	propagation	A, C to 1Y; see <u>Fig. 2</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	17.3	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	1.1	5.2	9.7	0.9	12.8	0.9	14.2	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.2	3.7	5.9	1.0	7.8	1.0	8.6	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.1	3.0	4.8	0.9	6.2	0.9	6.9	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.1	2.4	3.6	1.0	4.1	1.0	4.5	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.1	2.1	3.1	1.0	3.6	1.0	4.1	ns
		A, B to 2Y; see <u>Fig. 2</u> [2]								
		V <sub>CC</sub> = 0.8 V	-	21.5	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	1.7	6.0	12.7	1.4	12.8	1.4	14.2	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.7	4.2	7.2	1.4	7.8	1.4	8.7	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.4	3.3	5.8	1.2	6.5	1.2	7.2	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.2	2.6	4.1	1.0	4.7	1.0	5.2	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.1	2.3	3.5	0.9	3.8	0.9	4.2	ns
C <sub>L</sub> = 10	pF					'		'		
t <sub>pd</sub>	propagation	A, C to 1Y; see <u>Fig. 2</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	20.8	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	1.2	6.1	11.4	1.2	14.6	1.2	16.1	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.4	4.3	7.2	1.2	8.7	1.2	9.6	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.4	3.6	5.7	1.3	6.8	1.3	7.5	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.4	2.9	4.2	1.2	4.8	1.2	5.4	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.4	2.7	3.9	1.3	4.1	1.3	4.6	ns
		A, B to 2Y; see <u>Fig. 2</u> [2]								
		V <sub>CC</sub> = 0.8 V	-	25.0	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	1.8	6.9	14.4	1.7	14.6	1.7	16.1	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.9	4.8	8.5	1.5	9.1	1.5	10.1	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.9	3.9	6.6	1.7	7.2	1.7	8.0	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.5	3.1	4.7	1.3	5.4	1.3	5.9	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.4	2.8	4.3	1.3	4.6	1.3	5.1	ns

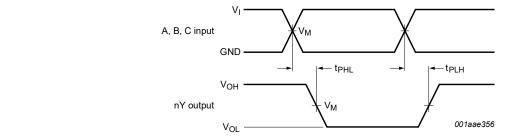
Symbol	Parameter	Conditions		25 °C			°C to 5 °C	-40 °C to +125 °C		Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
C <sub>L</sub> = 15	pF					•				
t <sub>pd</sub>	propagation	A, C to 1Y; see <u>Fig. 2</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	_	24.3	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	1.3	6.9	13.0	1.2	16.2	1.2	17.9	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.7	4.9	8.0	1.4	9.7	1.4	10.8	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.5	4.1	6.4	1.4	7.6	1.4	8.4	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.7	3.4	5.0	1.6	5.4	1.6	6.0	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.7	3.1	4.4	1.6	4.7	1.6	5.3	ns
		A, B to 2Y; see <u>Fig. 2</u> [2]								
		V <sub>CC</sub> = 0.8 V	-	28.5	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.1	7.7	16.0	1.9	16.3	1.9	18.0	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.2	5.4	9.4	2.4	10.3	2.4	11.4	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.0	4.4	7.4	1.8	8.2	1.8	9.1	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.8	3.6	5.5	1.6	6.0	1.6	6.7	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.7	3.3	4.8	1.5	5.2	1.5	5.8	ns
C <sub>L</sub> = 30	pF					'				
t <sub>pd</sub>	propagation delay	A, C to 1Y; see <u>Fig. 2</u> [2]								
		V <sub>CC</sub> = 0.8 V	-	34.7	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.4	9.2	17.7	2.3	20.9	2.3	23.0	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.5	6.5	10.6	2.5	12.2	2.5	13.5	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.5	5.4	8.5	2.4	9.4	2.4	10.4	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.6	4.5	6.4	2.4	7.0	2.4	7.7	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.5	4.2	5.7	2.3	6.6	2.3	7.3	ns
		A, B to 2Y; see <u>Fig. 2</u> [2]								
		V <sub>CC</sub> = 0.8 V	-	38.9	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.6	10.0	20.5	2.6	21.5	2.6	23.7	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.6	6.9	11.9	2.6	13.2	2.6	14.5	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.7	5.7	9.5	2.7	10.5	2.7	11.6	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.5	4.7	6.9	2.5	7.6	2.5	8.4	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.4	4.4	6.1	2.4	7.1	2.4	7.9	ns
C <sub>L</sub> = 5 p	F, 10 pF, 15 pF	and 30 pF								
C <sub>PD</sub>	power	$f_i = 1 \text{ MHz}; V_I = \text{GND to } V_{CC}$ [3]								
	dissipation capacitance	V <sub>CC</sub> = 0.8 V	-	2.7	-	-	-	-	-	pF
	Sapasitarios	V <sub>CC</sub> = 1.1 V to 1.3 V	_	2.9	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	3.0	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.65 V to 1.95 V	_	3.1	-	-	-	-	-	pF
		V <sub>CC</sub> = 2.3 V to 2.7 V	_	3.5	-	-	-	-	-	pF
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	4.1	-	-	-	-	-	pF

<sup>[1]</sup> All typical values are measured at nominal  $V_{\mbox{\scriptsize CC}}.$ 

 $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .  $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu$ W).  $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$  where:  $f_i$  = input frequency in MHz;  $f_o$  = output frequency in MHz;  $C_L$  = output load capacitance in pF;  $V_{CC}$  = supply voltage in V; N = number of inputs switching;  $\Sigma(C_L \times V_{CC}^2 \times f_o)$  = sum of the outputs.

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### 11.1. Waveforms and test circuit



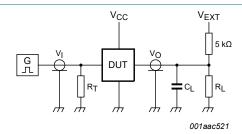
Measurement points are given in Table 9.

Logic levels: V<sub>OL</sub> and V<sub>OH</sub> are typical output voltage levels that occur with the output load.

Fig. 2. The data input (A, B, C) to output (nY) propagation delays

**Table 9. Measurement points** 

Supply voltage	Output	Input		
V <sub>CC</sub>	V <sub>M</sub>	V <sub>M</sub>	VI	$t_r = t_f$
0.8 V to 3.6 V	0.5 × V <sub>CC</sub>	0.5 × V <sub>CC</sub>	V <sub>CC</sub>	≤ 3.0 ns



Test data is given in Table 10.

Definitions for test circuit:

R<sub>I</sub> = Load resistance.

C<sub>L</sub> = Load capacitance including jig and probe capacitance.

 $R_T$  = Termination resistance should be equal to the output impedance  $Z_o$  of the pulse generator.

 $V_{EXT}$  = External voltage for measuring switching times.

Fig. 3. Test circuit for measuring switching times

#### Table 10. Test data

Supply voltage	Load	V <sub>EXT</sub>			
V <sub>CC</sub>	CL	R <sub>L</sub> [1]	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 × V <sub>CC</sub>

[1] For measuring enable and disable times  $R_L$  = 5  $k\Omega.$ 

For measuring propagation delays, set-up and hold times and pulse width  $R_L$  = 1  $M\Omega$ .

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# 12. Package outline

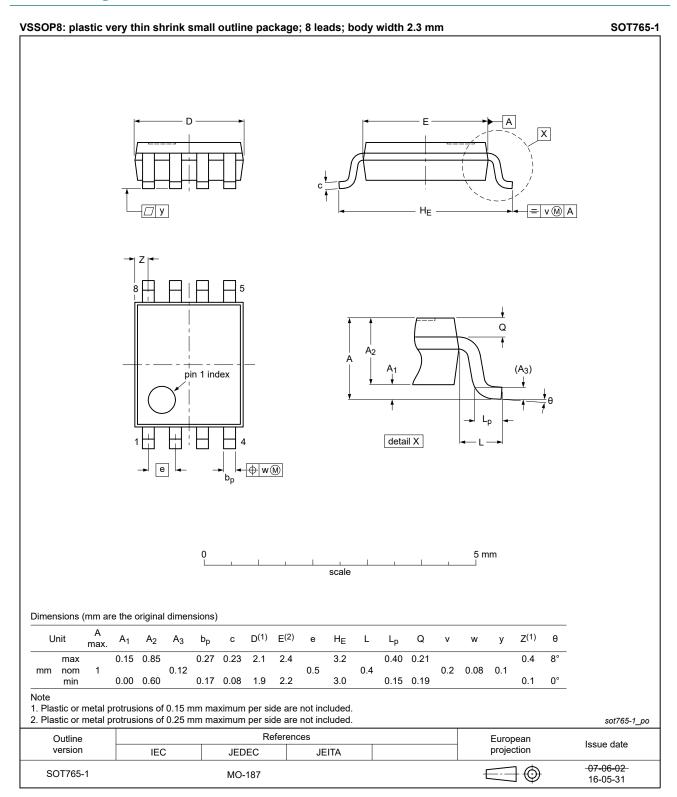


Fig. 4. Package outline SOT765-1 (VSSOP8)

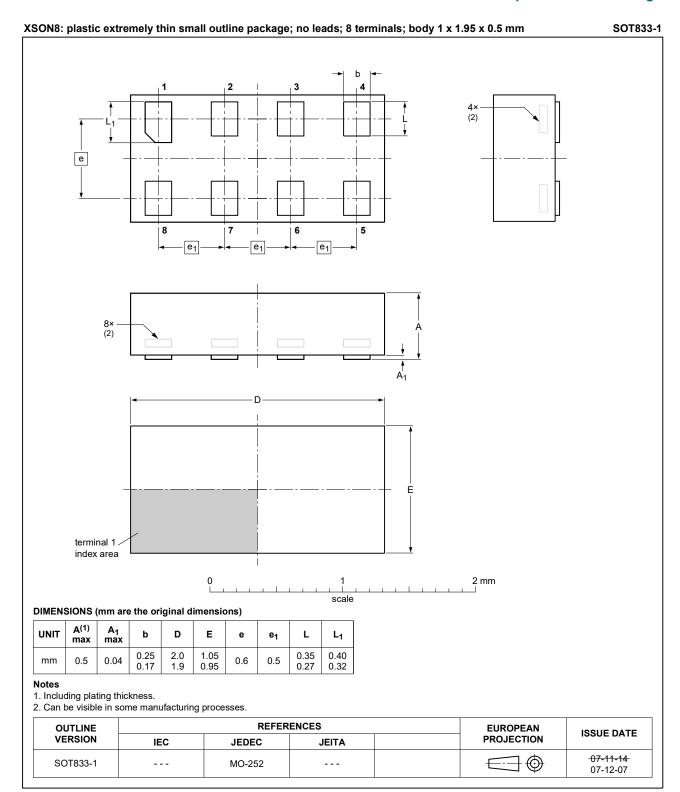


Fig. 5. Package outline SOT833-1 (XSON8)

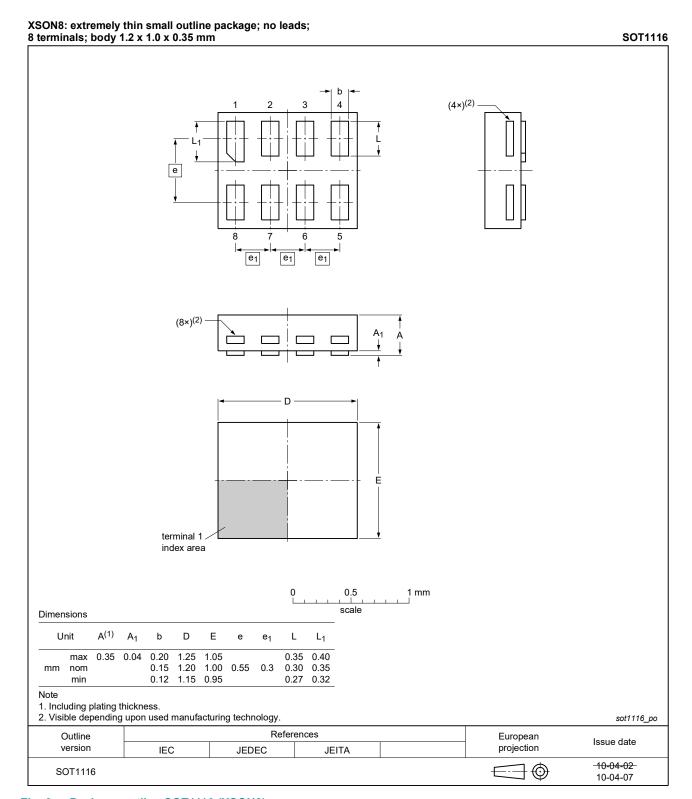


Fig. 6. Package outline SOT1116 (XSON8)

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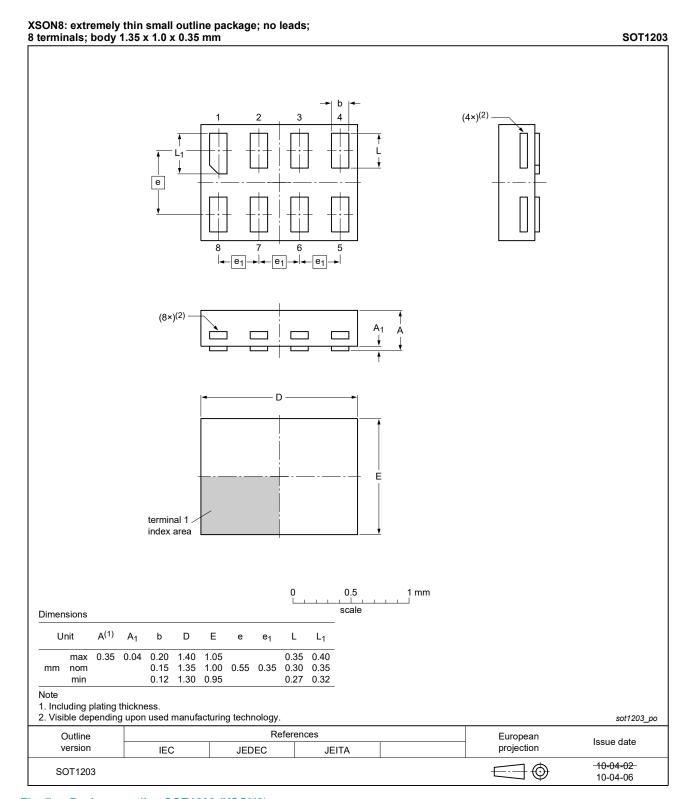


Fig. 7. Package outline SOT1203 (XSON8)

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## 13. Abbreviations

#### **Table 11. Abbreviations**

Acronym	Description
ANSI	American National Standards Institute
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
ESDA	ElectroStatic Discharge Association
НВМ	Human Body Model
JEDEC	Joint Electron Device Engineering Council

# 14. Revision history

### Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP1G885 v.13	20240812	Product data sheet	-	74AUP1G885 v.12
Modifications:	Type number	r 74AUP1G885GF (SOT10	089/XSON8) remo	ved.
74AUP1G885 v.12	20230728	Product data sheet	-	74AUP1G885 v.11
Modifications:	Section 2: E	SD specification updated a	ccording to the lat	est JEDEC standard.
74AUP1G885 v.11	20190722	Product data sheet	-	74AUP1G885 v.10
Modifications:	Type numbe	r 74AUP1G885GM (SOT9	02-2) removed.	
	<ul> <li>Layout of <u>Ta</u></li> </ul>	ble 8 Dynamic characterist	ics updated.	
74AUP1G885 v.10	20190314	Product data sheet	-	74AUP1G885 v.9
74AUP1G885 v.9	<ul><li>Type numbe</li><li>Package out</li></ul>	nave been adapted to the ner 74AUP1G885GD (SOT99tline drawing SOT765-1 (Vtline drawing SOT902-2 (Xtline drawing SOT902-2)	96-2) removed. SSOP8) updated.	e where appropriate. 74AUP1G885 v.8
Modifications:	For type nur	nber 74AUP1G885GD XS0	DN8U has change	d to XSON8.
74AUP1G885 v.8	20120608	Product data sheet	-	74AUP1G885 v.7
74AUP1G885 v.7	20111129	Product data sheet	-	74AUP1G885 v.6
74AUP1G885 v.6	20101021	Product data sheet	-	74AUP1G885 v.5
74AUP1G885 v.5	20090626	Product data sheet	-	74AUP1G885 v.4
74AUP1G885 v.4	20090401	Product data sheet	-	74AUP1G885 v.3
74AUP1G885 v.3	20080328	Product data sheet	-	74AUP1G885 v.2
74AUP1G885 v.2	20070710	Product data sheet	-	74AUP1G885 v.1
74AUP1G885 v.1	20061201	Product data sheet	-	-

### Low-power dual function gate

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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