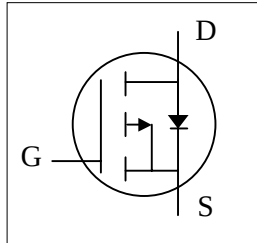




- ▼ Lower On-resistance
- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic

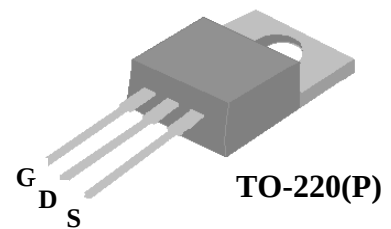
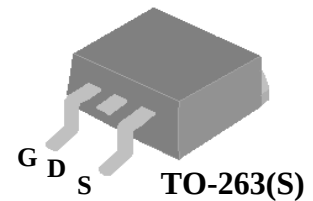


|              |              |
|--------------|--------------|
| $BV_{DS}$    | -30V         |
| $R_{DS(ON)}$ | 14m $\Omega$ |
| $I_D$        | -50A         |

## Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-263 package is widely preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters. The through-hole version (AP4407GP) are available for low-profile applications.



## Absolute Maximum Ratings

| Symbol                 | Parameter                                | Rating     | Units          |
|------------------------|------------------------------------------|------------|----------------|
| $V_{DS}$               | Drain-Source Voltage                     | -30        | V              |
| $V_{GS}$               | Gate-Source Voltage                      | +25        | V              |
| $I_D@T_C=25^{\circ}C$  | Continuous Drain Current, $V_{GS}$ @ 10V | -50        | A              |
| $I_D@T_C=100^{\circ}C$ | Continuous Drain Current, $V_{GS}$ @ 10V | -32        | A              |
| $I_{DM}$               | Pulsed Drain Current <sup>1</sup>        | 180        | A              |
| $P_D@T_C=25^{\circ}C$  | Total Power Dissipation                  | 54         | W              |
|                        | Linear Derating Factor                   | 0.4        | W/ $^{\circ}C$ |
| $T_{STG}$              | Storage Temperature Range                | -55 to 150 | $^{\circ}C$    |
| $T_J$                  | Operating Junction Temperature Range     | -55 to 150 | $^{\circ}C$    |

## Thermal Data

| Symbol | Parameter                                    | Value | Unit          |
|--------|----------------------------------------------|-------|---------------|
| Rthj-c | Maximum Thermal Resistance, Junction-case    | 2.3   | $^{\circ}C/W$ |
| Rthj-a | Maximum Thermal Resistance, Junction-ambient | 62    | $^{\circ}C/W$ |



## AP4407GS/P

### Electrical Characteristics@T<sub>j</sub>=25°C(unless otherwise specified)

| Symbol                              | Parameter                                            | Test Conditions                                           | Min. | Typ.  | Max. | Units |
|-------------------------------------|------------------------------------------------------|-----------------------------------------------------------|------|-------|------|-------|
| BV <sub>DSS</sub>                   | Drain-Source Breakdown Voltage                       | V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA               | -30  | -     | -    | V     |
| ΔBV <sub>DSS</sub> /ΔT <sub>j</sub> | Breakdown Voltage Temperature Coefficient            | Reference to 25°C, I <sub>D</sub> =-1mA                   | -    | -0.01 | -    | V/°C  |
| R <sub>DS(ON)</sub>                 | Static Drain-Source On-Resistance <sup>2</sup>       | V <sub>GS</sub> =-10V, I <sub>D</sub> =-24A               | -    | -     | 14   | mΩ    |
|                                     |                                                      | V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-16A              | -    | -     | 23   | mΩ    |
| V <sub>GS(th)</sub>                 | Gate Threshold Voltage                               | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA | -1   | -     | -3   | V     |
| g <sub>fs</sub>                     | Forward Transconductance                             | V <sub>DS</sub> =-10V, I <sub>D</sub> =-24A               | -    | 36    | -    | S     |
| I <sub>DSS</sub>                    | Drain-Source Leakage Current                         | V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V                | -    | -     | -1   | uA    |
|                                     | Drain-Source Leakage Current (T <sub>j</sub> =150°C) | V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V                | -    | -     | -25  | uA    |
| I <sub>GSS</sub>                    | Gate-Source Leakage                                  | V <sub>GS</sub> = ±25V                                    | -    | -     | ±100 | nA    |
| Q <sub>g</sub>                      | Total Gate Charge <sup>2</sup>                       | I <sub>D</sub> =-24A                                      | -    | 35    | 60   | nC    |
| Q <sub>gs</sub>                     | Gate-Source Charge                                   | V <sub>DS</sub> =-24V                                     | -    | 5     | -    | nC    |
| Q <sub>gd</sub>                     | Gate-Drain ("Miller") Charge                         | V <sub>GS</sub> =-4.5V                                    | -    | 26    | -    | nC    |
| t <sub>d(on)</sub>                  | Turn-on Delay Time <sup>2</sup>                      | V <sub>DS</sub> =-15V                                     | -    | 11    | -    | ns    |
| t <sub>r</sub>                      | Rise Time                                            | I <sub>D</sub> =-24A                                      | -    | 64    | -    | ns    |
| t <sub>d(off)</sub>                 | Turn-off Delay Time                                  | R <sub>G</sub> =3.3Ω, V <sub>GS</sub> =-10V               | -    | 63    | -    | ns    |
| t <sub>f</sub>                      | Fall Time                                            | R <sub>D</sub> =0.63Ω                                     | -    | 100   | -    | ns    |
| C <sub>iss</sub>                    | Input Capacitance                                    | V <sub>GS</sub> =0V                                       | -    | 2120  | 3390 | pF    |
| C <sub>oss</sub>                    | Output Capacitance                                   | V <sub>DS</sub> =-25V                                     | -    | 630   | -    | pF    |
| C <sub>rss</sub>                    | Reverse Transfer Capacitance                         | f=1.0MHz                                                  | -    | 550   | -    | pF    |

### Source-Drain Diode

| Symbol          | Parameter                          | Test Conditions                            | Min. | Typ. | Max. | Units |
|-----------------|------------------------------------|--------------------------------------------|------|------|------|-------|
| V <sub>SD</sub> | Forward On Voltage <sup>2</sup>    | I <sub>S</sub> =-24A, V <sub>GS</sub> =0V  | -    | -    | -1.2 | V     |
| t <sub>rr</sub> | Reverse Recovery Time <sup>2</sup> | I <sub>S</sub> =-24A, V <sub>GS</sub> =0V, | -    | 39   | -    | ns    |
| Q <sub>rr</sub> | Reverse Recovery Charge            | dI/dt=-100A/μs                             | -    | 38   | -    | nC    |

#### Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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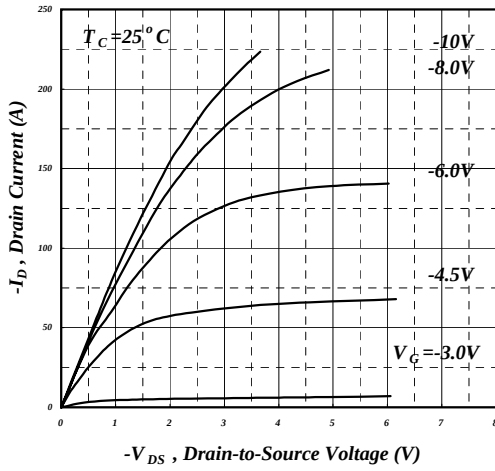


Fig 1. Typical Output Characteristics

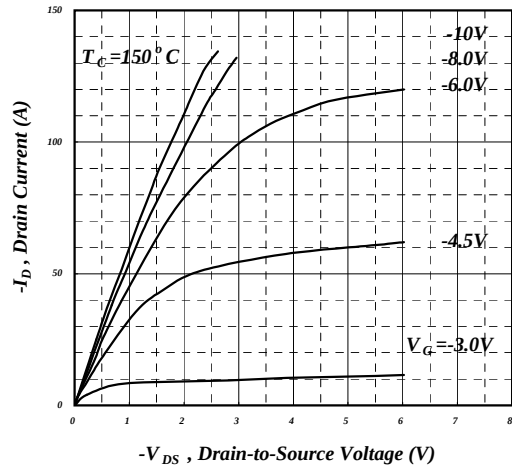


Fig 2. Typical Output Characteristics

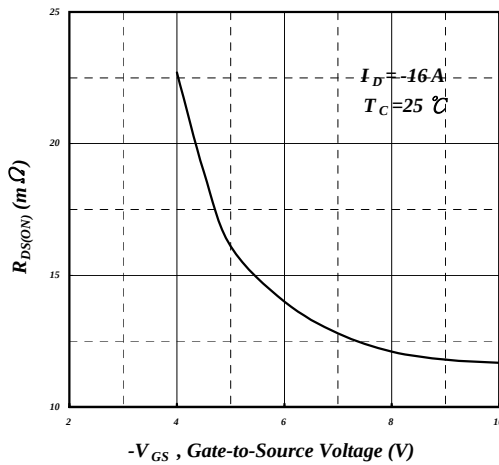


Fig 3. On-Resistance v.s. Gate Voltage

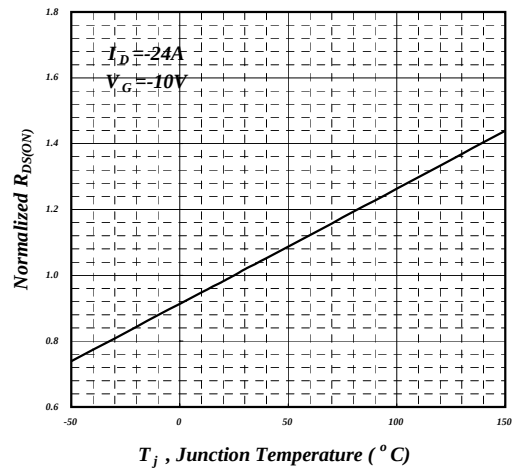


Fig 4. Normalized On-Resistance v.s. Junction Temperature

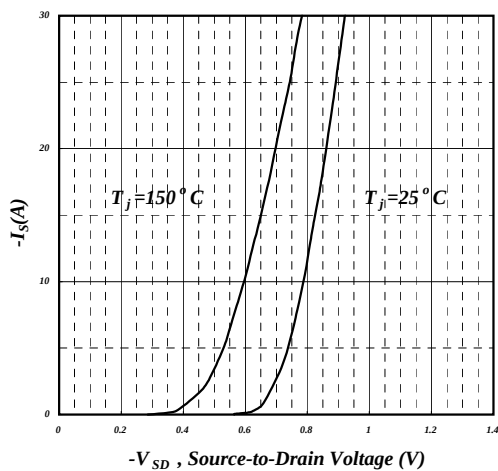


Fig 5. Forward Characteristic of Reverse Diode

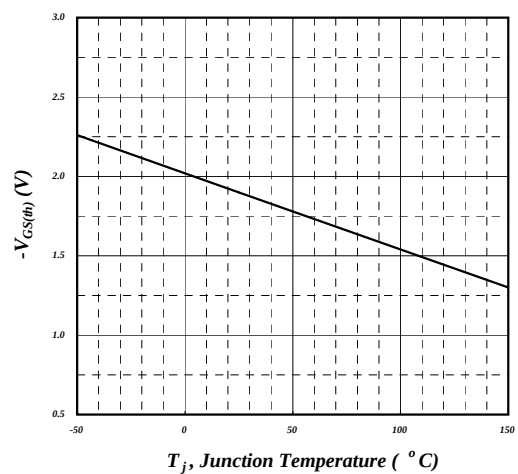


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

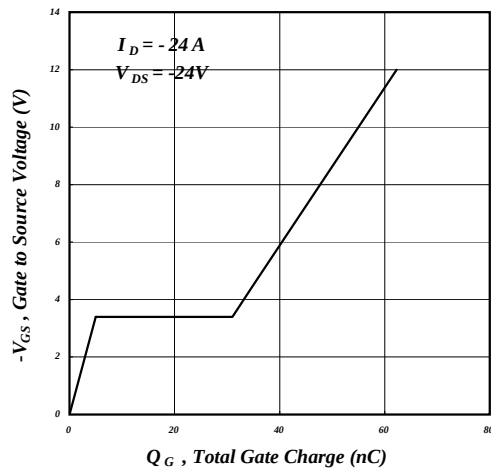


Fig 7. Gate Charge Characteristics

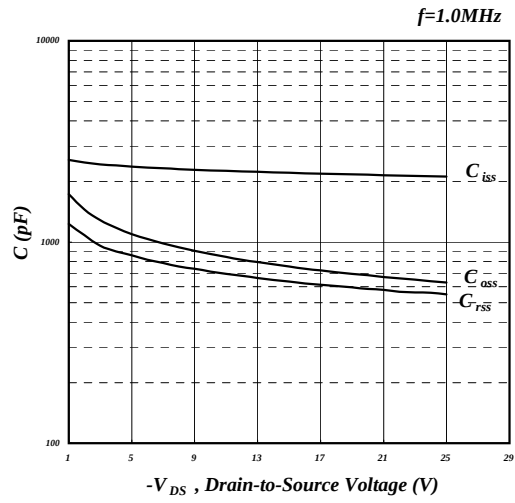


Fig 8. Typical Capacitance Characteristics

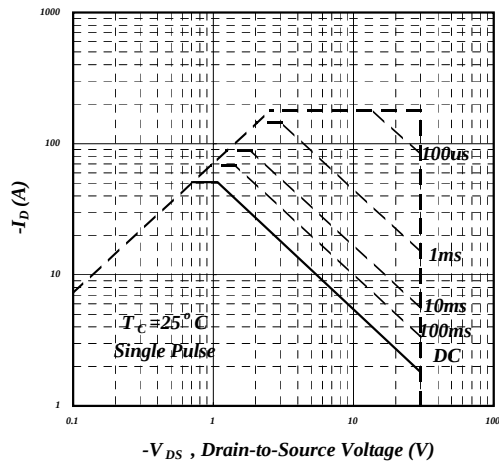


Fig 9. Maximum Safe Operating Area

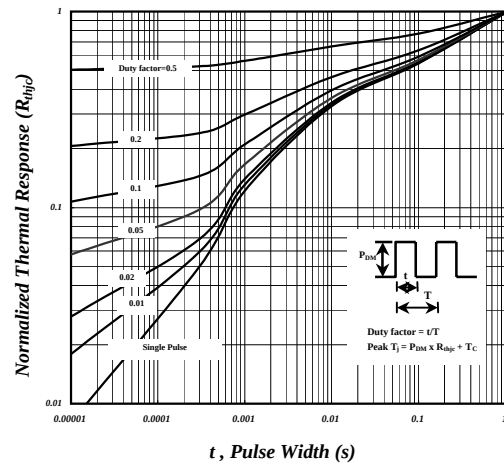


Fig 10. Effective Transient Thermal Impedance

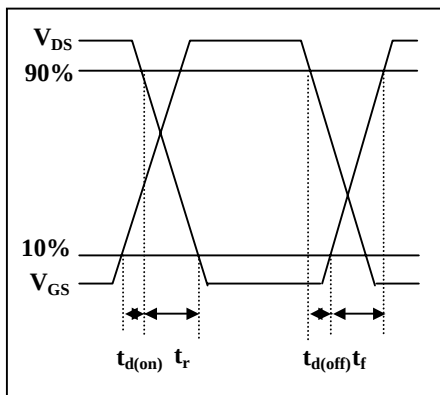


Fig 11. Switching Time Waveform

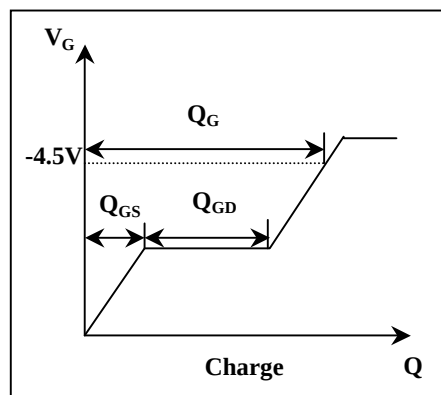
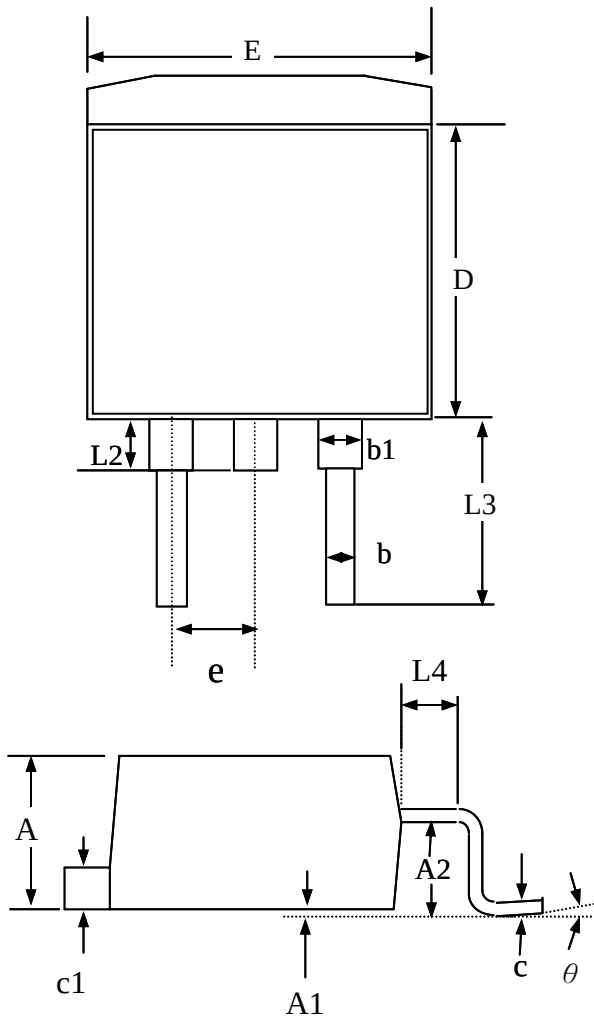


Fig 12. Gate Charge Waveform



## Package Outline : TO-263

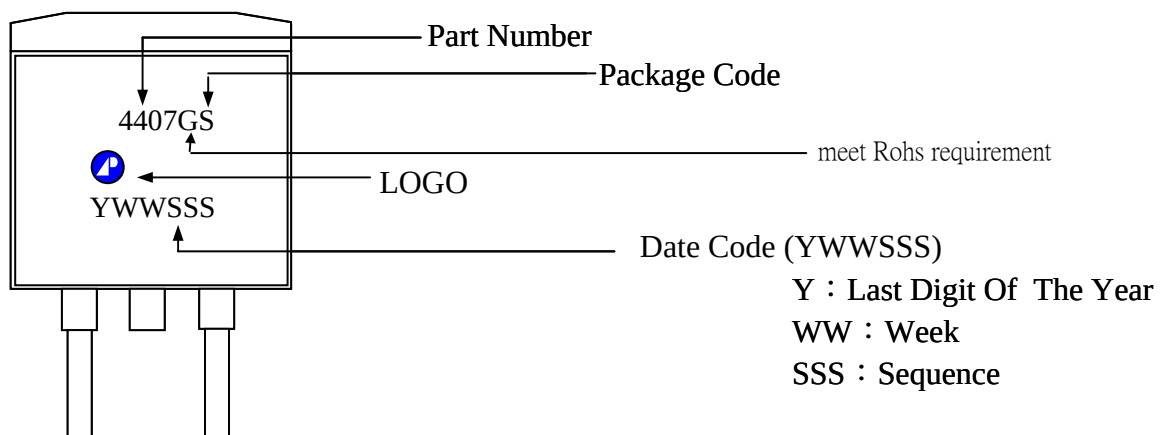


| SYMBOLS | Millimeters |       |       |
|---------|-------------|-------|-------|
|         | MIN         | NOM   | MAX   |
| A       | 4.25        | 4.75  | 5.20  |
| A1      | 0.00        | 0.15  | 0.30  |
| A2      | 2.20        | 2.45  | 2.70  |
| b       | 0.70        | 0.90  | 1.10  |
| b1      | 1.07        | 1.27  | 1.47  |
| c       | 0.30        | 0.45  | 0.60  |
| c1      | 1.15        | 1.30  | 1.45  |
| D       | 8.30        | 8.90  | 9.40  |
| E       | 9.70        | 10.10 | 10.50 |
| e       | 2.04        | 2.54  | 3.04  |
| L2      | -----       | 1.50  | ----- |
| L3      | 4.50        | 4.90  | 5.30  |
| L4      | -----       | 1.50  | ----  |

1.All Dimensions Are in Millimeters.

2.Dimension Does Not Include Mold Protrusions.

## Part Marking Information & Packing : TO-263





Technical drawing of a probe with dimensions and a table of values.

The drawing shows a top view and a side view of the probe. The top view includes dimensions E, E1, L1, L5, L4, D, D1, and a circular feature with diameter  $\phi$ . The side view includes dimensions A, c1, L, and c. The probe has a main body with a rectangular cutout and a vertical shaft with a conical tip.

| SYMBOLS | Millimeters |       |       |
|---------|-------------|-------|-------|
|         | MIN         | NOM   | MAX   |
| A       | 4.40        | 4.60  | 4.80  |
| b       | 0.76        | 0.88  | 1.00  |
| D       | 8.60        | 8.80  | 9.00  |
| c       | 0.36        | 0.43  | 0.50  |
| E       | 9.80        | 10.10 | 10.40 |
| L4      | 14.70       | 15.00 | 15.30 |
| L5      | 6.20        | 6.40  | 6.60  |
| D1      | 5.10 REF.   |       |       |
| c1      | 1.25        | 1.35  | 1.45  |
| b1      | 1.17        | 1.32  | 1.47  |
| L       | 13.25       | 13.75 | 14.25 |
| e       | 2.54 REF.   |       |       |
| L1      | 2.60        | 2.75  | 2.89  |
| $\phi$  | 3.71        | 3.84  | 3.96  |
| E1      | 7.4 REF.    |       |       |

1.All Dimensions Are in Millimeters.  
2.Dimension Does Not Include Mold Protrusions.

Diagram illustrating the marking layout for a component, specifically a 4407GP package, showing the arrangement of markings and their corresponding labels:

- Part Number:** 4407GP
- Package Code:** (Marking above the Part Number)
- Date Code (YWWSSS):** (Marking below the Part Number)
  - Y : Last Digit Of The Year
  - WW : Week
  - SSS : Sequence
- LOGO:** (Marking to the left of the Date Code)

Additional notes from the diagram:

- meet Rohs requirement