

HSL285

Silicon Schottky Barrier Diode for Detector

REJ03G0527-0100

Rev.1.00

Feb 09, 2005

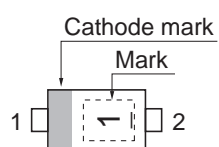
Features

- Low forward voltage, Low capacitance and High detection sensitivity.
- Extremely small Flat Lead Package (EFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Renesas Code	Previous Code
HSL285	1	PXSF0002ZA-A	EFP

Pin Arrangement



1. Cathode
2. Anode

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V_R	2	V
Average rectified current	I_O	5	mA
Junction temperature	T_j	125	°C
Storage temperature	T_{stg}	-55 to +125	°C

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_{F1}	—	—	0.15	V	$I_F = 0.1 \text{ mA}$
	V_{F2}	—	—	0.27		$I_F = 1 \text{ mA}$
Capacitance	C	—	0.3	—	pF	$V_R = 1 \text{ V}$, $f = 1 \text{ MHz}$
ESD-Capability *1	—	10	—	—	V	C = 200 pF, $R_L = 0 \Omega$, Both forward and reverse direction 1 pulse.

Notes: 1. Failure criterion ; $I_R \geq 100 \mu\text{A}$ at $V_R = 0.5 \text{ V}$

2. Please do not use the soldering iron due to avoid high stress to the EFP package.

3. The material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

Main Characteristic

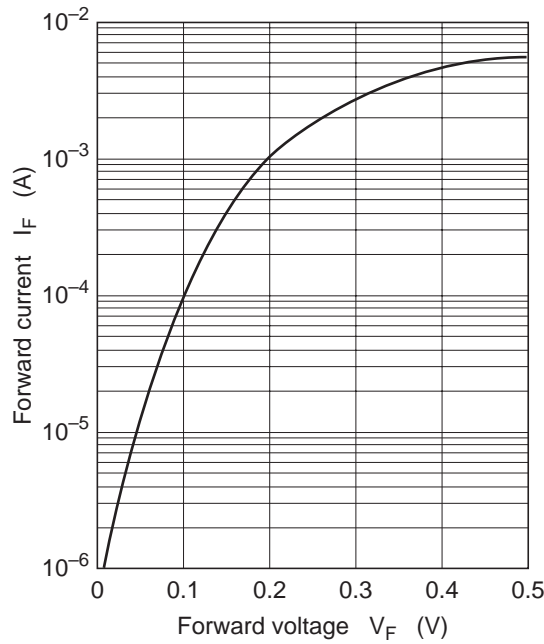


Fig.1 Forward current vs. Forward voltage

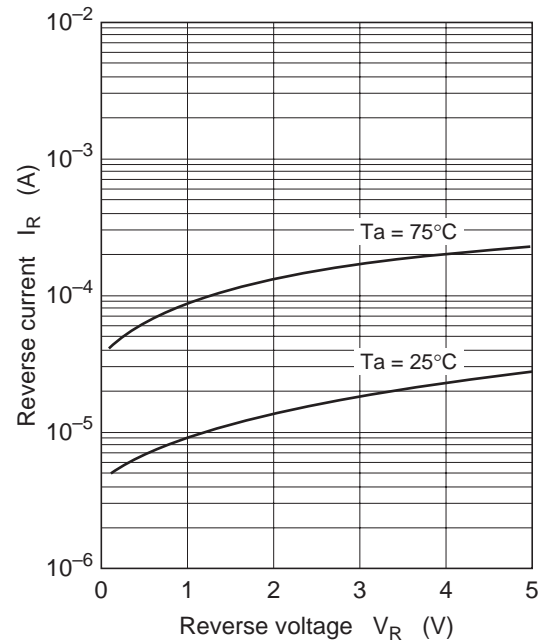


Fig.2 Reverse current vs. Reverse voltage

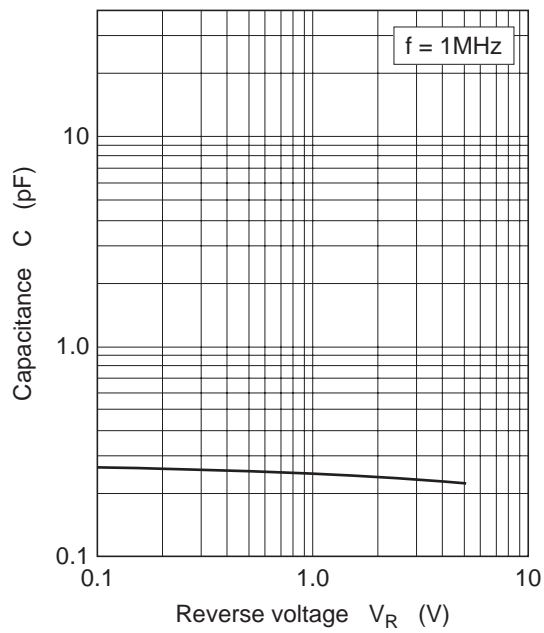
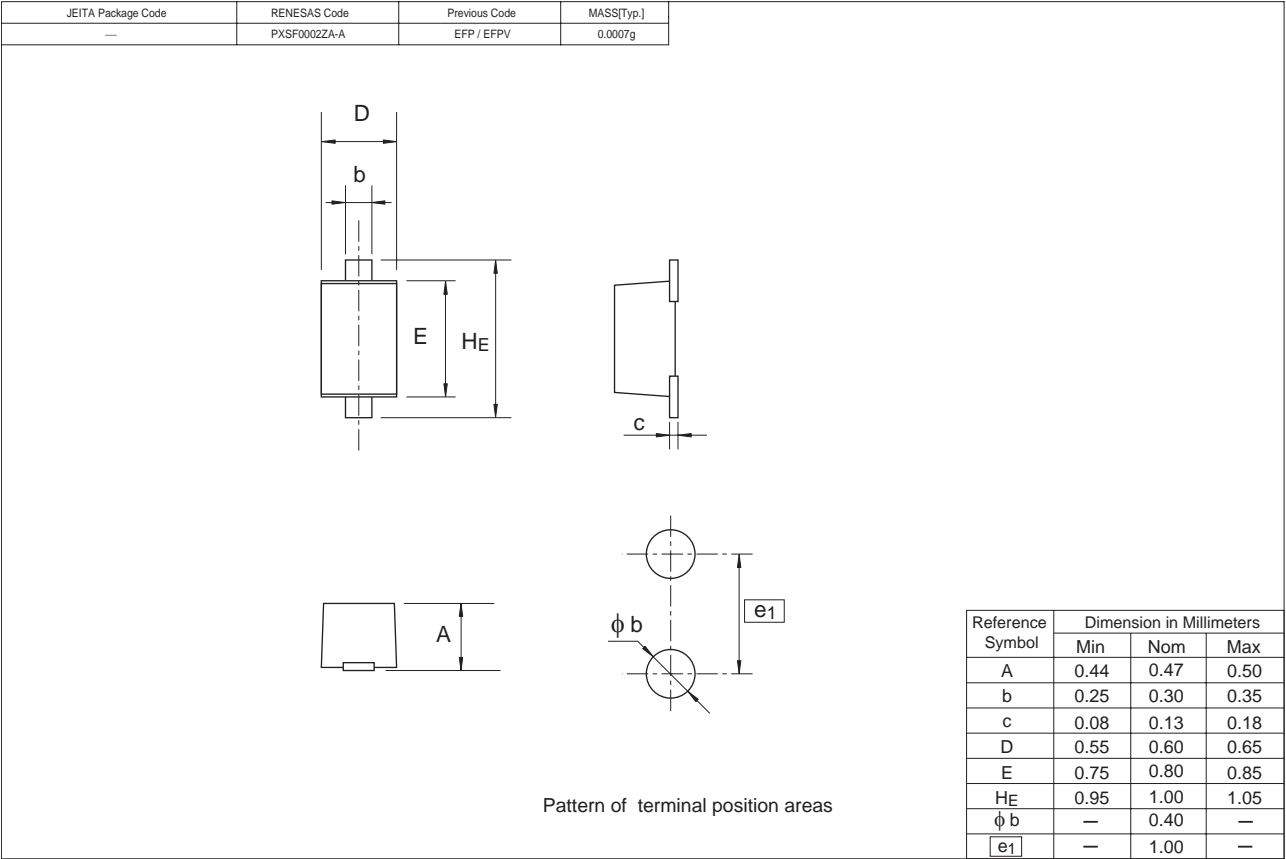


Fig.3 Capacitance vs. Reverse voltage

Package Dimensions



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