

# TD62308AP, TD62308AF

## 4ch Low Input Active High-Current Darlington Sink Driver

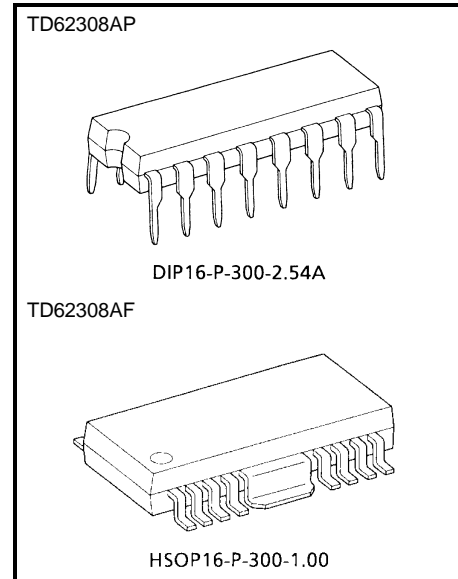
The TD62308AP/AF is a non-inverting transistor array which is comprised of four NPN darlington output stages and PNP input stages.

This device is low-level input active driver and is suitable for operation with 5-V TTL, 5-V CMOS and 5-V Microprocessor which have sink current output drivers.

Application include relay, hammer, lamp and stepping motor drivers.

### Features

- Output current (single output): 1.5 A (max)
- High sustaining voltage output: 50 V (min)
- Output clamp diodes
- Input compatible with TTL and 5 V CMOS
- Low level active inputs
- Standard supply voltage
- Two VCC terminals VCC1, VCC2 (separated)
- GND and SUB terminal = Heat sink
- Package type-AP: DIP-16 pin
- Package type-AF: HSOP-16 pin



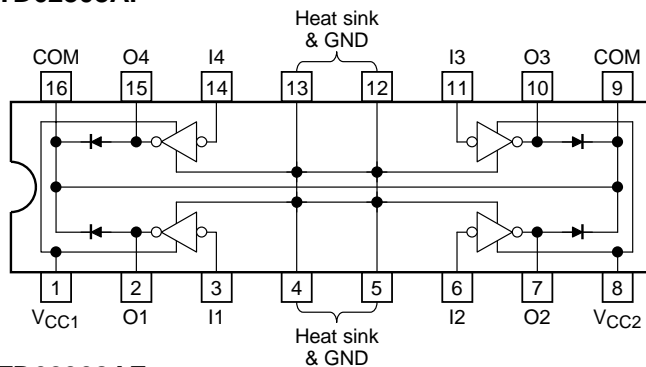
Weight

DIP16-P-300-2.54A: 1.11 g (typ.)

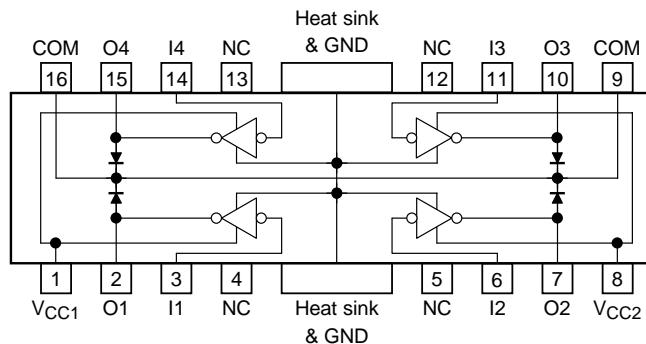
HSOP16-P-300-1.00: 0.50 g (typ.)

### Pin Assignment (top view)

#### TD62308AP



#### TD62308AF



[illegible]

Characteristics		Symbol	Rating	Unit
Supply voltage		$V_{CC}$	-0.5 to 10	V
Output sustaining voltage		$V_{CE(SUS)}$	-0.5 to 50	V
Output current		$I_{OUT}$	1.5	A/ch
Input current		$I_{IN}$	-10	mA
Input voltage		$V_{IN}$	-0.5 to 30	V
Clamp diode reverse voltage		$V_R$	50	V
Clamp diode forward current		$I_F$	1.5	A
Power dissipation	AP	$P_D$	1.47/2.7 (Note 1)	W
	AF		0.9/1.4 (Note 2)	
Operating temperature		$T_{opr}$	-40 to 85	°C
Storage temperature		$T_{stg}$	-55 to 150	°C

Note 2: On glass epoxy PCB (60 × 30 × 1.6 mm Cu 30%)

## Recommended Operating Conditions (Ta = -40 to 85°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Supply voltage		V <sub>CC</sub>	—	4.5	—	5.5	V
Output sustaining voltage		V <sub>CE (SUS)</sub>	—	0	—	50	V
Output current	AP AF	I <sub>OUT</sub>	DC1 circuit, Ta = 25°C	0	—	1250	mA/ch
			T <sub>pw</sub> = 25 ms 4 circuits Ta = 85°C T <sub>j</sub> = 120°C	Duty = 10%	0	—	1250
				Duty = 50%	0	—	700
				Duty = 10%	0	—	1250
				Duty = 50%	0	—	390
Input voltage		V <sub>IN</sub>	—	0	—	25	V
	Output ON	V <sub>IN (ON)</sub>	—	0	—	V <sub>CC</sub> - 3.6	V
	Output OFF	V <sub>IN (OFF)</sub>	—	V <sub>CC</sub> - 1.0	—	V <sub>CC</sub>	
Clamp diode reverse voltage		V <sub>R</sub>	—	—	—	50	V
Clamp diode forward current		I <sub>F</sub>	—	—	—	1.25	A
Power dissipation	AP	P <sub>D</sub>	Ta = 85°C (Note 1)	—	—	1.4	W
	AF		Ta = 85°C (Note 2)	—	—	0.7	

Note 1: On glass epoxy PCB (50 × 50 × 1.6 mm Cu 50%)

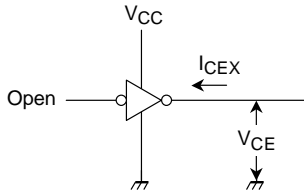
Note 2: On glass epoxy PCB (60 × 30 × 1.6 mm Cu 30%)

## Electrical Characteristics (Ta = 25°C)

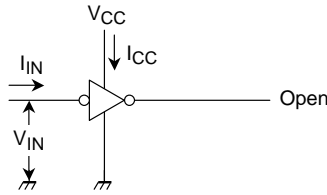
Characteristics		Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Output leakage current		I <sub>CEX</sub>	1	V <sub>CE</sub> = 50 V, Ta = 25°C	—	—	50	μA
				V <sub>CE</sub> = 50 V, Ta = 85°C	—	—	100	
Output saturation voltage		V <sub>CE (sat)</sub>	3	I <sub>OUT</sub> = 1.25 A	—	—	1.8	V
				I <sub>OUT</sub> = 0.75 A	—	—	1.3	
Input voltage	High level	V <sub>IH</sub>	—	—	V <sub>CC</sub> - 1.6	—	25	V
	Low level	V <sub>IL</sub>	—	—	—	—	V <sub>CC</sub> - 3.6	
Input current	High level	I <sub>IH</sub>	—	—	—	—	10	μA
	Low level	I <sub>IL</sub>	—	—	—	-0.05	-0.36	mA
Clamp diode reverse current		I <sub>R</sub>	4	V <sub>R</sub> = 50 V, Ta = 25°C	—	—	50	μA
Clamp diode forward voltage		V <sub>F</sub>	5	I <sub>F</sub> = 1.25 A	—	1.5	2.0	V
Supply current	Output ON	I <sub>CC (ON)</sub>	2	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0 V	—	8.5	12.5	mA/ch
	Output OFF	I <sub>CC (OFF)</sub>		V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = V <sub>CC</sub>	—	—	1.0	μA
Turn-ON delay		t <sub>ON</sub>	6	C <sub>L</sub> = 15 pF, V <sub>OUT</sub> = 50 V, R <sub>L</sub> = 40 Ω	—	0.2	—	μs
Turn-OFF delay		t <sub>OFF</sub>	6	C <sub>L</sub> = 15 pF, V <sub>OUT</sub> = 35 V, R <sub>L</sub> = 40 Ω	—	5.0	—	μs

## Test Circuit

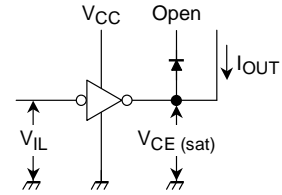
### 1. $I_{CEX}$



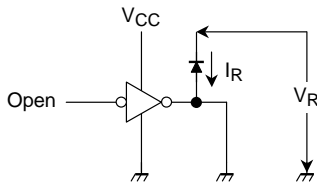
### 2. $I_{CC}$



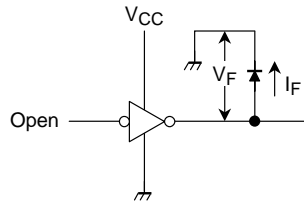
### 3. $V_{CE(sat)}$



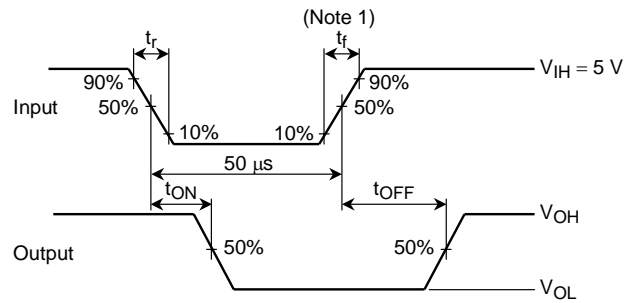
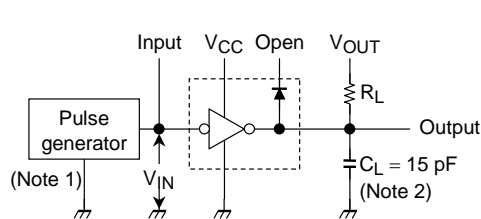
### 4. $I_R$



### 5. $V_F$

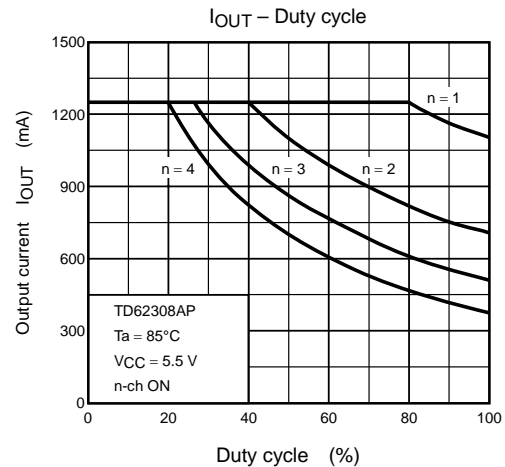
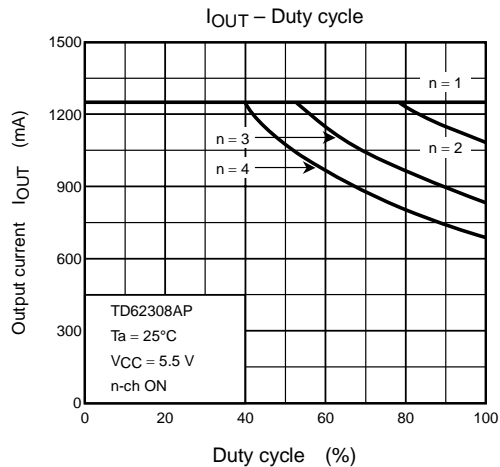
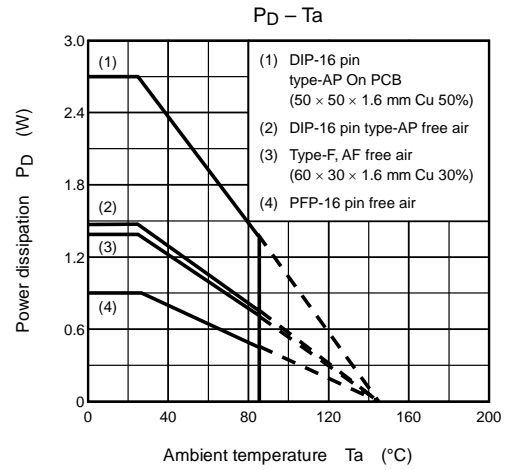
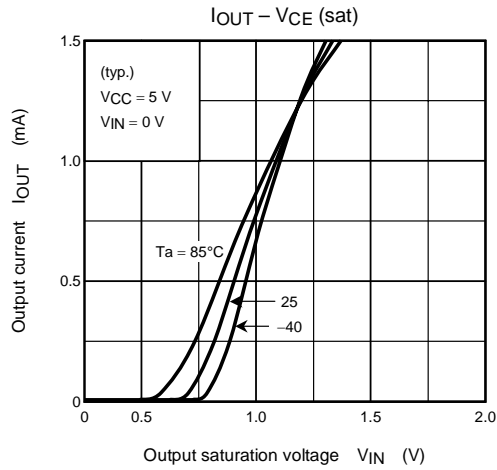
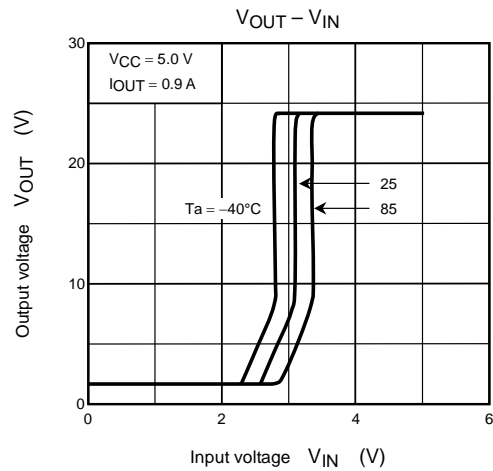
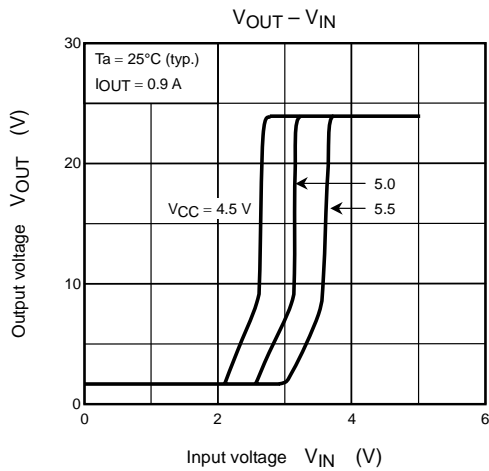


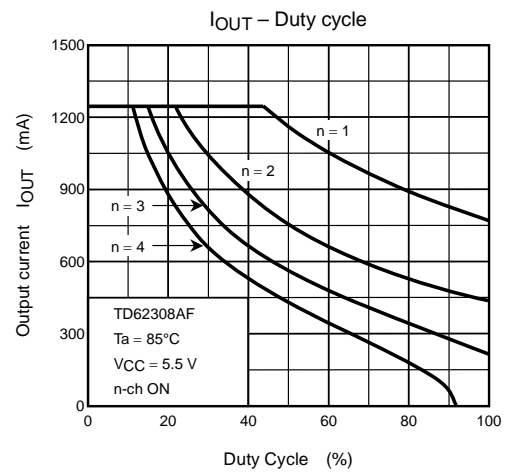
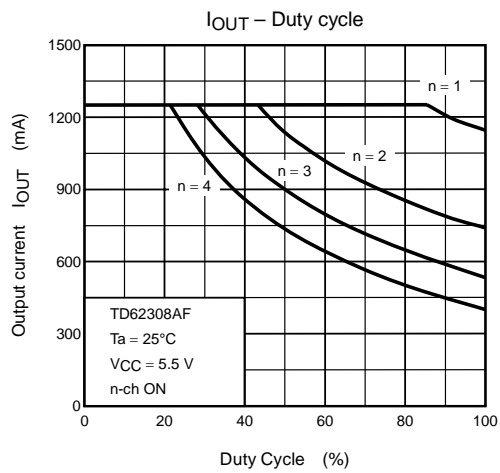
### 6. $t_{ON}$ , $t_{OFF}$



Note 1: Pulse Width 50  $\mu$ s, Duty Cycle 10%  
Output Impedance 50  $\Omega$ ,  $t_r \leq 5$  ns,  $t_f \leq 10$  ns

Note 2:  $C_L$  includes probe and jig capacitance

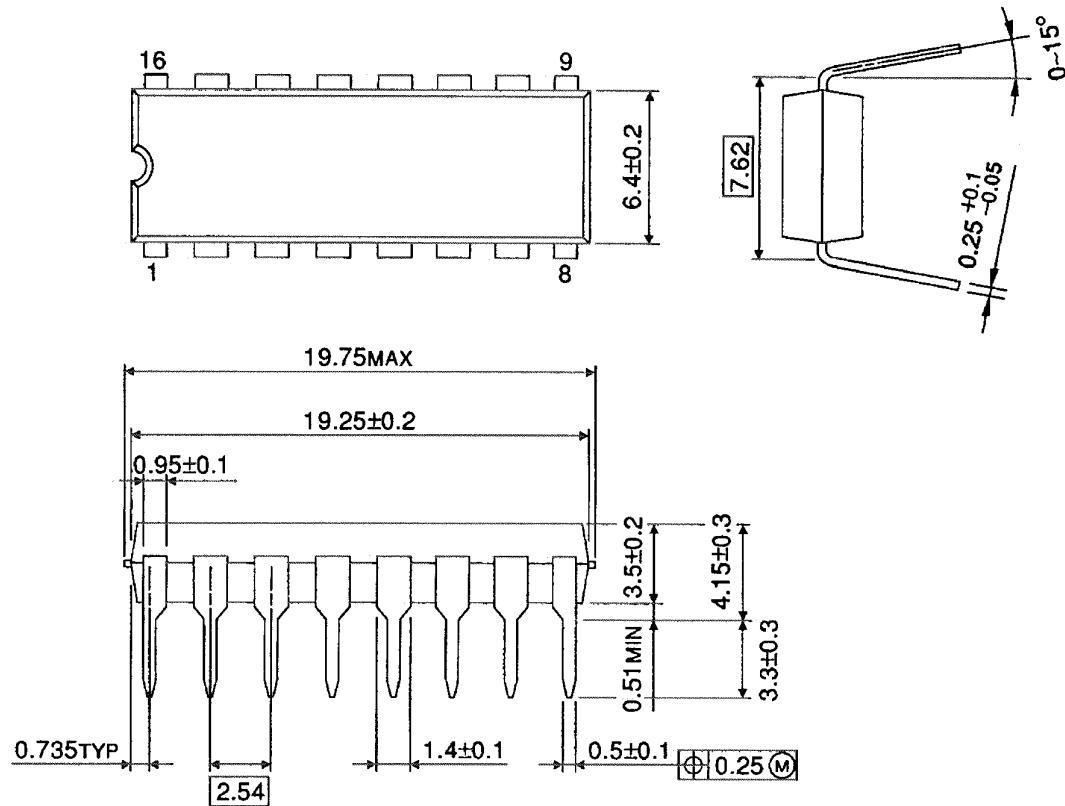




## Package Dimensions

DIP16-P-300-2.54A

Unit : mm

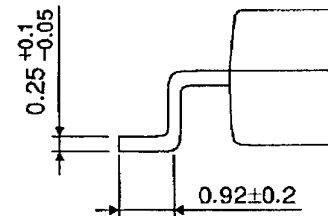
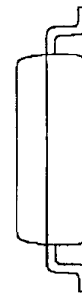
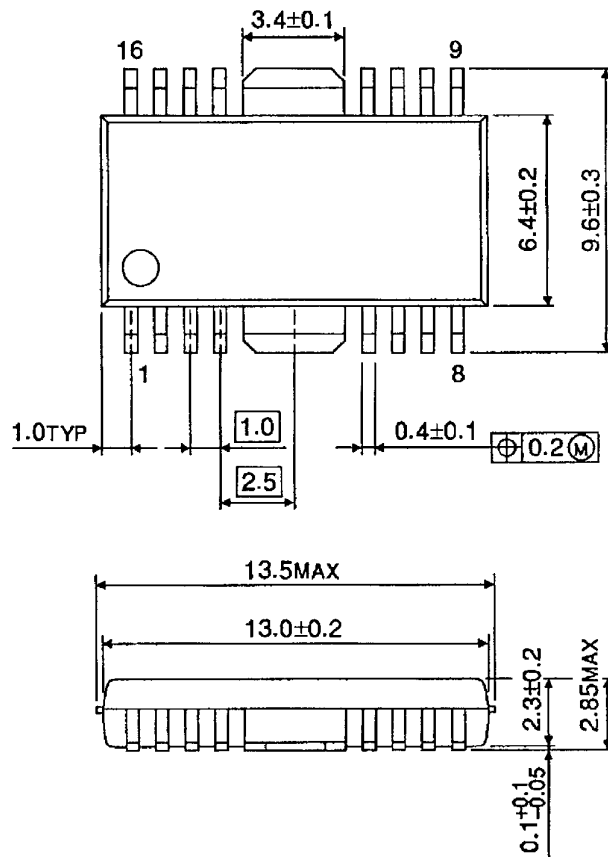


Weight: 1.11 g (typ.)

## Package Dimensions

HSOP16-P-300-1.00

Unit : mm



Weight: 0.50 g (typ.)



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