

SOT-26

Pin Definition:

- 1. Drain 6. Drain
- 2. Drain 5. Drain
- 3. Gate 4. Source

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$(mΩ)	I_D (A)
30	60 @ $V_{GS} = 10V$	4.5
	85 @ $V_{GS} = 4.5V$	3.8

Features

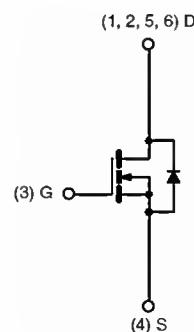
- Advance Trench Process Technology
- High Density Cell Design for Ultra Low On-resistance

Application

- Load Switch
- PA Switch

Ordering Information

Part No.	Package	Packing
TSM3454CX6 RF	SOT-26	3Kpcs / 7" Reel

Block Diagram

N-Channel MOSFET
Absolute Maximum Rating ($T_a = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	4.5	A
Pulsed Drain Current	I_{DM}	± 20	A
Continuous Source Current (Diode Conduction) ^{a,b}	I_S	1.7	A
Maximum Power Dissipation	P_D	2.0	W
		1.3	
Operating Junction Temperature	T_J	+150	$^\circ\text{C}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Thermal Performance

Parameter	Symbol	Limit	Unit
Junction to Case Thermal Resistance	$R\theta_{JF}$	62.5	$^\circ\text{C/W}$
Junction to Ambient Thermal Resistance (PCB mounted)	$R\theta_{JA}$	110	$^\circ\text{C/W}$

Notes:

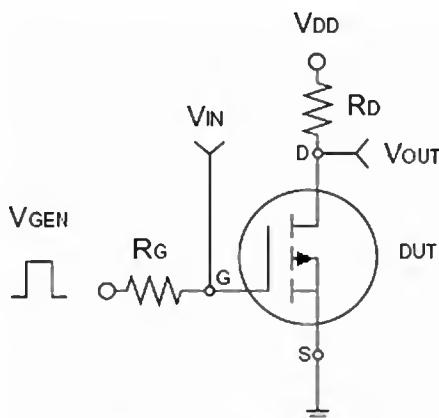
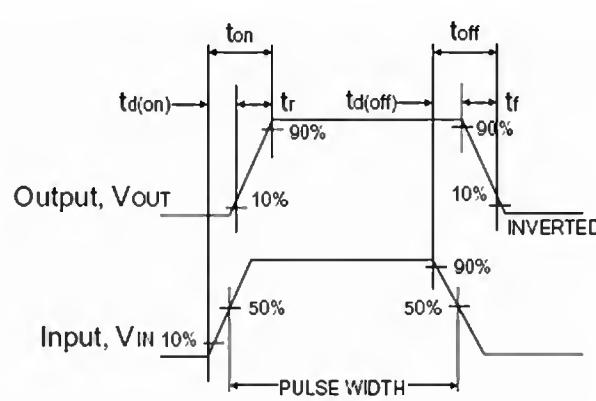
- a. Pulse width limited by the Maximum junction temperature
- b. Surface Mounted on FR4 Board, $t \leq 5$ sec.

Electrical Specifications ($T_a = 25^\circ\text{C}$ unless otherwise noted)

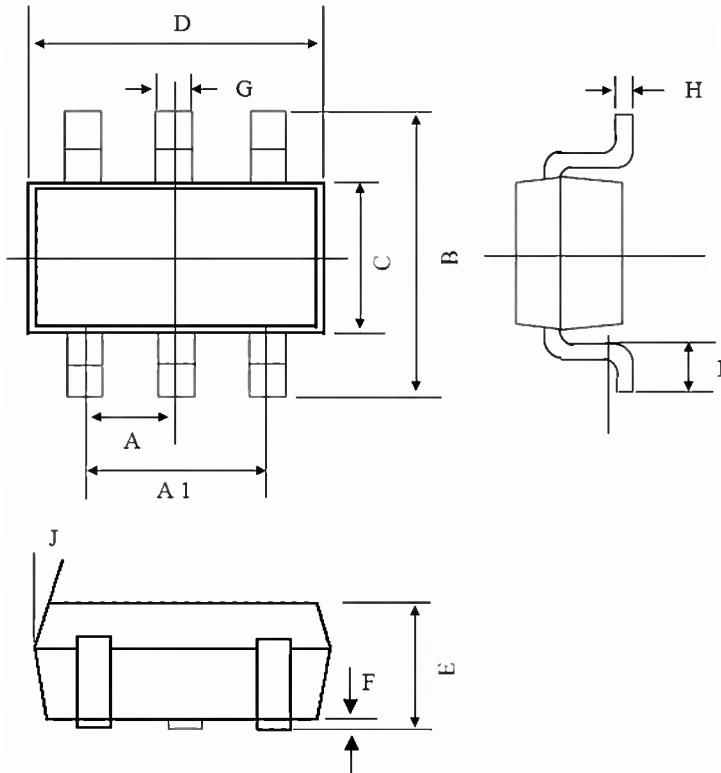
Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}$, $I_D = 250\mu\text{A}$	BV_{DSS}	30	--	--	V
Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250\mu\text{A}$	$V_{GS(\text{TH})}$	1	--	3	V
Gate Body Leakage	$V_{GS} = \pm 20\text{V}$, $V_{DS} = 0\text{V}$	I_{GSS}	--	--	± 100	nA
Zero Gate Voltage Drain Current	$V_{DS} = 30\text{V}$, $V_{GS} = 0\text{V}$	I_{DSS}	--	--	1.0	μA
On-State Drain Current	$V_{DS} \geq 5\text{V}$, $V_{GS} = 10\text{V}$	$I_{D(\text{ON})}$	15	--	--	A
Drain-Source On-State Resistance	$V_{GS} = 10\text{V}$, $I_D = 4.5\text{A}$	$R_{DS(\text{ON})}$	--	48	60	$\text{m}\Omega$
	$V_{GS} = 4.5\text{V}$, $I_D = 3.8\text{A}$		--	70	85	
Forward Transconductance	$V_{DS} = 10\text{V}$, $I_D = 4.5\text{A}$	g_{fs}	--	10	--	S
Diode Forward Voltage	$I_S = 1.7\text{A}$, $V_{GS} = 0\text{V}$	V_{SD}	--	--	1.2	V
Dynamic^b						
Total Gate Charge	$V_{DS} = 15\text{V}$, $I_D = 4.5\text{A}$, $V_{GS} = 10\text{V}$	Q_g	--	4.2	7	nC
Gate-Source Charge		Q_{gs}	--	1.9	--	
Gate-Drain Charge		Q_{gd}	--	1.35	--	
Input Capacitance	$V_{DS} = 15\text{V}$, $V_{GS} = 0\text{V}$, $f = 1.0\text{MHz}$	C_{iss}	--	555	--	pF
Output Capacitance		C_{oss}	--	120	--	
Reverse Transfer Capacitance		C_{rss}	--	60	--	
Switching^c						
Turn-On Delay Time	$V_{DD} = 15\text{V}$, $R_L = 15\Omega$, $I_D = 1\text{A}$, $V_{GEN} = 10\text{V}$, $R_G = 6\Omega$	$t_{d(\text{on})}$	--	9	20	nS
Turn-On Rise Time		t_r	--	7.5	18	
Turn-Off Delay Time		$t_{d(\text{off})}$	--	17	35	
Turn-Off Fall Time		t_f	--	5.2	12	

Notes:

- pulse test: $PW \leq 300\mu\text{S}$, duty cycle $\leq 2\%$
- For DESIGN AID ONLY, not subject to production testing.
- Switching time is essentially independent of operating temperature.

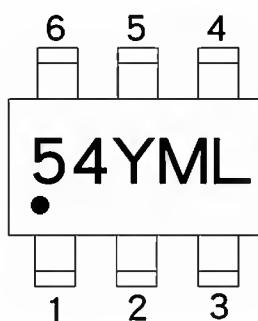

Switching Test Circuit

Switching Waveforms

SOT-26 Mechanical Drawing



DIM	SOT-26 DIMENSION			INCHES		
	MILLIMETERS					
	MIN	_TYP	MAX	MIN	_TYP	MAX
A	0.95	BSC		0.0374	BSC	
A1	1.9	BSC		0.0748	BSC	
B	2.60	2.80	3.00	0.1024	0.1102	0.1181
C	1.40	1.50	1.70	0.0551	0.0591	0.0669
D	2.80	2.90	3.10	0.1101	0.1142	0.1220
E	1.00	1.10	1.20	0.0394	0.0433	0.0472
F	0.00	--	0.10	0.00		0.0039
G	0.35	0.40	0.50	0.0138	0.0157	0.0197
H	0.10	0.15	0.20	0.0039	0.0059	0.0079
I	0.30	--	0.60	0.0118	--	0.0236
J	5°	--	10°	5°	--	10°

Marking Diagram



54 = Device Code
Y = Year Code
M = Month Code
 (A=Jan, B=Feb, C=Mar, D=Apl, E=May, F=Jun, G=Jul, H=Aug,
 I=Sep, J=Oct, K=Nov, L=Dec)
L = Lot Code

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