

# 74ACT11151 1-OF-8 DATA SELECTOR/MULTIPLEXER

SCAS067A – D3349, JULY 1989 – REVISED APRIL 1993

- Inputs Are TTL-Voltage Compatible
- 8-Line to 1-Line Multiplexers Can Perform as: Boolean Function Generators, Parallel-to-Serial Converters, Data Source Selectors
- Flow-Through Architecture Optimizes PCB Layout
- Center-Pin  $V_{CC}$  and GND Configurations Minimize High-Speed Switching Noise
- EPIC™ (Enhanced-Performance Implanted CMOS) 1- $\mu$ m Process
- 500-mA Typical Latch-Up Immunity at 125°C
- Package Options Include Plastic Small-Outline Packages and Standard Plastic 300-mil DIPs

## description

This monolithic data selector/multiplexer provides full binary decoding to select one-of-eight data sources. The strobe input ( $\bar{G}$ ) must be at a low logic level to enable the inputs. A high level at the strobe terminal forces the W output high and the Y output low.

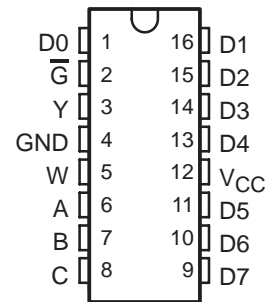
The 74ACT11151 is characterized for operation from – 40°C to 85°C.

FUNCTION TABLE

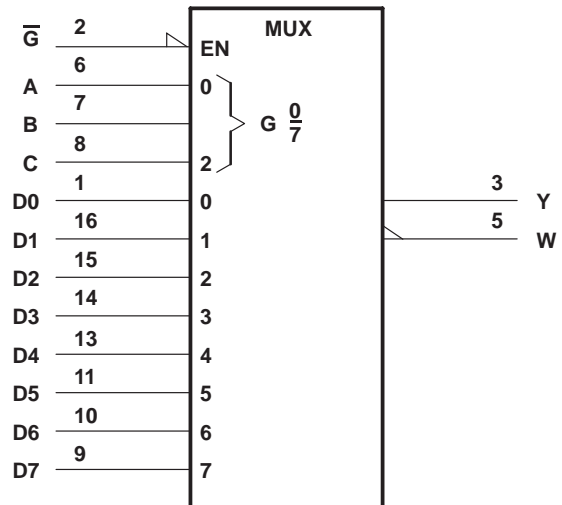
INPUTS			OUTPUTS	
SELECT			STROBE $\bar{G}$	Y      W
C	B	A		
X	X	X	H	L      H
L	L	L	L	D0 $\bar{D}0$
L	L	H	L	D1 $\bar{D}1$
L	H	L	L	D2 $\bar{D}2$
L	H	H	L	D3 $\bar{D}3$
H	L	L	L	D4 $\bar{D}4$
H	L	H	L	D5 $\bar{D}5$
H	H	L	L	D6 $\bar{D}6$
H	H	H	L	D7 $\bar{D}7$

H = high level, L = low level, X = irrelevant  
D0, D1, . . . D7 = the level of the respective D input

D OR N PACKAGE  
(TOP VIEW)



## logic symbol†



† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

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PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

**TEXAS  
INSTRUMENTS**

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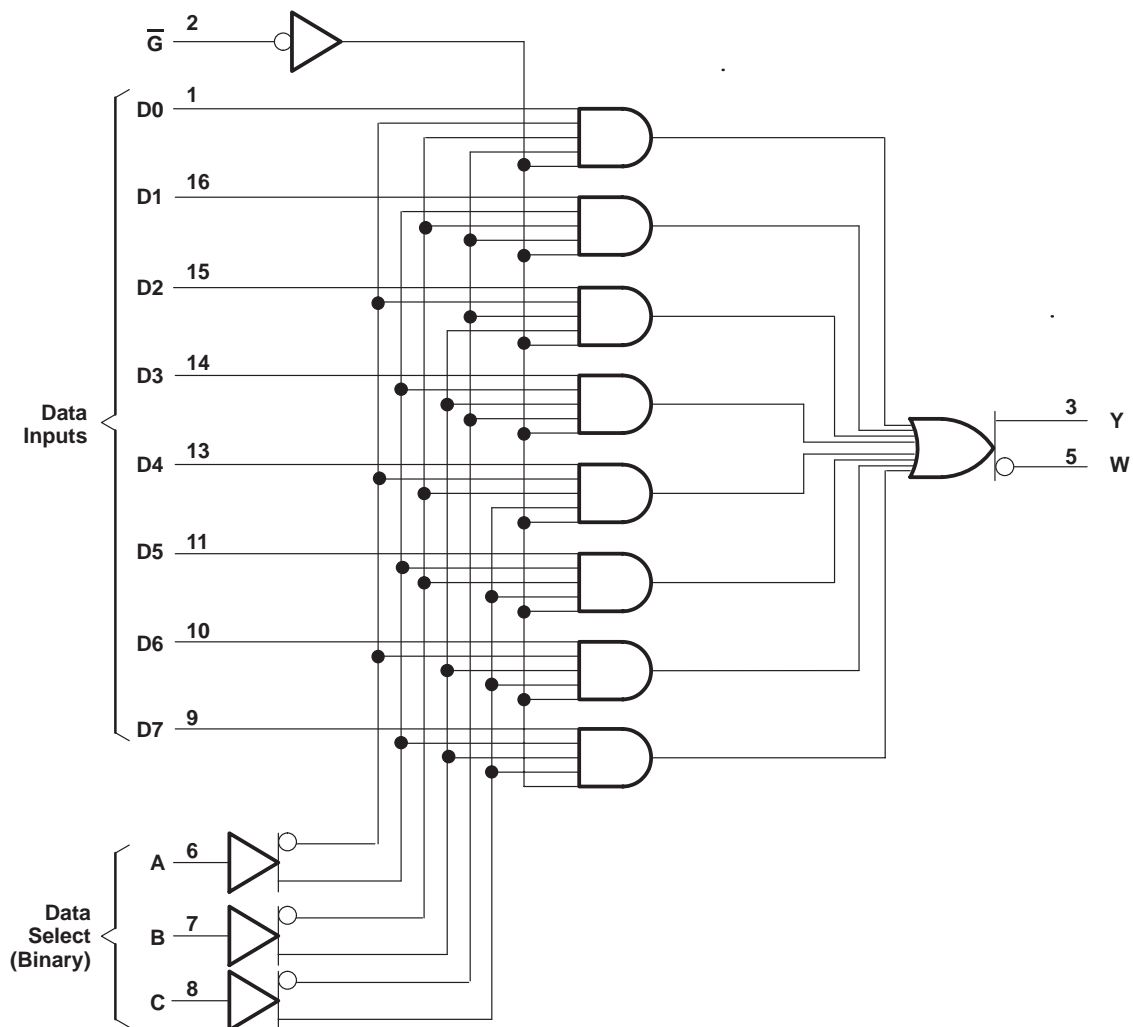
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logic diagram (positive logic)



**absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>**

Supply voltage range, $V_{CC}$	–0.5 V to 7 V
Input voltage range, $V_I$ (see Note 1)	–0.5 V to $V_{CC} + 0.5$ V
Output voltage range, $V_O$ (see Note 1)	–0.5 V to $V_{CC} + 0.5$ V
Input clamp current, $I_{IK}$ ( $V_I < 0$ or $V_I > V_{CC}$ )	$\pm 20$ mA
Output clamp current, $I_{OK}$ ( $V_O < 0$ or $V_O > V_{CC}$ )	$\pm 50$ mA
Continuous output current, $I_O$ ( $V_O = 0$ to $V_{CC}$ )	$\pm 50$ mA
Continuous current through $V_{CC}$ or GND	$\pm 100$ mA
Storage temperature range	–65°C to 150°C

<sup>†</sup> Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

**recommended operating conditions**

		MIN	MAX	UNIT
$V_{CC}$	Supply voltage	4.5	5.5	V
$V_{IH}$	High-level input voltage	2		V
$V_{IL}$	Low-level input voltage		0.8	V
$V_I$	Input voltage	0	$V_{CC}$	V
$V_O$	Output voltage	0	$V_{CC}$	V
$I_{OH}$	High-level output current		–24	mA
$I_{OL}$	Low-level output current		24	mA
$\Delta t/\Delta v$	Input transition rise or fall rate	0	10	ns/V
$T_A$	Operating free-air temperature	–40	85	°C

**electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)**

PARAMETER	TEST CONDITIONS	$V_{CC}$	$T_A = 25^\circ\text{C}$			MIN	MAX	UNIT
			MIN	TYP	MAX			
$V_{OH}$	$I_{OH} = -50 \mu\text{A}$	4.5 V	4.4			4.4		V
		5.5 V	5.4			5.4		
	$I_{OH} = -24 \text{ mA}$	4.5 V	3.94			3.8		
		5.5 V	4.94			4.8		
	$I_{OH} = -75 \text{ mA}^\dagger$	5.5 V				3.85		
$V_{OL}$	$I_{OL} = 50 \mu\text{A}$	4.5 V			0.1		0.1	V
		5.5 V			0.1		0.1	
	$I_{OL} = 24 \text{ mA}$	4.5 V			0.36		0.44	
		5.5 V			0.36		0.44	
	$I_{OL} = 75 \text{ mA}^\dagger$	5.5 V					1.65	
$I_I$	$V_I = V_{CC}$ or GND	5.5 V			$\pm 0.1$		$\pm 1$	$\mu\text{A}$
$I_{CC}$	$V_I = V_{CC}$ or GND, $I_O = 0$	5.5 V			8		80	$\mu\text{A}$
$\Delta I_{CC}^\ddagger$	One input at 3.4 V, Other inputs at GND or $V_{CC}$	5.5 V			0.9		1	mA
$C_i$	$V_I = V_{CC}$ or GND	5 V			3.5			pF

<sup>†</sup> Not more than one output should be tested at a time, and the duration of the test should not exceed 10 ms.

<sup>‡</sup> This is the increase in supply current for each input that is at one of the specified TTL voltage levels rather than 0 V to  $V_{CC}$ .

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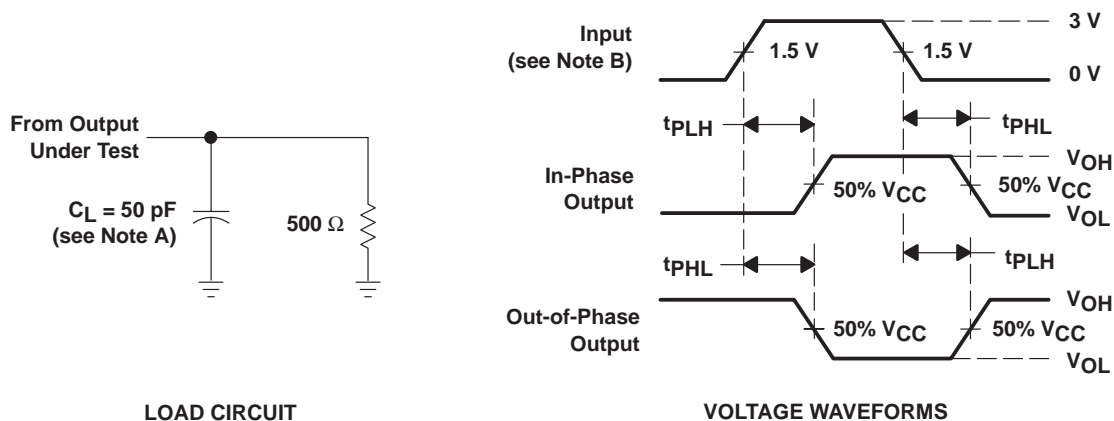
switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$T_A = 25^\circ\text{C}$			MIN	MAX	UNIT
			MIN	TYP	MAX			
$t_{PLH}$	A, B, or C	Y	3.6	6.8	9.9	3.6	11	ns
$t_{PHL}$			3.1	6.7	9.5	3.1	10.5	
$t_{PLH}$	A, B, or C	W	2.9	6.3	9	2.9	10	ns
$t_{PHL}$			2.7	6.3	9.3	2.7	10.4	
$t_{PLH}$	Any D	Y	3.2	5.7	7.5	3.2	8.3	ns
$t_{PHL}$			2.2	5.2	8	2.2	8.8	
$t_{PLH}$	Any D	W	2.1	4.7	7.3	2.1	7.8	ns
$t_{PHL}$			2.7	5.1	6.9	2.7	7.6	
$t_{PLH}$	$\overline{G}$	Y	1.5	3.7	5.8	1.5	6.3	ns
$t_{PHL}$			2.1	4.0	5.6	2.1	6.2	
$t_{PLH}$	$\overline{G}$	W	2.5	4.4	6.1	2.5	6.7	ns
$t_{PHL}$			1.7	4.1	6.4	1.7	6.9	

operating characteristics,  $V_{CC} = 5\text{ V}$ ,  $T_A = 25^\circ\text{C}$

PARAMETER	TEST CONDITIONS	TYP	UNIT
$C_{pd}$ Power dissipation capacitance	$C_L = 50\text{ pF}$ , $f = 1\text{ MHz}$	56	pF

### PARAMETER MEASUREMENT INFORMATION



- NOTES:
- $C_L$  includes probe and jig capacitance.
  - Input pulses are supplied by generators having the following characteristics:  $PRR \leq 10\text{ MHz}$ ,  $Z_O = 50\ \Omega$ ,  $t_r = 3\text{ ns}$ ,  $t_f = 3\text{ ns}$ .
  - The outputs are measured one at a time with one input transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms

## PACKAGING INFORMATION

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
74ACT11151D	OBSOLETE	SOIC	D	16		TBD	Call TI	Call TI
74ACT11151DR	OBSOLETE	SOIC	D	16		TBD	Call TI	Call TI
74ACT11151DR	OBSOLETE	SOIC	D	16		TBD	Call TI	Call TI
74ACT11151N	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI
74ACT11151N	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI

<sup>(1)</sup> The marketing status values are defined as follows:

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**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

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<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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74ACT11151DR	OBSOLETE	SOIC	D	16		TBD	Call TI	Call TI
74ACT11151N	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI
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