

NLX2GU04

Dual Unbuffered Inverter

The NLX2GU04 MiniGate™ is an advanced high-speed CMOS dual unbuffered inverter in ultra-small footprint.

This device is well suited for use in oscillator, pulse-shaping and high input impedance amplifier applications. For digital applications, the NLX2GU04 is recommended.

The NLX2GU04 input and output structures provide protection when voltages up to 7.0 V are applied, regardless of the supply voltage.

Features

- Designed for 1.65 V to 5.5 V V_{CC} Operation
- Unbuffered for Crystal Oscillator and Analog Applications
- 16 mA Balanced Output Source and Sink Capability
- Balanced Propagation Delays
- Overvoltage Tolerant (OVT) Input and Output Pins
- Ultra-Small Packages
- These are Pb-Free Devices

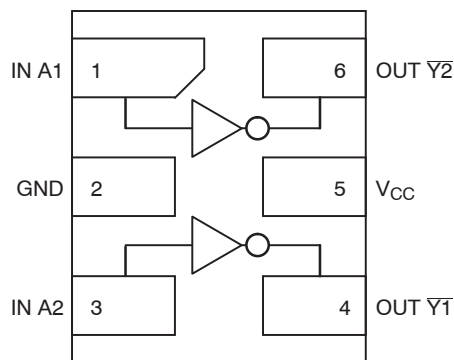


Figure 1. Pinout (Top View)

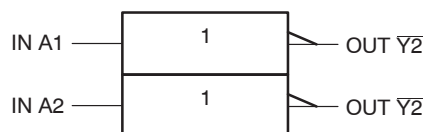


Figure 2. Logic Symbol

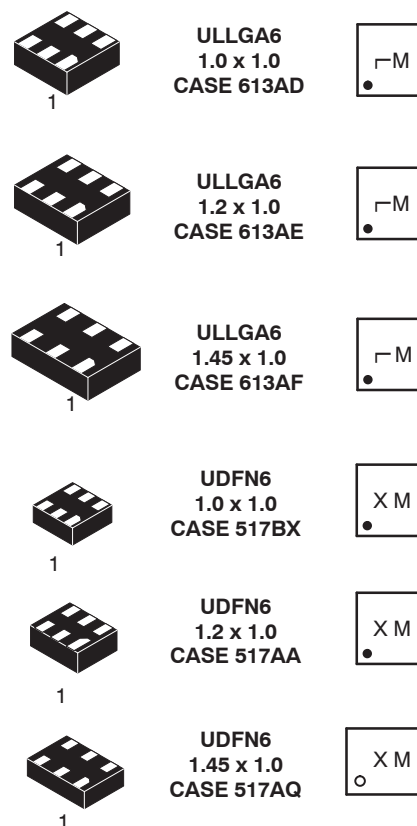
FUNCTION TABLE		PIN ASSIGNMENT	
A	Y		
L	H	1	IN A1
H	L	2	GND
		3	IN A2
		4	OUT Y2
		5	V_{CC}
		6	OUT Y1



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MARKING DIAGRAMS



L = Device Marking
M = Date Code

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

NLX2GU04

MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CC}	DC Supply Voltage	-0.5 to +7.0	V
V_{IN}	DC Input Voltage	-0.5 to +7.0	V
V_{OUT}	DC Output Voltage	-0.5 to +7.0	V
I_{IK}	DC Input Diode Current $V_{IN} < GND$	-50	mA
I_{OK}	DC Output Diode Current $V_{OUT} < GND$	-50	mA
I_O	DC Output Source/Sink Current	± 50	mA
I_{CC}	DC Supply Current Per Supply Pin	± 100	mA
I_{GND}	DC Ground Current per Ground Pin	± 100	mA
T_{STG}	Storage Temperature Range	-65 to +150	°C
T_L	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C
T_J	Junction Temperature Under Bias	150	°C
MSL	Moisture Sensitivity	Level 1	
F_R	Flammability Rating Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	
$I_{LATCHUP}$	Latchup Performance Above V_{CC} and Below GND at 125 °C (Note 5)	± 500	mA

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow.
2. Tested to EIA/JESD22-A114-A.
3. Tested to EIA/UESD22-A115-A.
4. Tested to JESD22-C101-A.
5. Tested to EIA / JESD78.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V_{CC}	Positive DC Supply Voltage	1.65	5.5	V
V_{IN}	Digital Input Voltage	0	5.5	V
V_{OUT}	Output Voltage	0	5.5	V
T_A	Operating Free-Air Temperature	-55	+125	°C
$\Delta t/\Delta V$	Input Transition Rise or Fall Rate $V_{CC} = 3.3 V \pm 0.3 V$ $V_{CC} = 5.0 V \pm 0.5 V$	0 0	100 20	ns/V

DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Conditions	V _{CC} (V)	T _A = 25 °C			T _A = +85°C		T _A = -55°C to +125°C		Unit
				Min	Typ	Max	Min	Max	Min	Max	
V _{IH}	Low-Level Input Voltage		1.65 2.3 to 5.5	0.85 x V _{CC} 0.80 x V _{CC}			0.85 x V _{CC} 0.80 x V _{CC}		0.85 x V _{CC} 0.80 x V _{CC}		V
V _{IL}	Low-Level Input Voltage		1.65 2.3 to 5.5			0.15 x V _{CC} 0.20 x V _{CC}		0.15 x V _{CC} 0.20 x V _{CC}		0.15 x V _{CC} 0.20 x V _{CC}	V
V _{OH}	High-Level Output Voltage	V _{IN} = V _{IH} or V _{IL} I _{OH} = -100 µA	1.65 to 5.5	V _{CC} - 0.1	V _{CC}		V _{CC} - 0.1		V _{CC} - 0.1		V
		V _{IN} = V _{IH} or V _{IL} I _{OH} = -3 mA	1.65	1.29	1.52		1.29		1.29		
		I _{OH} = -4 mA	2.3	1.9	2.1		1.9		1.9		
		I _{OH} = -6 mA	2.7	2.2	2.3		2.2		2.2		
		I _{OH} = -8 mA	3.0	2.4	2.6		2.4		2.4		
		I _{OH} = -12 mA	3.0	2.3	2.5		2.3		2.3		
		I _{OH} = -16 mA	4.5	3.8	4.0		3.8		3.8		
V _{OL}	Low-Level Output Voltage	V _{IN} = V _{IH} or V _{IL} I _{OL} = 50 µA	1.65 to 5.5			0.1		0.1		0.1	V
		V _{IN} = V _{IH} or V _{IL} I _{OH} = 3 mA	1.65		0.08	0.24		0.24		0.24	
		I _{OH} = 4 mA	2.3		0.12	0.3		0.3		0.3	
		I _{OH} = 6 mA	2.7		0.2	0.4		0.4		0.4	
		I _{OH} = 8 mA	3.0		0.24	0.4		0.4		0.4	
		I _{OH} = 12 mA	3.0		0.36	0.55		0.55		0.55	
		I _{OH} = 16 mA	4.5		0.31	0.55		0.55		0.55	
I _{IN}	Input Leakage Current	0 ≤ V _{IN} ≤ 5.5 V	0 to 5.5			±0.1		±1.0		±1.0	µA
I _{OFF}	Power-Off Output Leakage Current	V _{OUT} = 5.5 V	0			1.0		10		10	µA
I _{CC}	Quiescent Supply Current	0 ≤ V _{IN} ≤ V _{CC}	5.5			1.0		10		10	µA

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AC ELECTRICAL CHARACTERISTICS (Input $t_r = t_f = 3.0$ ns)

Symbol	Parameter	V_{CC} (V)	Test Condition	$T_A = 25^\circ\text{C}$			$T_A = -55^\circ\text{C}$ to $+125^\circ\text{C}$		Unit
				Min	Typ	Max	Min	Max	
t_{PLH} , t_{PHL}	Propagation Delay, Input A to Output Y	1.65 to 1.95	$R_L = 1\text{ M}\Omega$, $C_L = 15\text{ pF}$	1.5	1.8	5.5	1.5	11	ns
		2.3 to 2.7	$R_L = 1\text{ M}\Omega$, $C_L = 15\text{ pF}$	1.2	3.3	5.7	1.2	6.3	
		3.0 to 3.6	$R_L = 1\text{ M}\Omega$, $C_L = 15\text{ pF}$	0.8	2.7	4.1	0.8	4.5	
			$R_L = 500\text{ }\Omega$, $C_L = 50\text{ pF}$	1.2	4.0	6.4	1.2	7.0	
		4.5 to 5.5	$R_L = 1\text{ M}\Omega$, $C_L = 15\text{ pF}$	0.5	2.2	3.3	0.5	3.6	
			$R_L = 500\text{ }\Omega$, $C_L = 50\text{ pF}$	0.8	3.4	5.6	0.8	6.2	
C_{IN}	Input Capacitance	5.5	$V_{IN} = 0\text{ V}$ or V_{CC}		7				pF
C_{OUT}	Output Capacitance	5.5	$V_{IN} = 0\text{ V}$ or V_{CC}		8				pF
C_{PD}	Power Dissipation Capacitance (Note 6)	5.5	10 MHz $V_{IN} = 0\text{ V}$ or V_{CC}		25				pF

6. C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the dynamic operating current consumption without load. Average operating current can be obtained by the equation $I_{CC(OPR)} = C_{PD} \cdot V_{CC} \cdot f_{in} + I_{CC}$. C_{PD} is used to determine the no-load dynamic power consumption: $P_D = C_{PD} \cdot V_{CC}^2 \cdot f_{in} + I_{CC} \cdot V_{CC}$.

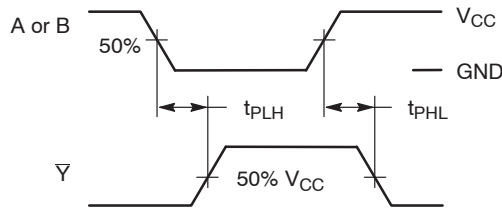
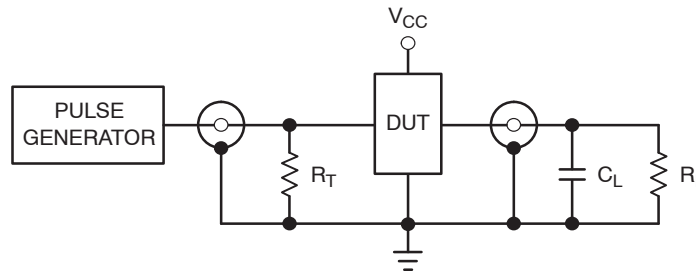


Figure 3. Switching Waveforms



$R_T = Z_{OUT}$ of pulse generator (typically $50\text{ }\Omega$)

Figure 4. Test Circuit

NLX2GU04

ORDERING INFORMATION

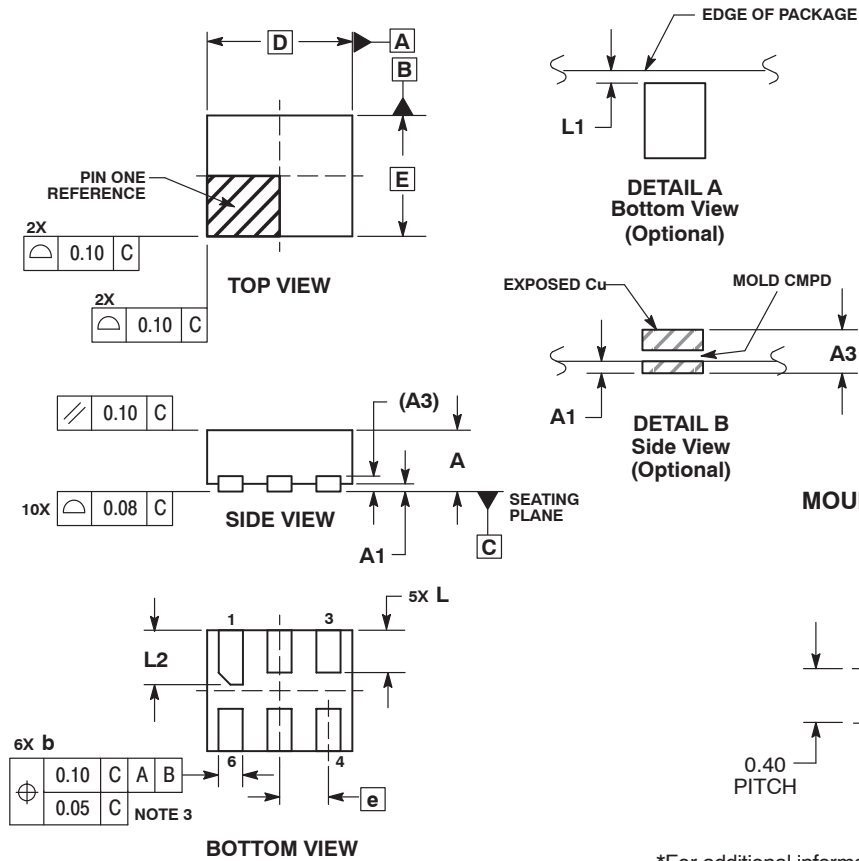
Device	Package	Shipping†
NLX2GU04AMX1TCG	ULLGA6, 1.45 x 1.0, 0.5P (Pb-Free)	3000 / Tape & Reel
NLX2GU04BMX1TCG	ULLGA6, 1.2 x 1.0, 0.4P (Pb-Free)	3000 / Tape & Reel
NLX2GU04CMX1TCG	ULLGA6, 1.0 x 1.0, 0.35P (Pb-Free)	3000 / Tape & Reel
NLX2GU04MUTCG	UDFN6, 1.2 x 1.0, 0.4P (Pb-Free)	3000 / Tape & Reel
NLX2GU04AMUTCG	UDFN6, 1.45 x 1.0, 0.5P (Pb-Free)	3000 / Tape & Reel
NLX2GU04AMUTCG	UDFN6, 1.0 x 1.0, 0.35P (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

NLX2GU04

PACKAGE DIMENSIONS

UDFN6 1.2x1.0, 0.4P
CASE 517AA
ISSUE O

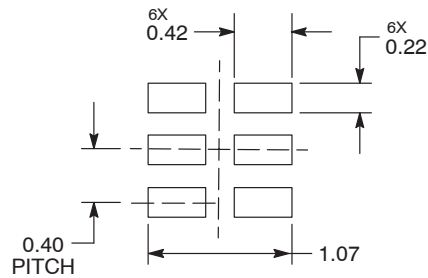


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.25 AND 0.30 mm FROM TERMINAL.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

MILLIMETERS		
DIM	MIN	MAX
A	0.45	0.55
A1	0.00	0.05
A3	0.127 REF	
b	0.15	0.25
D	1.20 BSC	
E	1.00 BSC	
e	0.40 BSC	
L	0.30	0.40
L1	0.00	0.15
L2	0.40	0.50

MOUNTING FOOTPRINT*



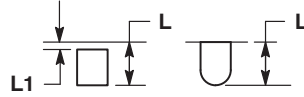
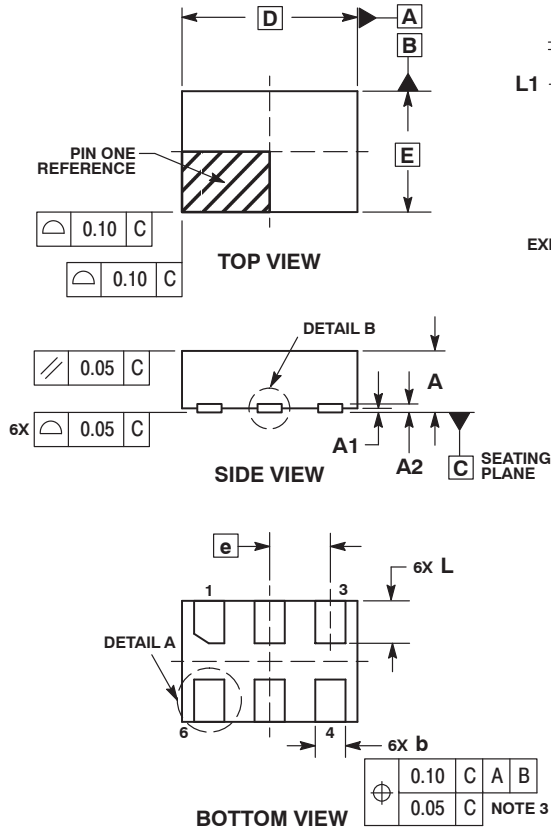
DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

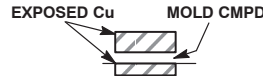
NLX2GU04

PACKAGE DIMENSIONS

UDFN6 1.45x1.0, 0.5P
CASE 517AQ
ISSUE O



DETAIL A
OPTIONAL
CONSTRUCTIONS



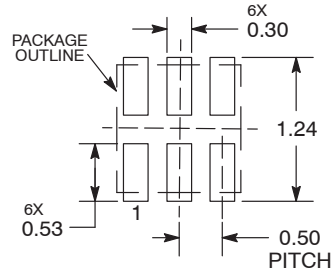
DETAIL B
OPTIONAL
CONSTRUCTIONS

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM THE TERMINAL TIP.

MILLIMETERS		
DIM	MIN	MAX
A	0.45	0.55
A1	0.00	0.05
A2	0.07 REF	
b	0.20	0.30
D	1.45 BSC	
E	1.00 BSC	
e	0.50 BSC	
L	0.30	0.40
L1	---	0.15

MOUNTING FOOTPRINT



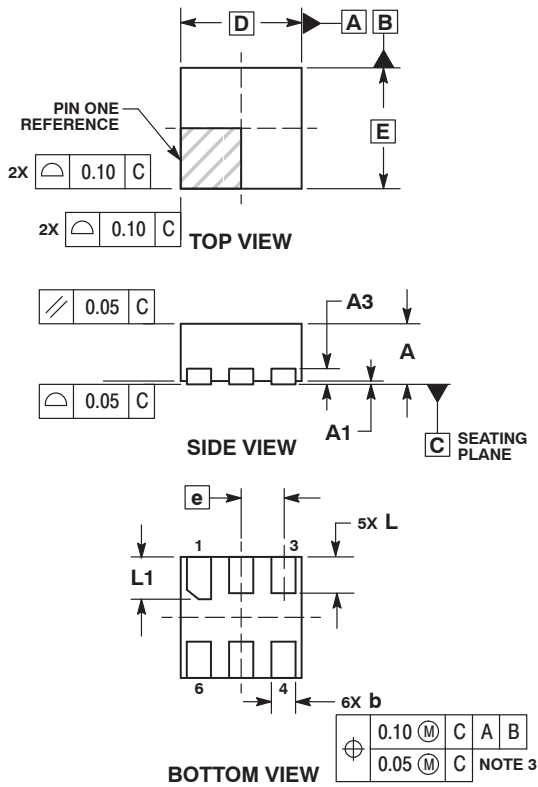
DIMENSIONS: MILLIMETERS

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NLX2GU04

PACKAGE DIMENSIONS

UDFN6 1.0x1.0, 0.35P
CASE 517BX
ISSUE O

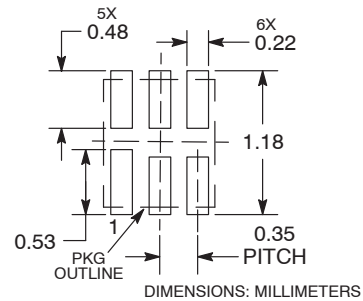


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 MM FROM TERMINAL TIP.
4. PACKAGE DIMENSIONS EXCLUSIVE OF BURRS AND MOLD FLASH.

MILLIMETERS		
DIM	MIN	MAX
A	0.45	0.55
A1	0.00	0.05
A3	0.13	REF
b	0.12	0.22
D	1.00	BSC
E	1.00	BSC
e	0.35	BSC
L	0.25	0.35
L1	0.30	0.40

RECOMMENDED SOLDERING FOOTPRINT*

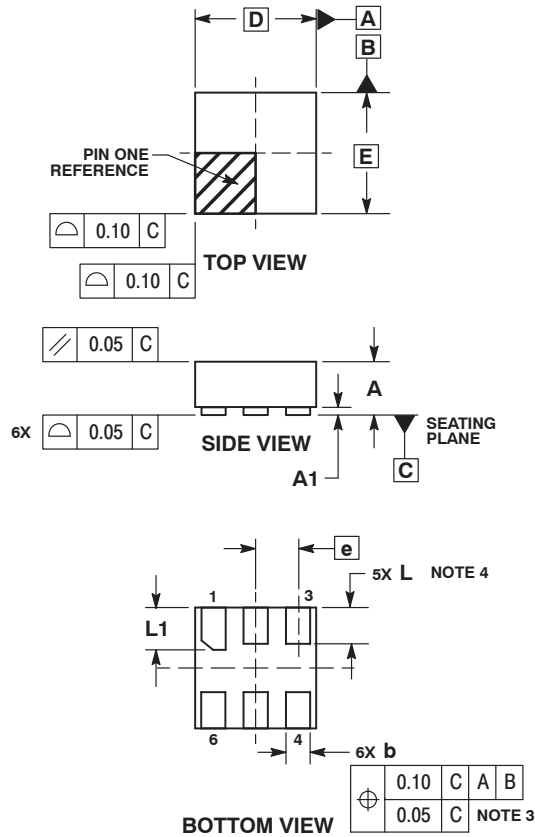


*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NLX2GU04

PACKAGE DIMENSIONS

ULLGA6 1.0x1.0, 0.35P
CASE 613AD
ISSUE A

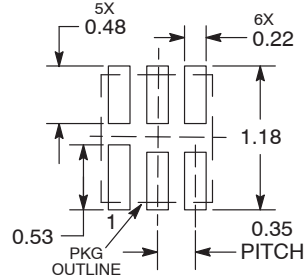


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM THE TERMINAL TIP.
4. A MAXIMUM OF 0.05 PULL BACK OF THE PLATED TERMINAL FROM THE EDGE OF THE PACKAGE IS ALLOWED.

MILLIMETERS		
DIM	MIN	MAX
A	---	0.40
A1	0.00	0.05
b	0.12	0.22
D	1.00	BSC
E	1.00	BSC
e	0.35	BSC
L	0.25	0.35
L1	0.30	0.40

MOUNTING FOOTPRINT SOLDERMASK DEFINED*



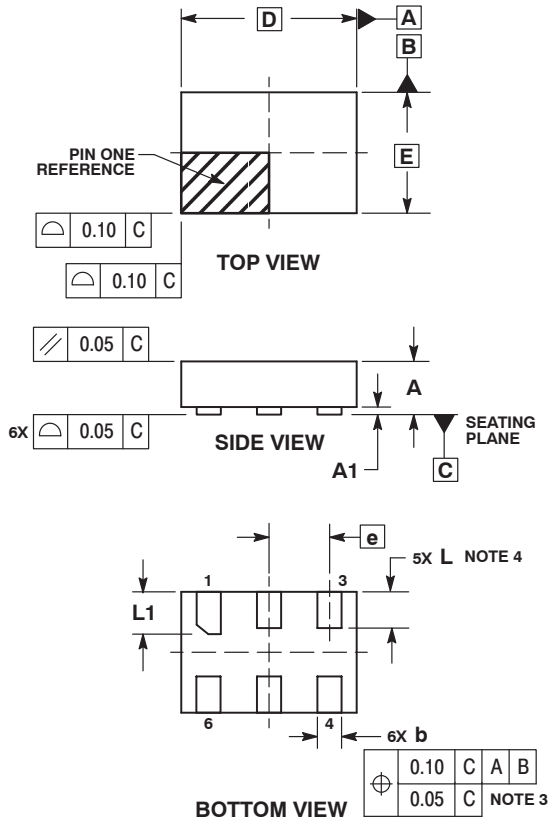
DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NLX2GU04

PACKAGE DIMENSIONS

ULLGA6 1.45x1.0, 0.5P
CASE 613AF
ISSUE A

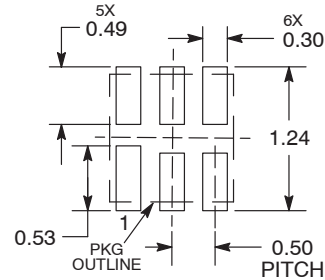


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2. CONTROLLING DIMENSION: MILLIMETERS.
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4. A MAXIMUM OF 0.05 PULL BACK OF THE PLATED TERMINAL FROM THE EDGE OF THE PACKAGE IS ALLOWED.

MILLIMETERS		
DIM	MIN	MAX
A	---	0.40
A1	0.00	0.05
b	0.15	0.25
D	1.45	BSC
E	1.00	BSC
e	0.50	BSC
L	0.25	0.35
L1	0.30	0.40

MOUNTING FOOTPRINT SOLDERMASK DEFINED*



DIMENSIONS: MILLIMETERS

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