

ACX114YUQ

SMALL SIGNAL COMPLEMENTARY PRE-BIASED DUAL TRANSISTOR

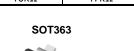
Features

- Epitaxial Planar Die Construction
- · Built-In Biasing Resistors
- Surface Mount Package Suited for Automated Assembly
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical	Data
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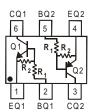
- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.006 grams (Approximate)







Top View



Device Schematic

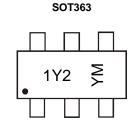
Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ACX114YUQ-7R	Automotive	1Y2	7	8	3,000
ACX114YUQ-13R	Automotive	1Y2	13	8	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/quality/product_compliance_definitions/.
- 5. -13R are parts rotated in the pocket tape by +180°. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



1Y2 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: D = 2016) M = Month (ex: 9 = September)

Date Code Kev

Year	2016	2017	2018	2019	202	20 20)21 2	2022	2023	2024	2025	2026
Code	D	Е	F	G	Н		I	J	K	L	М	N
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Absolute Maximum Ratings - NPN Section (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Supply Voltage <pin: (1)="" (6)="" to=""></pin:>	V _{CC}	50	V
Input Voltage <pin: (1)="" (2)="" to=""></pin:>	V _{IN}	-6 to +40	V
Output Current	l ₀	70	mA
Output Current	I _C (Max)	100	mA

Absolute Maximum Ratings - PNP Section (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Supply Voltage <pin: (3)="" (4)="" to=""></pin:>	V _{CC}	-50	V
Input Voltage <pin: (4)="" (5)="" to=""></pin:>	V _{IN}	+6 to -40	V
Output Current	lo	-70	mA
Output Current	I _C (Max)	-100	mA

Thermal Characteristics ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Notes 6 & 7)	P_{D}	270	mW
Thermal Resistance, Junction to Ambient Air (Note 6)	$R_{ hetaJA}$	450	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

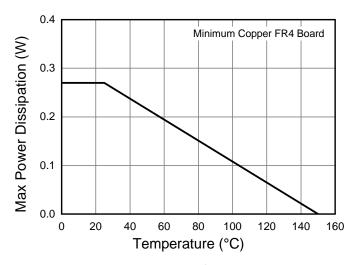
Notes:

^{6.} Mounted on FR4 PC Board with minimum recommended pad layout.

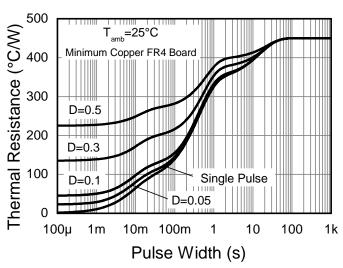
^{7. 150}mW per element must not be exceeded.



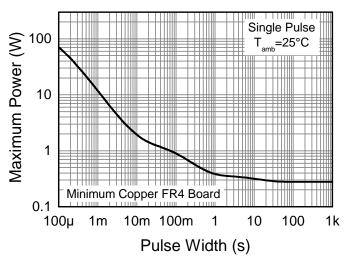
Thermal Characteristics and Derating Information



Derating Curve



Transient Thermal Impedance



Pulse Power Dissipation



Electrical Characteristics - NPN Section (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Input Voltage	V _{I(OFF)}	0.3	_		V	$V_{CC} = 5V, I_{O} = 100\mu A$
imput voltage	$V_{I(ON)}$	_	_	1.4	V	$V_O = 0.3V$, $I_O = 1mA$
Output Voltage	V _{O(ON)}	_	0.1	0.3	V	$I_0/I_1 = 5mA / 0.25mA$
Input Current	l _l	_	_	0.88	mA	$V_I = 5V$
Output Current	I _{O(OFF)}	_	_	0.5	μΑ	$V_{CC} = 50V, V_{I} = 0V$
DC Current Gain (Note 8)	Gı	80	_	_	_	$V_0 = 5V, I_0 = 10mA$
Input Resistor (R ₁) Tolerance	ΔR_1	-30	_	+30	%	_
Resistance Ratio Tolerance	$\Delta R_2/R_1$	-20	_	+20	%	_
Gain-Bandwidth Product	f _T	_	250	_	MHz	V _{CE} = 10V, I _E = 5mA, f = 100MHz

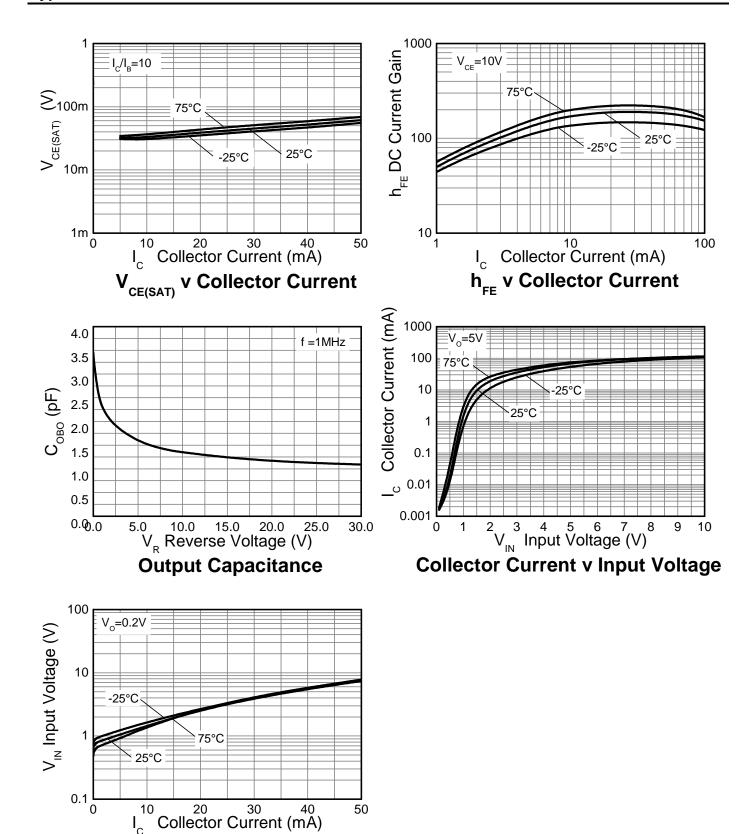
Electrical Characteristics - PNP Section (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Input Voltage	$V_{I(OFF)}$	-0.3	_	_	V	$V_{CC} = -5V$, $I_{O} = -100\mu A$
input voltage	$V_{I(ON)}$			-1.4	V	$V_O = -0.3V$, $I_O = -1mA$
Output Voltage	V _{O(ON)}	_	-0.1	-0.3	V	$I_0/I_1 = -5mA / -0.25mA$
Input Current	II	_	_	-0.88	mA	$V_I = -5V$
Output Current	I _{O(OFF)}	_	_	-0.5	μΑ	$V_{CC} = -50V, V_I = 0V$
DC Current Gain (Note 8)	Gı	80	_	_	_	$V_0 = -5V$, $I_0 = -10mA$
Input Resistor (R ₁) Tolerance	ΔR_1	-30	_	+30	%	_
Resistance Ratio Tolerance	$\Delta R_2/R_1$	-20	_	+20	%	_
Gain-Bandwidth Product	f_T	_	250	_	MHz	$V_{CE} = -10V$, $I_{E} = -5mA$, $f = 100MHz$

Note: 8. Measured under pulsed conditions. Pulse width \leq 300 μ s. Duty cycle \leq 2%.



Typical Electrical Characteristics - NPN Section (@T_A = +25°C, unless otherwise specified.)

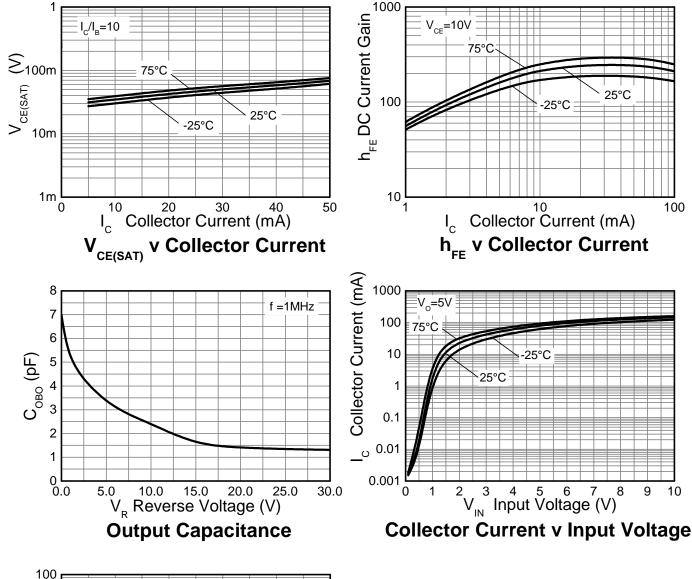


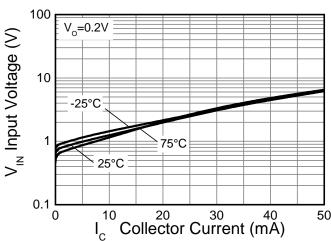
Input Voltage v Collector Current

50



Typical Electrical Characteristics - PNP Section (@T_A = +25°C, unless otherwise specified.)



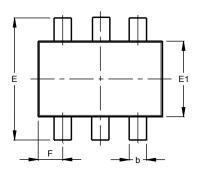


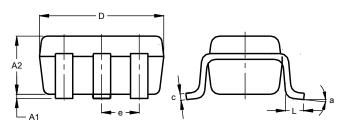
Input Voltage v Collector Current



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

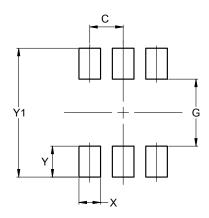




	SOT363							
Dim	Min	Max	Тур					
A1	0.00	0.10	0.05					
A2	0.90	1.00	1.00					
b	0.10	0.30	0.25					
С	0.10	0.22	0.11					
D	1.80	2.20	2.15					
E	2.00	2.20	2.10					
E1	1.15	1.35	1.30					
е	().650 E	SC					
F	0.40	0.45	0.425					
L	0.25	0.40	0.30					
а	0°	8°						
All	All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
C	0.650
G	1.300
X	0.420
Y	0.600
V1	2 500



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