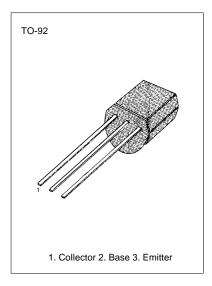
PNP EPITAXIAL SILICON TRANSISTOR

SWITCHING AND AMPLIFIER

HIGH VOLTAGE: BC556, V_{CEO}= -65V
LOW NOISE: BC559, BC560
Complement to BC546 ... BC 550

ABSOLUTE MAXIMUM RATINGS (T_A=25°C)

Characteristic	Symbol	Rating	Unit
Collector-Base Capacitance : BC556 : BC557/560 : BC558/559 Collector-Emitter Voltage : BC556 : BC557/560 : BC558/559	V _{CBO}	-80 -50 -30 -65 -45 -30	>
Emitter-Base Voltage Collector Current (DC) Collector Dissipation Junction Temperature Storage Temperature	V _{EBO} I _C P _C T _J T _{STG}	-5 -100 500 150 -65 ~ 150	V mA mW °C °C



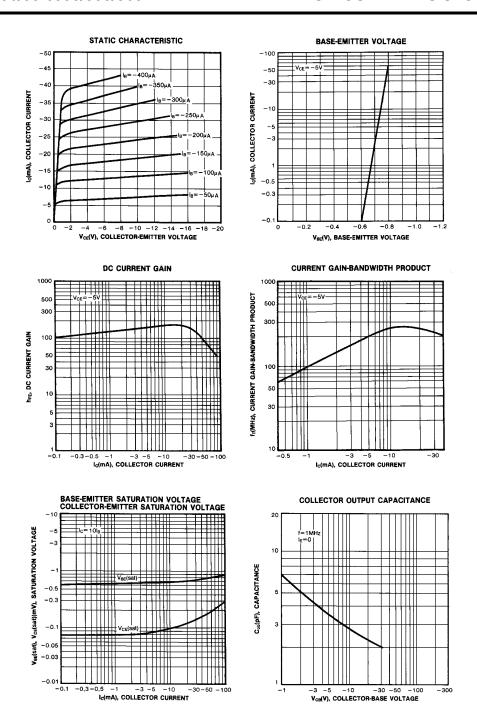
ELECTRICAL CHARACTERISTICS (T_A=25°C)

Char	acteristic	Symbol	Test Conditions	Min	Тур	Max	Unit
Collector Cut-off Cu	urrent	I _{CBO}	V _{CB} = -30V, I _E =0			-15	nA
DC Current Gain		h _{FE}	V_{CE} = -5V, I_{C} =2mA	110		800	
Collector Emitter S	aturation Voltage	V _{CE} (sat)	I_{C} = -10mA, I_{B} = -0.5mA		-90	-300	mV
	· ·		I_{C} = -100mA, I_{B} = -5mA		-250	-650	mV
Collector Base Sati	uration Voltage	V _{BE} (on)	I_{C} = -10mA, I_{B} = -0.5mA		-700		mV
	_		$I_{C} = -100 \text{mA}, I_{B} = -5 \text{mA}$		-900		mV
Base Emitter On Vo	oltage	V _{BE} (on)	V_{CE} = -5V, I_{C} = -2mA	-600	-660	-750	mV
	· ·		$V_{CE} = -5V, I_{C} = -10mA$			-800	mV
Current Gain Band	width Product	f⊤	V_{CE} = -5V, I_{C} = -10mA		150		MHz
Collector Base Cap	pacitance	C _{CBO}	V _{CB} = -10V, f=1MHz			6	pF
Noise Figure	: BC556/557/558	NF	$V_{CE} = -5V$, $I_{C} = -200\mu A$		2	10	dB
3	: BC559/560		$f=1KHz$, $R_G=2K\Omega$		1	4	dB
	: BC559	NF	$V_{CE} = -5V$, $I_{C} = -200\mu A$		1.2	4	dB
	: BC560	141	R _G =2KΩ f=30~15000MHz		1.2	2	dB

h_{FE} CLASSIFICATION

Classification	Α	В	С
h _{FE}	110-220	200-450	420-800







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FACTTM QSTM

 $\begin{array}{lll} \mathsf{FACT} \ \mathsf{Quiet} \ \mathsf{Series^{\mathsf{TM}}} & \mathsf{Quiet} \ \mathsf{Series^{\mathsf{TM}}} \\ \mathsf{FAST}^{\tiny{\textcircled{\tiny{\$}}}} & \mathsf{SuperSOT^{\mathsf{TM}}}\text{-3} \\ \mathsf{FASTr^{\mathsf{TM}}} & \mathsf{SuperSOT^{\mathsf{TM}}}\text{-6} \\ \mathsf{GTO^{\mathsf{TM}}} & \mathsf{SuperSOT^{\mathsf{TM}}}\text{-8} \\ \mathsf{HiSeC^{\mathsf{TM}}} & \mathsf{TinyLogic^{\mathsf{TM}}} \end{array}$

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PRODUCT STATUS DEFINITIONS

Definition of Terms

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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