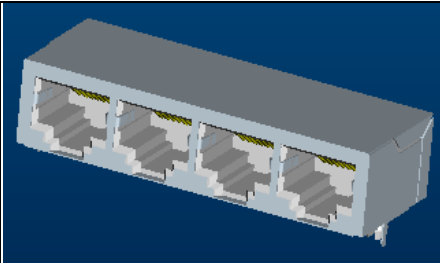
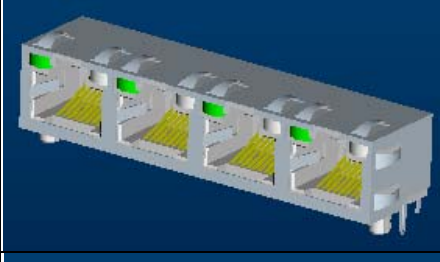
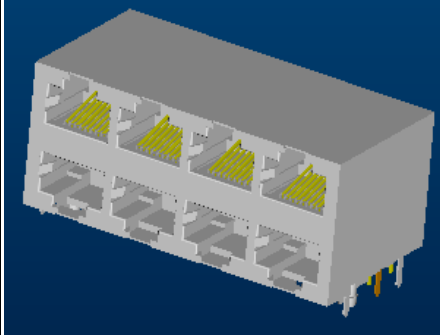
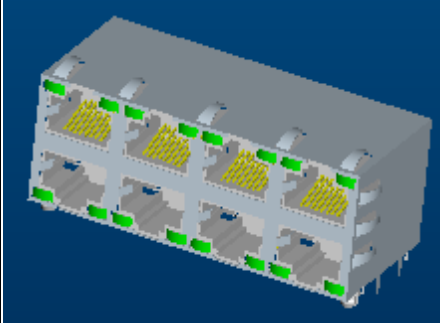


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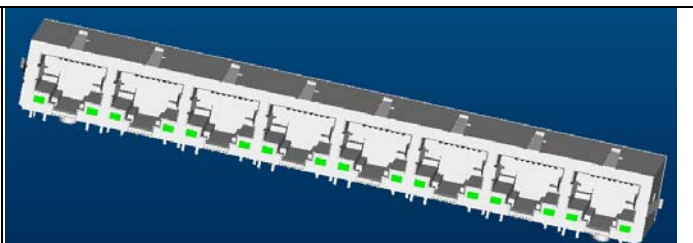
1.0 OBJECTIVE

This specification defines the performance, test and reliability requirements of RJ45 series connector.

Ganged Multi-ports without LED (10118061,10118062,10118063,10118064, 10118065)	
Ganged Multi-ports with LED (10118066,10118067,10118068)	
Stacked Multi-ports without LED (10118069,10118070,10118071,10118072, 10118073,10118074)	
Stacked Multi-ports with LED (10118075,10118076,10118077,10118078)	

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Ganged Multi-ports with LED
(10113616)



2.0 SCOPE

This specification is applicable to RJ45 series connector. The standard operating temperatures for this product is -40°C to 85°C .

3.0 CONNECTOR SPECIFICATION

- 3-1 Connector shape, dimensions and PCB layout are shown on the customer drawings.
- 3-2 Packaging condition is shown on package specification.
- 3-3 RoHS Compliance per EU Directive 2002/95/EC
- 3-4 Plastic Housings:
 - A. Main Housing: High Thermoplastic UL94V-0, Black.
- 3-5 Terminals:
 - A. RJ Contacts: Copper Alloy.
 - B. RJ Solder Pins: Copper Alloy.
 - C. LED Solder Pins: Steel. (LED by customer request)
- 3-6 Shields:
 - A. Shield: Copper Alloy.

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3-7 Plating:

- A. RJ Contacts: As P/N specified.
- B. RJ Solder Pins: 1u" gold over 50u" Nickel under-plating
- C. LED Solder Pins: 100u" min. Bright Tin or matte Tin over 50u" Nickel under-plating (LED by customer request)
- D. Shield: 20u" Nickel over all.

4.0 OPERATING & TEST REQUIREMENTS

Product is designed to meet electrical, mechanical and environmental performance requirements specified below. All tests are performed at ambient environmental conditions per MIL-STD-1344A and EIA-364 unless otherwise specified.

4.1 Operating Temperature Range: -40° TO +85°.

4.2 Storage Temperature: -40° to 85°.

4.3 Voltage : 150 volts AC.

4.4 Current : 1 Amps.

4.5 Test requirements and procedure summary

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ELECTRICAL			
	Item	Test Condition	Specification
4.5.1	Low Level Contact Resistance	Mate subject connector with compatible connector. EIA-364-23	40 mΩ max. initial 50 mΩ max. final
4.5.2	Insulation Resistance	Apply 100±10% Volts DC between adjacent contacts of mated connectors for one minute. EIA-364-21	1000 MΩ min initial 50 MΩ min final
4.5.3	Dielectric Withstanding Voltage	For mated specimens, 2250VDC between connected RJ interface contacts and all PCB tails connected together with shield. 1 mA cutoff current, 500 Volts per second maximum ramp. EIA-364-20	No discharge, flashover or breakdown for 1 min. Current leakage: 1 mA max.
4.5.4	Temperature Rise	30□ Max. Change allowed	Mate connector: measure the temperature rise at rated current until temperature stable. The ambient condition is still air.(EIA-364-70)
MECHANICAL			
	Item	Test Condition	Specification
4.5.5	Un-mating Force (with locked & unlocked)	Measure force necessary to un-mate connectors using the free floating fixtures at rate of 25mm/min.	Un-mating force: unlocked: 9.8N max. locked: 89N min.
4.5.6	Mating Force	Measured with latch depressed. EIA-364-13	Mating force: 22N max.

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4.5.7	Physical Shock	Subject mated connectors to 50 G's (peak value) half-sine shock pulses of 11 milliseconds duration. Three shocks in each direction shall be applied along the three mutually perpendicular axes of the test specimen (18 shocks). The electrical load condition shall be 100mA maximum for all contacts. EIA-364-27	No electrical discontinuity greater than 1μsecond. Shall meet visual requirements, and show no physical damages.
4.5.8	Durability	The sample should be mounted in the tester and fully mated and unmated 300 times per hour at the rate of 25mm/min. EIA-364-09	1000 cycles with no function damage for RJ-45. Low Level Contact Resistance: 50mΩ max. final
4.5.9	Random Vibration	The electrical load condition shall be 100mA maximum for all contacts. Subject to a simple harmonic motion having amplitude of 0.76mm (1.52mm maximum total excursion) in frequency which being varied uniformly between the approximate limits of 10 and 55 Hz. The entire frequency range, from 10 to 55 Hz and return to 10 Hz, shall be traversed in approximately 1 minute. This motion shall be applied for 2 hours in each of three mutually perpendicular directions. EIA-364-28	No electrical discontinuity greater than 1μsecond. Shall meet visual requirements, and show no physical damages.

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ENVIRONMENTAL			
	Item	Test Condition	Specification
4.5.10	Thermal Shock (Temperature Cycling)	Subject mated connectors to 5 cycles between -55□ and 85□, 30 minutes duration at both temperature extremes. EIA-364-32	Shall meet visual requirements, show no physical damage. Low Level Contact Resistance: 50 mΩ max. final
4.5.11	Humidity-Temperature Cycling	Mated connectors placed in humidity chamber (Humidity 90-95%, Temperature 40±2□) for 96 hours. EIA-364-31	Shall meet visual requirements, show no physical damage. Low Level Contact Resistance: 50 mΩ max. final
4.5.12	Temperature Life (Heat Aging)	Subject mated connectors to temperature life at 65±2□ for 96 hours. EIA-364-17, METHOD A	Shall meet visual requirements, show no physical damage. Low Level Contact Resistance: 50 mΩ max. final
4.5.13	Solderability	Subject the test area of contacts into flux for 5~10 seconds and then dip into solder bath, controlled at 265±5□, for 5±0.5 seconds at a rate of 25±6mm per second.	Solderable area shall have minimum of 95% solder coverage.

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ENVIRONMENTAL			
	Item	Test Condition	Specification
4.5.14	Salt Spray	Subject mated/unmated connectors to 5% salt-solution concentration, 35° for 48 hours. EIA-364-26	Shall meet visual requirements, show no physical damage. Low Level Contact Resistance: 50 mΩ max. final
4.5.15	Resistance to Wave Soldering Heat	Peak Temp. : 265±5° for 5 seconds. Refer to "7. Recommend wave soldering profile) "	Shall meet visual requirements, show no physical damage.

(a) All tests are for RJ45 Module unless otherwise specified.

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5.0 TEST PLAN

Test group Test sequence Test Item		A	B	C	D	E	F	G	H	I
Examination of product		1,7	1,7	1,5	1,5	1,5	1,5	1,5	1,7	1,3
4.5.1	Low Level Contact Resistance		2,6	2,4	2,4	2,4		2,4	3,6	
4.5.2	Insulation Resistance	2,5								
4.5.3	Dielectric Withstanding Voltage	3,6								
4.5.4	Temperature Rise									2
4.5.5	Un-mating Forces						3			
4.5.6	Mating Force		3,5						2	
4.5.7	Physical Shock								5	
4.5.8	Durability	4	4							
4.5.9	Random Vibration								4	
4.5.10	Thermal Shock					3				
4.5.11	Humidity-Temperature Cycling				3					
4.5.12	Temperature Life			3						
4.5.13	Solderability						2			
4.5.14	Salt Spray							3		
4.5.15	Resistance to Wave Soldering Heat						4			

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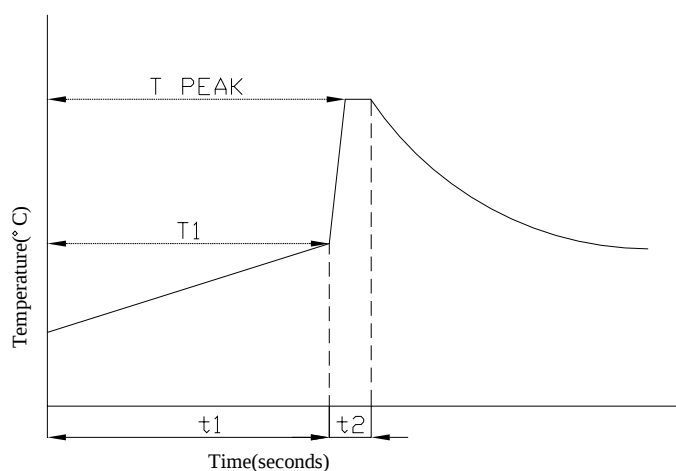
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6.0 STORAGE REQUIREMENTS

All products shall be packaged against any physical damage and corrosion during shipment or in storage.

7.0 RECOMMEND WAVE SOLDERING PROFILE



PARAMETER	REFERENCE	LEAD FREE SPECIFICATION
PREHEAT TEMPERATURE GRADIENT		+1~4°C/sec
PREHEAT TIME	t1	2~3 Min.
PREHEAT TEMPERATURE	T1	>100°C
SOLDER POT TEMPERATURE	T PEAK	265°C±5°C
DWELL TIME	t2	5 Sec.
PEAK BOARD TOP TEMPERATURE		190°C
COOLING TEMPERATURE GRADIENT		-6°C/SEC Max.

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8.0 APPLICABLE PART NUMBER:

Part Number	Product Description	Remark
10118061-xxxxxxxLF	Ganged Jack Multi-ports without LED	
10118062-xxxxxxxLF	Ganged Jack Multi-ports without LED	
10118063-xxxxxxxLF	Ganged Jack Multi-ports without LED	
10118064-xxxxxxxLF	Ganged Jack Multi-ports without LED	
10118065-xxxxxxxLF	Ganged Jack Multi-ports without LED	
10118066-xxxxxxxLF	Ganged Jack Multi-ports with LED	
10118067-xxxxxxxLF	Ganged Jack Multi-ports with LED	
10118068-xxxxxxxLF	Ganged Jack Multi-ports with LED	
10118069-xxxxxxxLF	Stacked Jack Multi-ports without LED	
10118069-xxxxxxxLF	Stacked Jack Multi-ports without LED	
10118070-xxxxxxxLF	Stacked Jack Multi-ports without LED	
10118071-xxxxxxxLF	Stacked Jack Multi-ports without LED	
10118072-xxxxxxxLF	Stacked Jack Multi-ports without LED	
10118073-xxxxxxxLF	Stacked Jack Multi-ports without LED	
10118074-xxxxxxxLF	Stacked Jack Multi-ports without LED	
10118075-xxxxxxxLF	Stacked Jack Multi-ports with LED	
10118076-xxxxxxxLF	Stacked Jack Multi-ports with LED	
10118077-xxxxxxxLF	Stacked Jack Multi-ports with LED	
10118078-xxxxxxxLF	Stacked Jack Multi-ports with LED	
10113616-xxxxxxxLF	Ganged Jack Multi-ports with LED	

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Revision Record

Revision	Page	Description	ECR No.	Date
A	ALL	Released	-----	09/03/2011
B	2,3,10	Adding new P/Ns of 10113616 series; Updating Operating Temperature Range from -0° ~ 70° to -40° ~ +85°.	ECN-ELX-N-008543	12/13/2011

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