



P—Dip Couplers

Device Type	Current Transfer Ratio		Electrical Isolation Ratings kV	Key Features
	Minimum %	α IF mA		
TIL111	12.5	16	1.5	Base lead provided for conventional transistor biasing. Six-pin dual-in-line plastic package
TIL112	2	10	1.5	
TIL113	300	10	1.5	
TIL114	12.5	16	2.5	
TIL115	2	10	2.5	
TIL116	20	10	2.5	Six-pin dual-in-line plastic package
TIL117	50	10	2.5	
TIL118	6	10	1.5	
TIL119	300	10	1.5	

Device Type	Current Transfer Ratio		Electrical Isolation Ratings kV	Package	Key Features
	Minimum %	α IF mA			
4N22*	25	10	1.0	B	JEDEC Version of TIL102
4N23*	60	10	1.0	B	JEDEC Version of TIL102A
4N24*	100	10	1.0	B	JEDEC Version of TIL103
TIL102	25	10	1.0	B	Hermetically sealed, Hi-Rel Coupler
TIL103†	100	10	1.0	B	High Gain Phototransistor Output
TIL107	4	15	1.0	C	Hermetically sealed package
TIL108	10	15	1.0	C	Hermetically sealed package
TIXL109	4	35	5.0	D	High input-to-output Voltage Isolation

* JAN versions available.

† UK Defence Standard available.