

TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π -MOSIV)

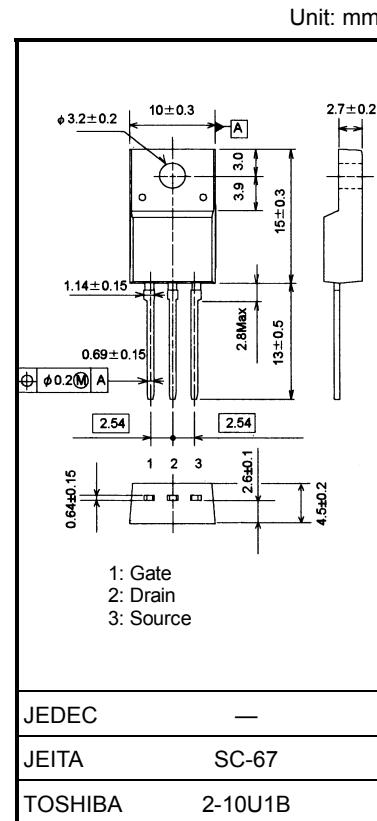
2SK3742

Switching Regulator Applications

- Low drain-source ON resistance: $R_{DS(ON)} = 2.2 \Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 3.5 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = 100 \mu\text{A}$ ($V_{DS} = 720 \text{ V}$)
- Enhancement model: $V_{th} = 4.0 \sim 5.0 \text{ V}$ ($V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$)

Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristic	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	900	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)	V_{DGR}	900	V
Gate-source voltage	V_{GSS}	± 30	V
Drain current	DC (Note 1)	I_D	5
	Pulse ($t = 1 \text{ ms}$) (Note 1)	I_{DP}	15
Drain power dissipation ($T_c = 25^\circ\text{C}$)	P_D	45	W
Single pulse avalanche energy (Note 2)	E_{AS}	595	mJ
Avalanche current	I_{AR}	5	A
Repetitive avalanche energy (Note 3)	E_{AR}	4.5	mJ
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature range	T_{stg}	-55~150	$^\circ\text{C}$



Weight: 1.7 g (typ.)

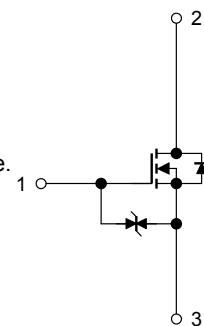
Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	$R_{th}(\text{ch-c})$	2.78	$^\circ\text{C/W}$
Thermal resistance, channel to ambient	$R_{th}(\text{ch-a})$	62.5	$^\circ\text{C/W}$

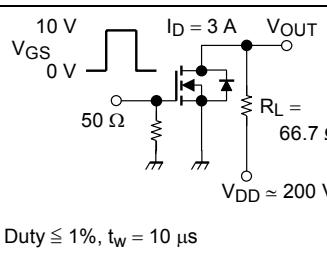
Note 1: Ensure that the channel temperature does not exceed 150°C during use of the device.Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^\circ\text{C}$ (initial), $L = 43.6 \text{ mH}$, $I_{AR} = 5.0 \text{ A}$, $R_G = 25 \Omega$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



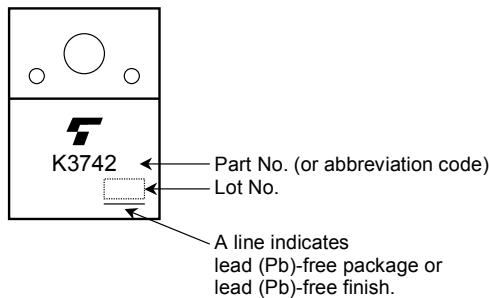
Electrical Characteristics (Ta = 25°C)

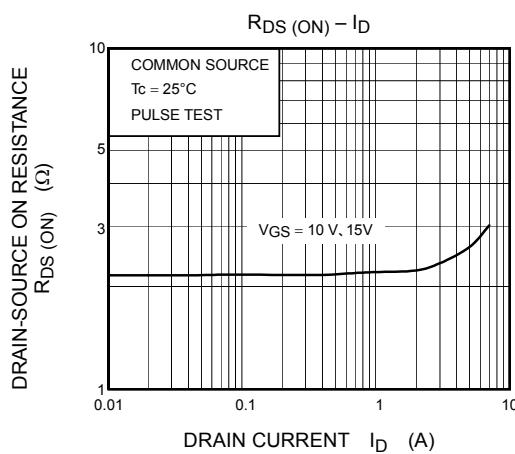
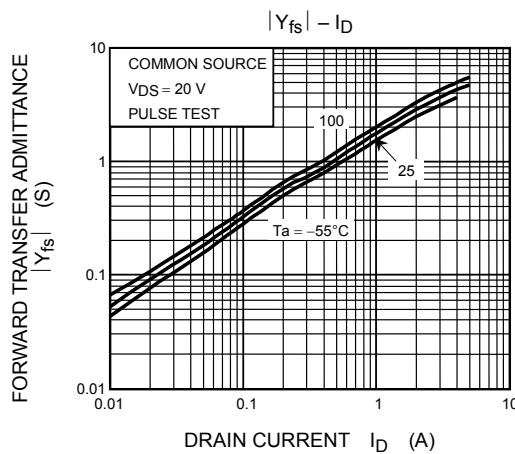
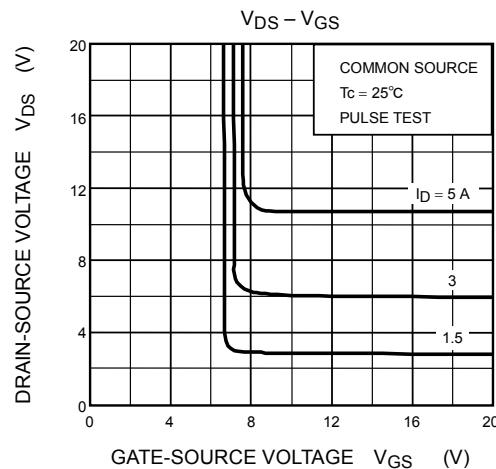
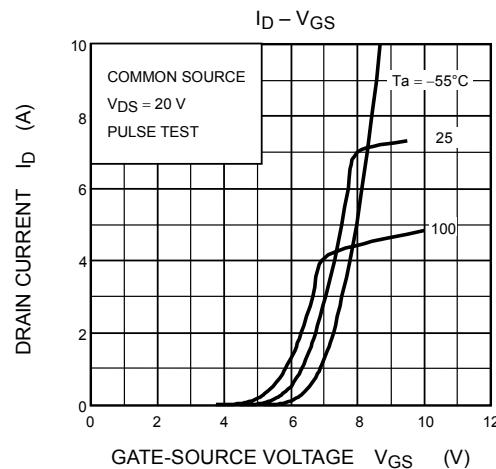
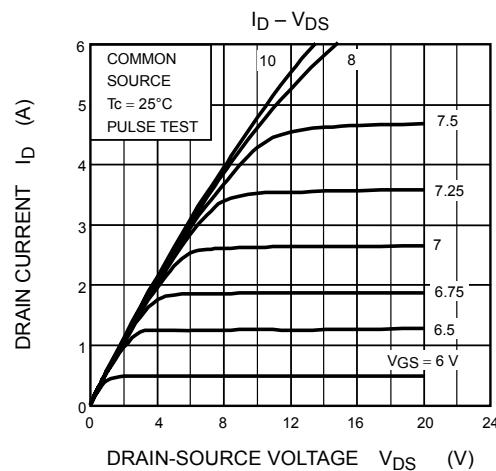
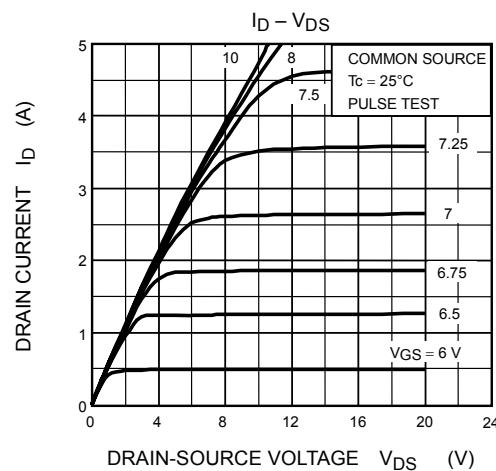
Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit	
Gate leakage current	I _{GSS}	V _{GS} = ±30 V, V _{DS} = 0 V	—	—	±10	μA	
Gate-source breakdown voltage	V _{(BR) GSS}	I _G = ±10 μA, V _{DS} = 0 V	±30	—	—	V	
Drain cutoff current	I _{DSS}	V _{DS} = 720 V, V _{GS} = 0 V	—	—	100	μA	
Drain-source breakdown voltage	V _{(BR) DSS}	I _D = 10 mA, V _{GS} = 0 V	900	—	—	V	
Gate threshold voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	4.0	—	5.0	V	
Drain-source ON resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 3 A	—	2.2	2.5	Ω	
Forward transfer admittance	Y _{fs}	V _{DS} = 20 V, I _D = 3 A	1.5	3.5	—	S	
Input capacitance	C _{iss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	—	1150	—	pF	
Reverse transfer capacitance	C _{rss}		—	20	—		
Output capacitance	C _{oss}		—	110	—		
Switching time	Rise time	t _r		—	100	—	ns
	Turn-on time	t _{on}		—	140	—	
	Fall time	t _f		—	40	—	
	Turn-off time	t _{off}		—	130	—	
Total gate charge	Q _g	V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 5 A	—	25	—	nC	
Gate-source charge	Q _{gs}		—	11	—		
Gate-drain charge	Q _{gd}		—	14	—		

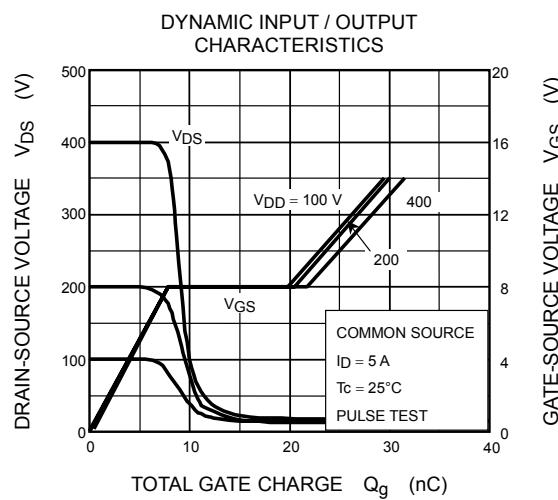
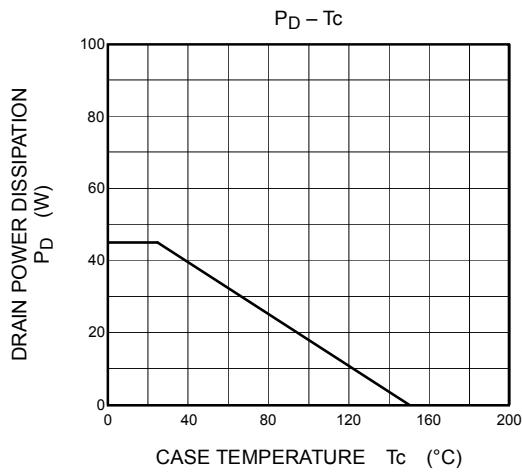
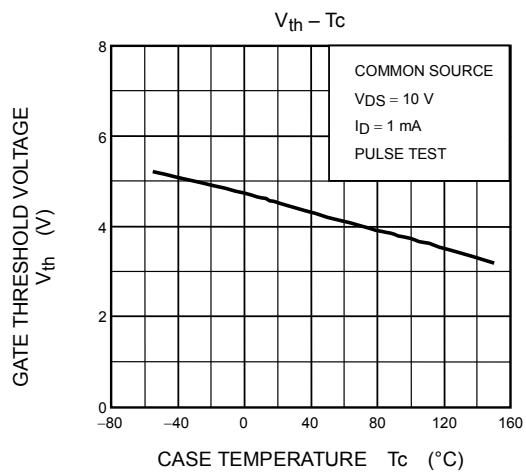
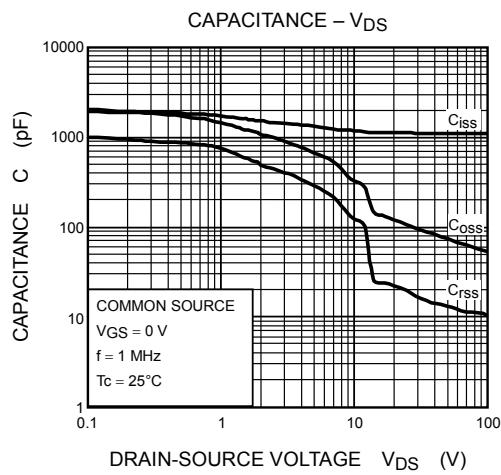
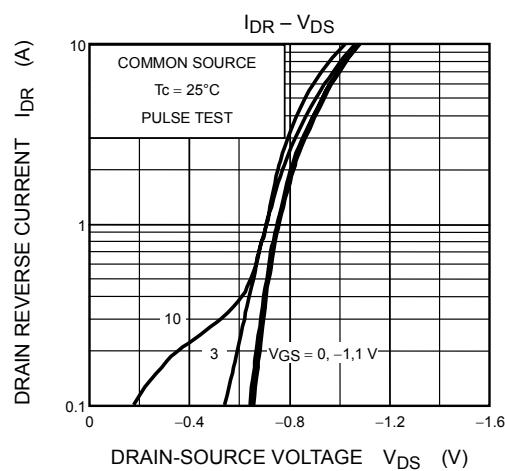
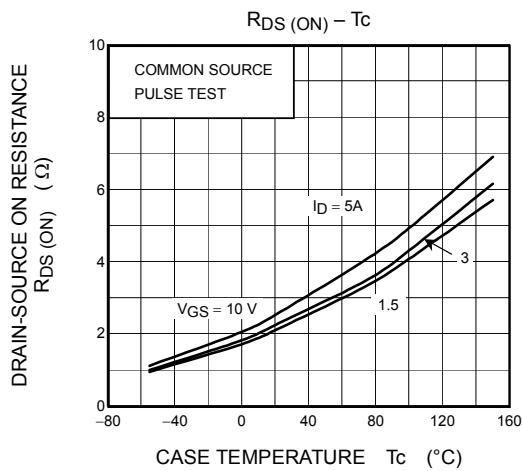
Source-Drain Ratings and Characteristics (Ta = 25°C)

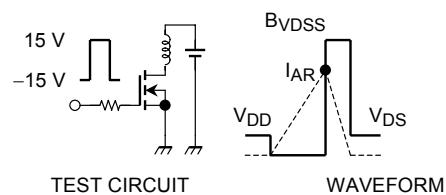
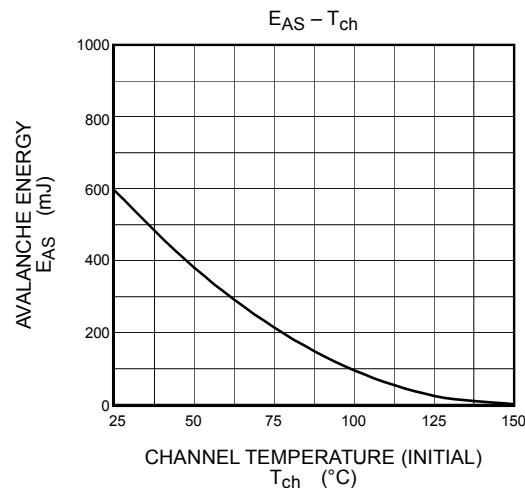
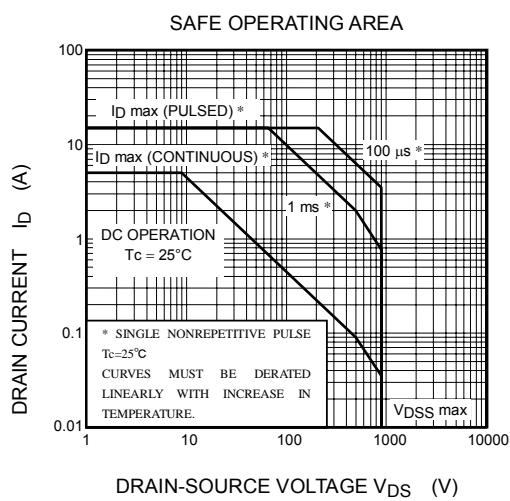
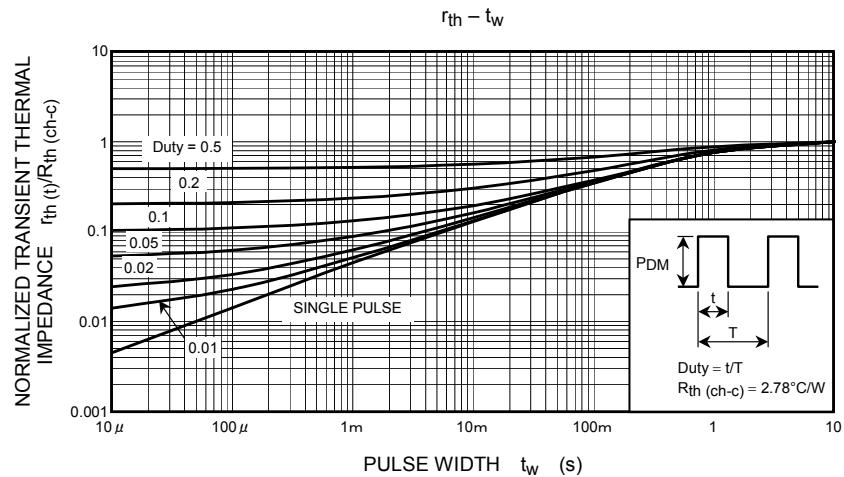
Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	—	—	5	A
Pulse drain reverse current (Note 1)	I _{DRP}	—	—	—	15	A
Forward voltage (diode)	V _{DSF}	I _{DR} = 5 A, V _{GS} = 0 V	—	—	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 5 A, V _{GS} = 0 V, dI _{DR} /dt = 100 A/μs	—	900	—	ns
Reverse recovery charge	Q _{rr}		—	5.4	—	μC

Marking









$$R_G = 25 \Omega$$

$$V_{DD} = 90 \text{ V}, L = 43.6 \text{ mH}$$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

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