

AN5025K

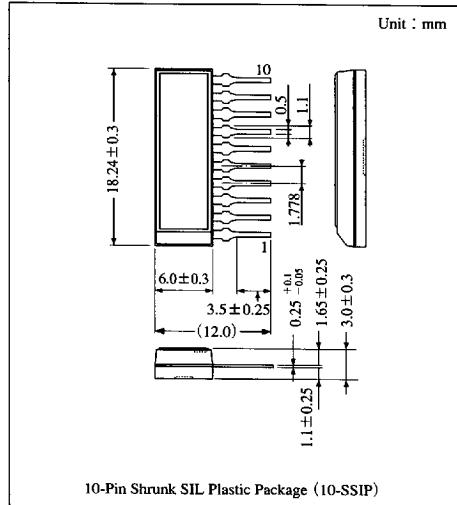
Remote Control Receiving IC

■ Overview

The AN5025K is an integrated circuit for the receiving preamplifier of the infrared remote control system, and consists of the first amplifier, limiter amplifier, BPF, signal waveform detector, and waveform shaping circuit.

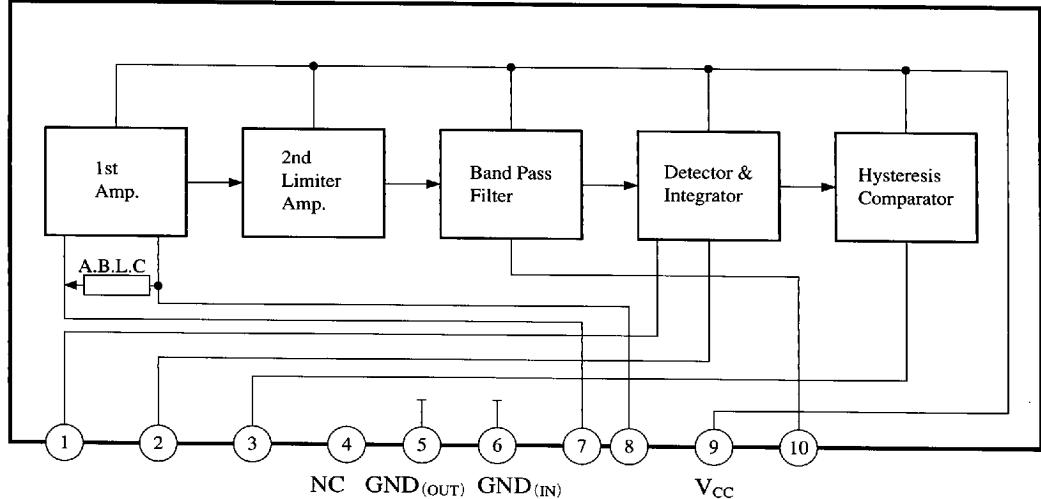
■ Features

- Band pass filter built-in
(tuning frequency can be changed from 30 to 60kHz by the external resistor.)
- TTL and CMOS can be connected directly to the output pin.
- Active high logic system.



ICs for
TV

■ Block Diagram



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■ Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit
Supply voltage	V _{CC}	6	V
	V ₁₋₆	0 to V ₉₋₆	
	V ₂₋₆	0.5 to V ₉₋₆	
	V ₇₋₆	0 to V ₉₋₆	
Circuit voltage	V ₈₋₆	0 to V ₉₋₆	V
	I _{CC}	10	
	I ₃	-0.05 to +2	
	I ₁₀	0 to 0.1	
Supply current	P _D	60	mW
Operating ambient temperature	T _{opr}	-20 to +70	°C
Storage temperature	T _{stg}	-55 to +150	°C

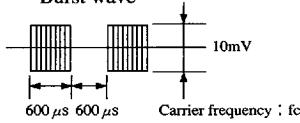
■ Recommended Operating Range (Ta=25°C)

Parameter	Symbol	Range
Operating supply voltage range	V _{CC}	4.5V to 5.5V

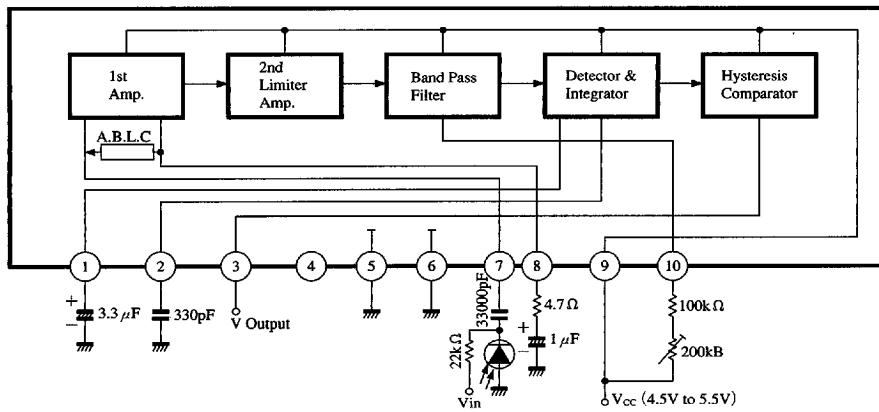
■ Electrical Characteristics (Ta=25°C)

Parameter	Symbol	Condition	min	typ	max	Unit
Circuit current	I ₉	V ₉₋₆ =5V	1	2	3	mA
Pin② voltage	V ₂₋₆	V ₁₋₆ =2.6V	0.6	0.8	1.2	V
Pin⑦ voltage	V ₇₋₆	V ₉₋₆ =5V	2.5	3	3.5	V
Output low level voltage ①	V ₃₋₆	V ₉₋₆ =5.5V, V ₂₋₆ =1.4V	-50	100	400	mV
Output high level voltage ②	V ₃₋₆	V ₉₋₆ =5.5V, V ₂₋₆ =3.4V	5.3	5.5	—	V
Voltage gain	A _v	v _{in} =20μV _{p-p} , f=56.9kHz	74	78	82	dB
Detection sensitivity	v _{det}	f ₀ =56.9kHz, f=f ₀ ±6.5kHz, Burst wave *	—	0.1	1.5	V

* Burst wave



■ Application Circuit

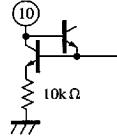


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■ Pin Descriptions

Pin No.	Pin name	Typ. waveform	Description	I/O impedance	Equivalent circuit
1	Detecting capacitor pin		To detect a signal, smooth a signal, make the DC level, and set the slice level.	6kΩ	
2	Integral capacitor pin		Capacitor pin converting the integrated current to the voltage for AM-detection of signal. decrease capacitor→increase sensitivity.	<100kΩ	
3	Output pin		The pulse signal that the remote-controlled signal was AM-detected is output by 0 to Vcc.	20kΩ	
—	NC	—	NC	—	—
5	Output GND pin	—	GND pin on the output side. In pattern-layout, this pin should come closer to the external GND pins, Pin① and Pin②.	—	—
6	Input GND pin	—	GND pin on the input side. So, pattern-layout of this pin may come closer to the external parts GND pin of Pin⑦ and Pin⑧.	—	—
7	Input pin		Pin for converting the light received by the pin diode to voltage.	50kΩ	
8	1st Amp. gain setting pin		—pin of Op. Amp. that Pin⑧ assumed as +pin. The resistor of 1kΩ is built-in between this pin and Op. Amp output pin.	1kΩ	
9	Power supply pin	—	Power pin. The applied voltage can be used over a range of from 4.5 to 5.5V.	—	—

■ Pin Descriptions (cont.)

Pin No.	Pin name	Typ. waveform	Description	I/O impedance	Equivalent circuit
10	Tuning frequency setting pin	—	Pin for setting the tuning frequency of built-in BPF. The tuning frequency varies with the value of current flowing in this pin.	10kΩ	

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