

Clock Jitter Cleaner With Cascaded PLLs and Integrated 1.6 GHz VCO (LVPECL LVCMOS Outputs)

(ACTIVE) LMK04002BEVAL

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Key Document

- LMK040xx Evaluation Board User's Guide (Rev. B)

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Description

The LMK04002 Evaluation Board simplifies evaluation of the LMK04002B Precision Clock Conditioner with Dual PLLs and Integrated VCO. Configuring and controlling the board is accomplished using Texas Instruments' CodeLoader software, which can be downloaded from TI's website.

The CodeLoader software will run on a Windows 2000 or Windows XP PC. The CodeLoader software is used to program the internal registers of the LMK04002B device through a MICROWIRE™ interface.



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User guides (1)

Title	Abstract	Type	Size (KB)	Date	Views	TI Recommends
LMK040xx Evaluation Board User's Guide (Rev. B)		ZIP	5219	08 Jan 2015	108	✓

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Part Number	Name	Product Family
LMK04002	Low-Noise Clock Jitter Cleaner with Cascaded PLLs	Unknown

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