

## Low-Voltage Dual SPST Analog Switch

## DESCRIPTION

The DG9432, DG9433, DG9434 is a dual single-pole/single-throw monolithic CMOS analog switch designed for high performance switching of analog signals. Combining low power, high speed ( $t_{ON}$ : 25 ns,  $t_{OFF}$ : 20 ns), the DG9432, DG9433, DG9434 is ideal for portable and battery powered applications requiring high performance and efficient use of board space.

The DG9432, DG9433, DG9434 is built on Vishay Siliconix's low voltage BCD-15 process. An epitaxial layer prevents latchup. Break-before-make is guaranteed for DG9432, DG9433, DG9434.

Each switch conducts equally well in both directions when on, and blocks up to the power supply level when off.

## FEATURES

- Wide operation voltage (+ 2.7 V to + 12 V)
- Low charge injection -  $Q_{INJ}$ : 1 pC
- Low power consumption
- TTL/CMOS logic compatible over the full operating voltage range
- Available in MSOP-8 and SOT23-8
- **Compliant to RoHS Directive 2002/95/EC**



RoHS  
COMPLIANT

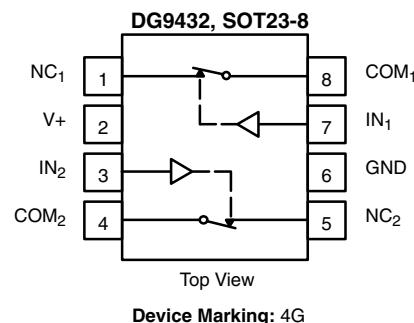
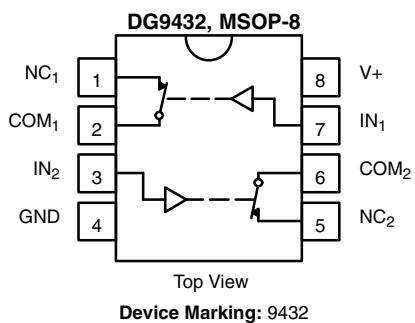
## BENEFITS

- Reduced power consumption
- Simple logic interface
- High accuracy
- Reduce board space

## APPLICATIONS

- Battery operated systems
- Portable test equipment
- Sample and hold circuits
- Cellular phones
- Communication systems
- Military radio
- PBX, PABX guidance and control systems

**FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION** - DG9432



## TRUTH TABLE DG9432

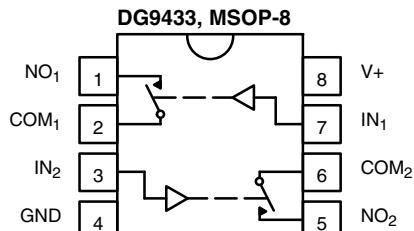
Logic	Switch
0	On
1	Off

# DG9432, DG9433, DG9434

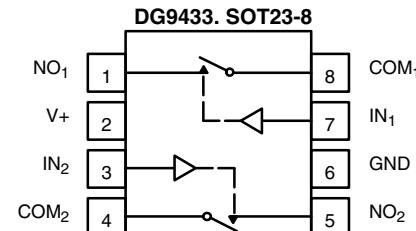
Vishay Siliconix



## FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION - DG9433/DG9434



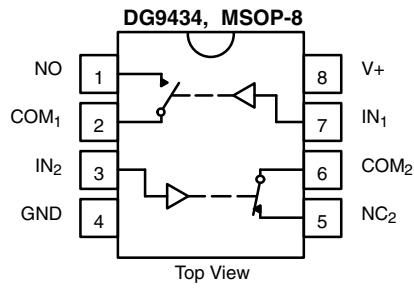
Top View  
Device Marking: 9433



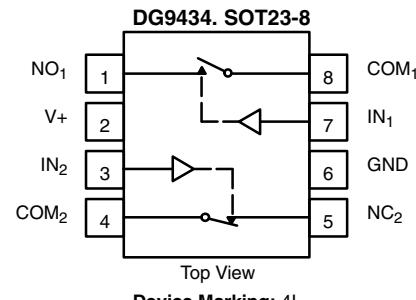
Top View  
Device Marking: 4H

### TRUTH TABLE DG9433

Logic	Switch
0	Off
1	On



Top View  
Device Marking: 9434



Top View  
Device Marking: 4I

### TRUTH TABLE DG9434

Logic	Switch-1	Switch-2
0	Off	On
1	On	Off

### ORDERING INFORMATION

Temp. Range	Package	Part Number
- 40 °C to 85 °C	MSOP-8	DG9432DQ-T1-E3
		DG9433DQ-T1-E3
		DG9434DQ-T1-E3
	SOT23-8	DG9432DS-T1-E3
		DG9433DS-T1-E3
		DG9433DS-T1-E3

ABSOLUTE MAXIMUM RATINGS				
Parameter	Limit		Unit	
Reference V+ to GND	- 0.3 to + 13.5		V	
IN, COM, NC, NO <sup>a</sup>	- 0.3 to (V+ + 0.3)			
Continuous Current (Any terminal)	± 10		mA	
Peak Current (Pulsed at 1 ms, 10 % duty cycle)	± 20			
Storage Temperature (D suffix)	- 65 to 150		°C	
Power Dissipation (Packages) <sup>b</sup>	MSOP-8 <sup>c</sup>	320		
	SOT23-8 <sup>c</sup>	515		

## Notes:

a. Signals on S<sub>X</sub>, D<sub>X</sub>, or IN<sub>X</sub> exceeding V+ or V- will be clamped by internal diodes. Limit forward diode current to maximum current ratings.  
b. All leads welded or soldered to PC board.  
c. Derate 6.5 mW/°C above 75 °C.

SPECIFICATIONS V+ = 3 V							
Parameter	Symbol	Test Conditions Otherwise Unless Specified V+ = 3.3 V, ± 10 %, V <sub>IN</sub> = 0.4 V or 1.8 V <sup>e</sup>	Temp. <sup>a</sup>	Limits - 40 °C °C to 85 °C			Unit
				Min. <sup>c</sup>	Typ. <sup>b</sup>	Max. <sup>c</sup>	
Switch On Resistance							
Analog Signal Range <sup>e</sup>	V <sub>ANALOG</sub>		Full	V-		V+	V
Drain-Source On- Resistance	R <sub>(on)</sub>	V+ = 2.7 V, I <sub>COM</sub> = 1 mA, V <sub>COM</sub> = 1.5 V	Room		81	100	Ω
R <sub>ON</sub> Match <sup>d</sup>	ΔR <sub>on</sub>		Full		120		
Digital Control							
Input, High Voltage	V <sub>INH</sub>	V+ Ranges 2.7 to 5 V	Full	1.8			V
Input, Low Voltage	V <sub>INL</sub>		Full			0.4	
Input Current	I <sub>INH</sub>			- 1		1	μA
Dynamic Characteristics							
Break-Before-Make <sup>d,g</sup>	t <sub>OPEN</sub>	V+ = 3 V, R <sub>L</sub> = 300 Ω V <sub>NO</sub> = V <sub>NC</sub> = 1.5 V C <sub>L</sub> = 35 pF, V <sub>IN</sub> = 0 V, 3 V	Room	1			
Turn-On Time <sup>d</sup>	t <sub>ON</sub>		Room	60	80	100	ns
Turn-Off Time <sup>d</sup>	t <sub>OFF</sub>		Room	14	25	35	
Charge Injection <sup>d</sup>	Q	C <sub>L</sub> = 1 nF, R <sub>GEN</sub> = 0 Ω, V <sub>g</sub> = 0 V	Room		0.16		pC
Off-Isolation <sup>d</sup>	OIRR	C <sub>L</sub> = 5 pF, R <sub>L</sub> = 50 Ω, f = 1 MHz	Room		77		dB
		C <sub>L</sub> = 5 pF, R <sub>L</sub> = 50 Ω, f = 10 MHz	Room		55		
Crosstalk <sup>d</sup>	X <sub>TALK</sub>	R <sub>L</sub> = 50 Ω, f = 1 MHz, V+ = 2.5 V	Room		98		
Source Off Capacitance <sup>d</sup>	C <sub>NO/NC(off)</sub>	f = 1 MHz, V <sub>NC/NO</sub> = 0 V	Room		7.5		pF
Drain Off Capacitance <sup>d</sup>	C <sub>COM(off)</sub>	f = 1 MHz V <sub>COM</sub> = 0 V	Room		7.8		
Drain On Capacitance <sup>d</sup>	C <sub>COM(on)</sub>		Room		22		
Supply Current	I <sub>+</sub>	V+ = 3.3 V, V <sub>IN</sub> = 0 or V+	Room	- 1		- 1	μA

## Notes:

a. Room = 25 °C, Full = as determined by the operating suffix.  
b. Typical values are for design aid only, not guaranteed nor subject to production testing.  
c. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this datasheet.  
d. Guarantee by design, not subjected to production test.  
e. V<sub>IN</sub> = input voltage to perform proper function.  
f. Guaranteed by 12 V leakage testing, not production tested.  
g. Applies for DG9434 only.

Parameter	Symbol	Test Conditions Otherwise Unless Specified $V_+ = 5 \text{ V}, \pm 10 \%, V_{IN} = 0.4 \text{ V or } 1.8 \text{ V}^e$	Temp. <sup>a</sup>	Limits			Unit
				- 40 °C to 85 °C			
<b>Switch On Resistance</b>							
Analog Signal Range <sup>e</sup>	$V_{ANALOG}$		Full	$V_-$		$V_+$	V
Drain-Source On-Resistance	$R_{(on)}$	$V_+ = 4.5 \text{ V}, I_{COM} = 1 \text{ mA}$ $V_{COM} = 2.5 \text{ V or } 3.5 \text{ V}$	Room Full		39	60 70	$\Omega$
$R_{DS(on)}$ Match	$\Delta R_{(on)}$	$V_+ = 4.5 \text{ V}, I_{COM} = 1 \text{ mA}, V_{COM} = 3.5 \text{ V}$	Room		0.3	3.0	
Switch Off Leakage Current <sup>f</sup>	$I_{NC/NO(off)}$	$V_+ = 5 \text{ V}, V_{COM} = 0.5 \text{ V, } 4.5 \text{ V}$ $V_{NC/NO} = 4.5 \text{ V, } 0.5 \text{ V}$	Room Full	- 1 - 10	0.3	1 10	nA
	$I_{COM(off)}$		Room Full	- 1 - 10	0.3	1 10	
Channel On Leakage Current <sup>f</sup>	$I_{COM(on)}$		Room Full	- 1 - 10	0.3	1 10	
<b>Digital Control</b>							
Input, High Voltage	$V_{INH}$	$V_+$ Ranges 2.7 to 5 V	Full	1.8			V
Input, Low Voltage	$V_{INL}$		Full			0.4	
Input Current	$I_{INH}$				- 1		1 $\mu\text{A}$
<b>Dynamic Characteristics</b>							
Break-Before-Make <sup>d,g</sup>	$t_{OPEN}$	$V_+ = 5 \text{ V}, R_L = 300 \Omega$ $V_{NO} = V_{NC} = 3 \text{ V}$ $C_L = 35 \text{ pF}, V_{IN} = 0 \text{ V, } 5 \text{ V}$	Room Full	1			ns
Turn-On Time	$t_{ON}$		Room Full		33	60 70	
Turn-Off Time	$t_{OFF}$		Room Full		10	20 30	
Charge Injection <sup>d</sup>	Q	$C_L = 1 \text{ nF}, R_{GEN} = 0 \Omega, V_g = 0 \text{ V}$	Room		0.56		pC
Off-Isolation <sup>d</sup>	OIRR	$C_L = 5 \text{ pF}, R_L = 50 \Omega, f = 1 \text{ MHz}$	Room		76		dB
		$C_L = 5 \text{ pF}, R_L = 50 \Omega, f = 10 \text{ MHz, } V_+ = 5 \text{ V}$	Room		54		
Crosstalk <sup>d</sup>	$X_{TALK}$	$R_L = 50 \Omega, f = 1 \text{ MHz, } V_+ = 5 \text{ V}$	Room		96		
Source Off Capacitance <sup>d</sup>	$C_{NC/NO(off)}$	$f = 1 \text{ MHz, } V_{NC/NO} = 0 \text{ V}$	Room		7.5		pF
Drain Off Capacitance <sup>d</sup>	$C_{COM(off)}$	$f = 1 \text{ MHz, } V_{COM} = 0 \text{ V}$	Room		7.8		
Drain On Capacitance <sup>d</sup>	$C_{COM(on)}$		Room		22		
Supply Current	$I_+$	$V_+ = 5.5 \text{ V, } V_{IN} = 0 \text{ or } V_+$	Room	- 1		- 1	$\mu\text{A}$

## Notes:

- a. Room = 25 °C, Full = as determined by the operating suffix.
- b. Typical values are for design aid only, not guaranteed nor subject to production testing.
- c. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this datasheet.
- d. Guarantee by design, not subjected to production test.
- e.  $V_{IN}$  = input voltage to perform proper function.
- f. Guaranteed by 12 V leakage testing, not production tested.
- g. Applies for DG9434 only.

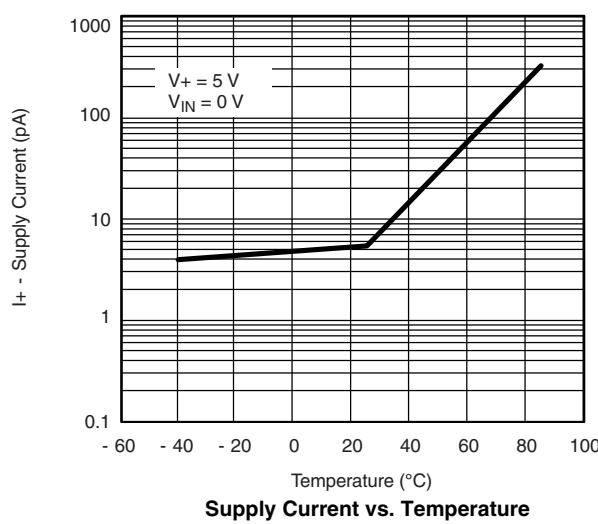
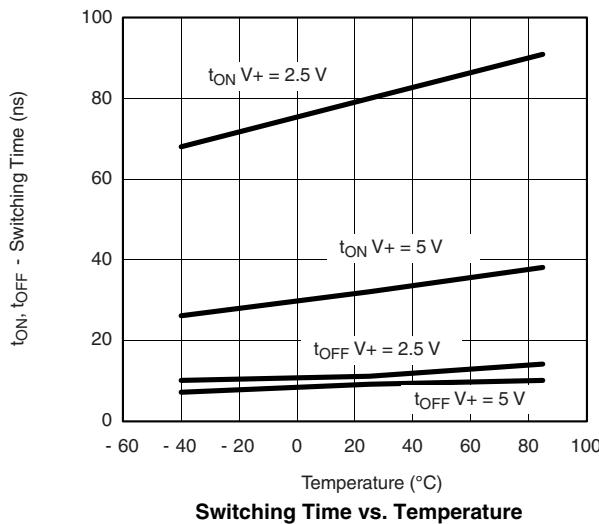
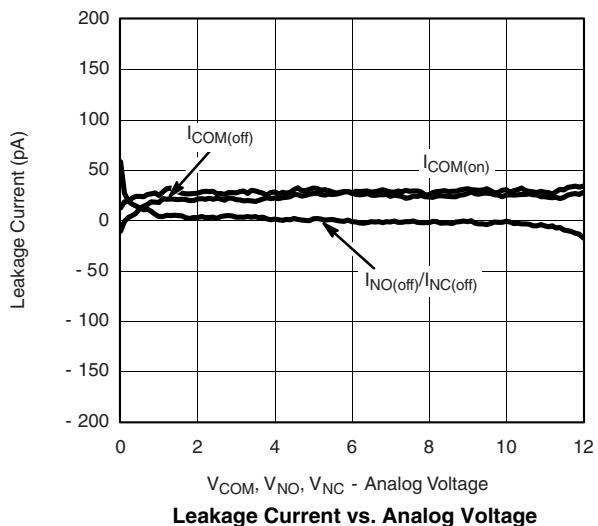
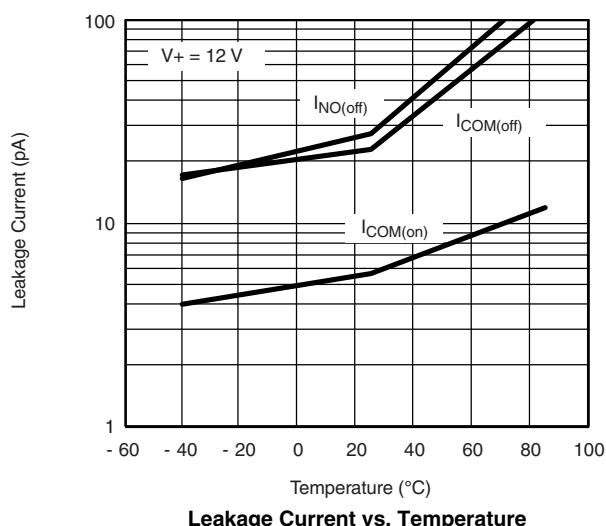
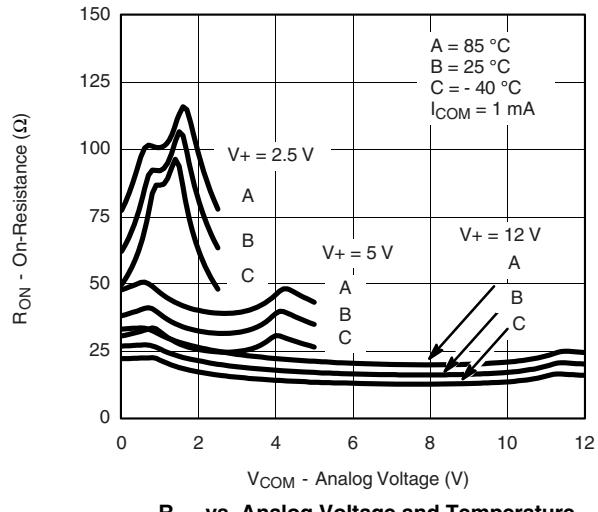
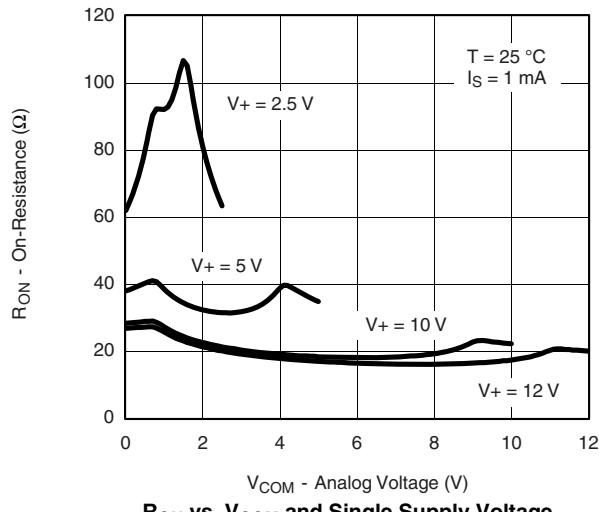
<b>SPECIFICATIONS</b> V <sub>+</sub> = 12 V							
Parameter	Symbol	Test Conditions Otherwise Unless Specified		Temp. <sup>a</sup>	Limits		
		V <sub>+</sub> = 12 V, ± 10 %, V <sub>IN</sub> = 0.8 V or 2.4 V <sup>e</sup>			Min. <sup>c</sup>	Typ. <sup>b</sup>	Max. <sup>c</sup>
<b>Switch On Resistance</b>							
Analog Signal Range <sup>e</sup>	V <sub>ANALOG</sub>			Full	V-		V+
Drain-Source On-Resistance	R <sub>(on)</sub>	V <sub>+</sub> = 10.8 V, I <sub>COM</sub> = 1 mA, V <sub>COM</sub> = 9 V	Room		19	30	Ω
R <sub>DS(on)</sub> Match	ΔR <sub>(on)</sub>		Full		0.3	3.0	
Switch Off Leakage Current <sup>a</sup>	I <sub>NC/NO(off)</sub>	V <sub>+</sub> = 12 V, V <sub>S</sub> = 1/11 V, V <sub>COM</sub> = 11/1 V	Room	- 1	0.3	1	nA
	I <sub>COM(off)</sub>		Full	- 10		10	
Channel On Leakage Current <sup>a</sup>	I <sub>COM(on)</sub>		Room	- 1	0.3	1	nA
			Full	- 10		10	
<b>Digital Control</b>							
Input, High Voltage	V <sub>INH</sub>	V <sub>+</sub> = 12 V	Full			2.4	V
Input, Low Voltage	V <sub>INL</sub>		Full	0.8			
Input Current	I <sub>INH</sub>				- 1	1	μA
<b>Dynamic Characteristics</b>							
Break-Before-Make <sup>d,g</sup>	t <sub>OPEN</sub>	V <sub>+</sub> = 12 V, R <sub>L</sub> = 300 Ω V <sub>NO</sub> = V <sub>NC</sub> = 8 V C <sub>L</sub> = 35 pF, V <sub>IN</sub> = 0 V, 12 V	Room	1			ns
Turn-On Time	t <sub>ON</sub>		Full		21	35	
Turn-Off Time	t <sub>OFF</sub>		Room		6	18	
Charge Injection <sup>d</sup>	Q	C <sub>L</sub> = 1 nF, R <sub>GEN</sub> = 0 Ω, V <sub>g</sub> = 0 V, V <sub>+</sub> = 5 V	Room		0.36		pC
Off-Isolation <sup>d</sup>	OIRR	C <sub>L</sub> = 5 pF, R <sub>L</sub> = 50 Ω, f = 1 MHz	Room		75		dB
		C <sub>L</sub> = 5 pF, R <sub>L</sub> = 50 Ω, f = 10 MHz	Room		53		
Crosstalk <sup>d</sup>	X <sub>TALK</sub>	R <sub>L</sub> = 50 Ω, f = 1 MHz, V <sub>+</sub> = 5 V	Room		96		
Source Off Capacitance <sup>d</sup>	C <sub>NO/NC(off)</sub>	f = 1 MHz, V <sub>NC/NO</sub> = 0 V	Room		7.5		pF
Drain Off Capacitance <sup>d</sup>	C <sub>COM(off)</sub>	f = 1 MHz, V <sub>COM</sub> = 0 V	Room		7.8		
Drain On Capacitance <sup>d</sup>	C <sub>COM(on)</sub>		Room		22		
Supply Current	I <sub>+</sub>	V <sub>+</sub> = 12 V, V <sub>IN</sub> = 0 or V <sub>+</sub>	Room	- 1		- 1	μA

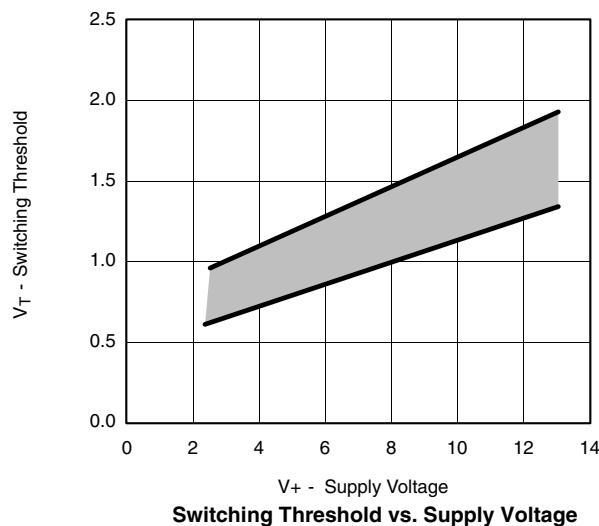
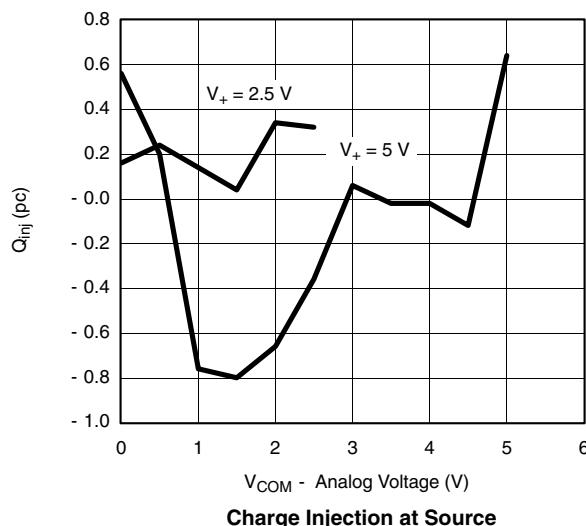
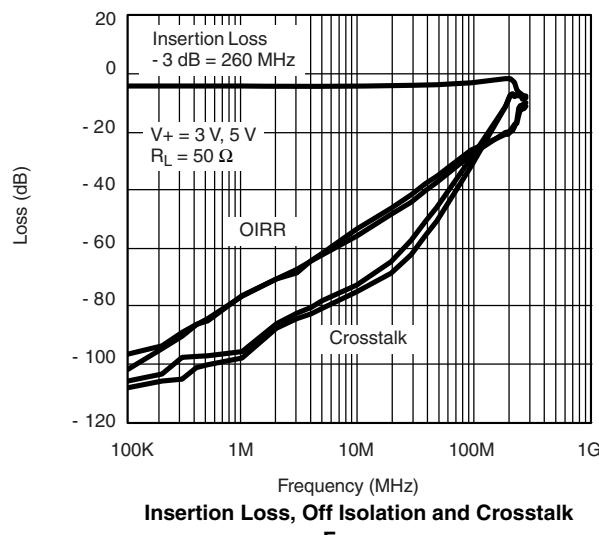
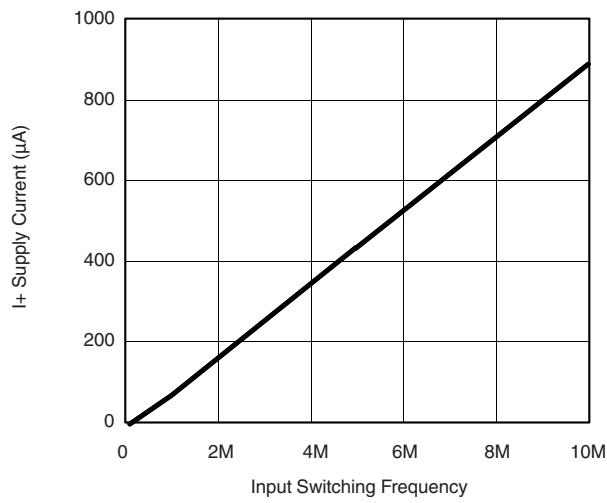
## Notes:

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- e. V<sub>IN</sub> = input voltage to perform proper function.
- f. Guaranteed by 12 V leakage testing, not production tested.
- g. Applies for DG9434 only.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**Switching Threshold vs. Supply Voltage**

**Charge Injection at Source**

**Insertion Loss, Off Isolation and Crosstalk vs. Frequency**

**Supply Current vs. Input Switching Frequency**

### TEST CIRCUITS

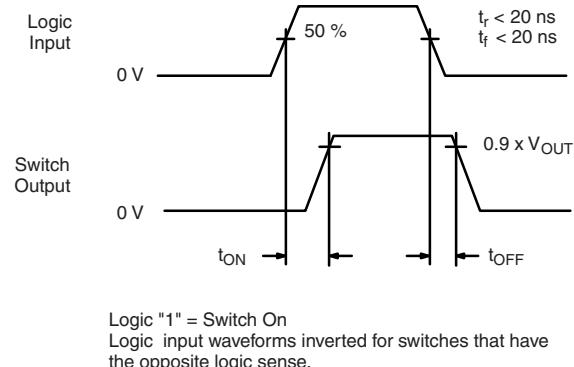
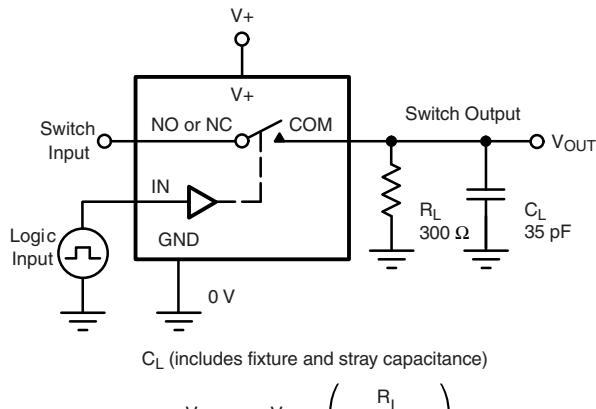


Figure 1. Switching Time

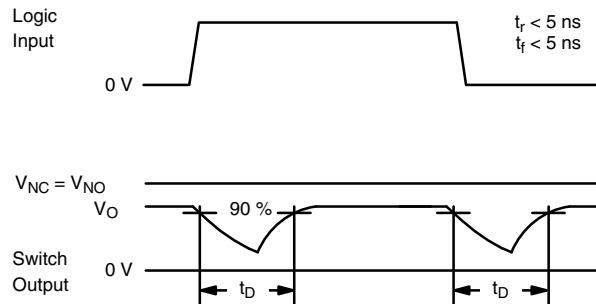
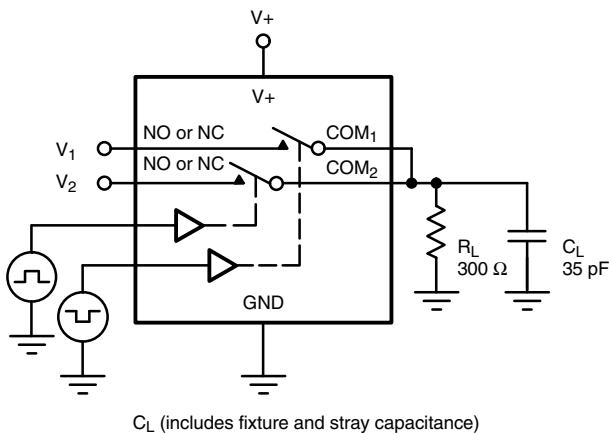
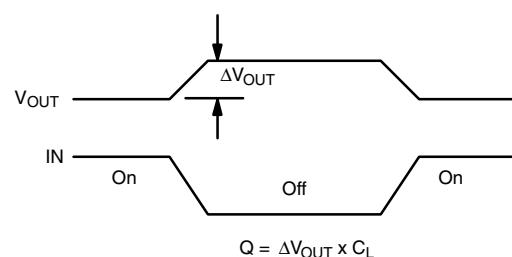
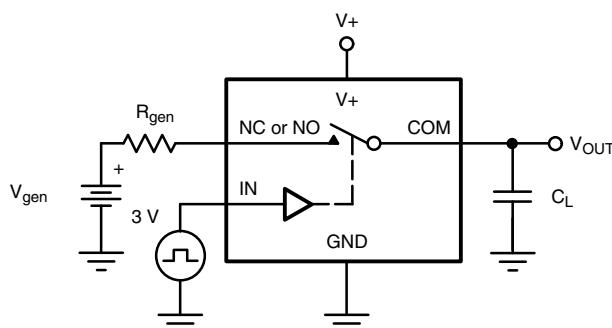
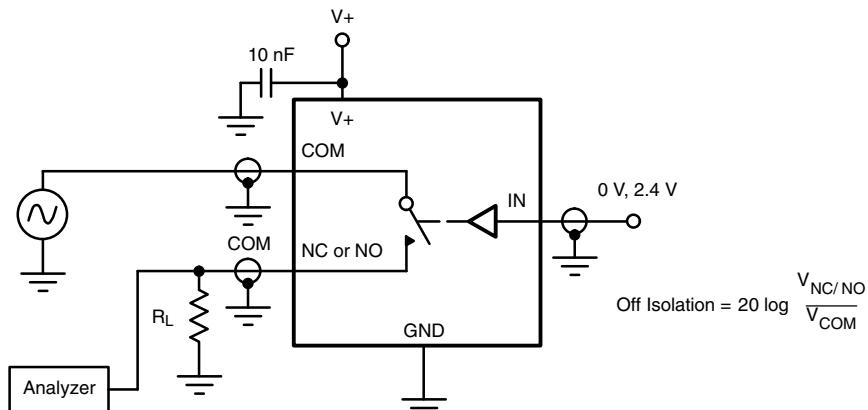
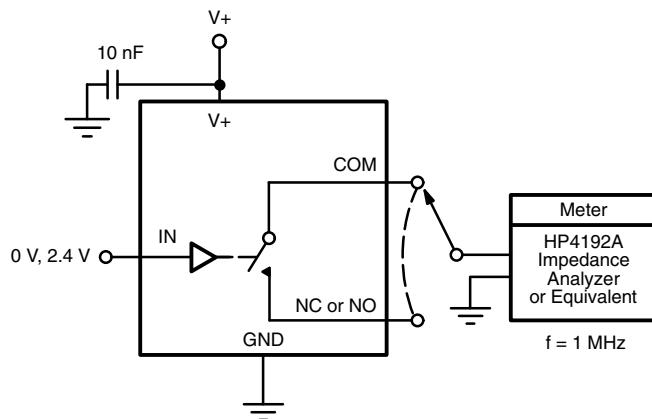


Figure 2. Break-Before-Make Interval



IN depends on switch configuration: input polarity determined by sense of switch.

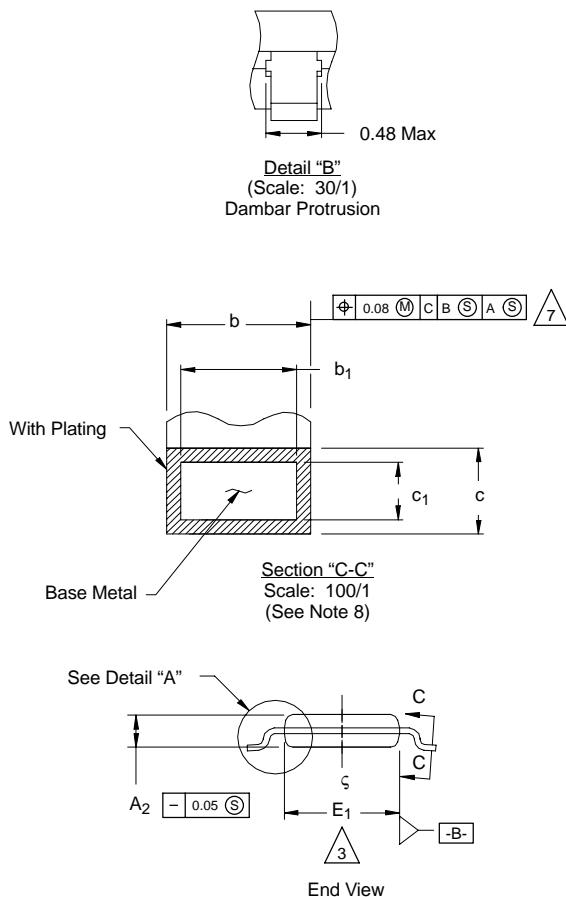
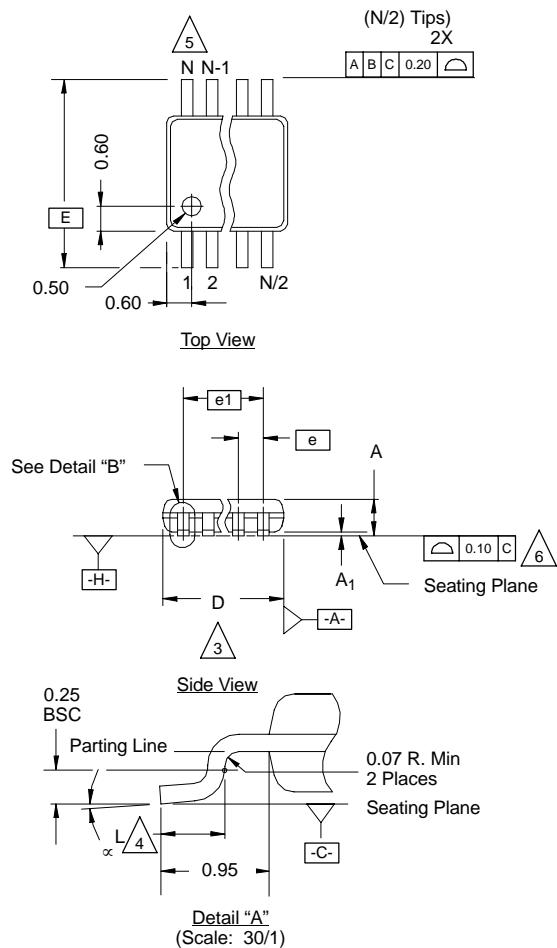
Figure 3. Charge Injection

**TEST CIRCUITS**

**Figure 4. Off-Isolation**

**Figure 5. Channel Off/On Capacitance**

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see [www.vishay.com/ppg?72311](http://www.vishay.com/ppg?72311).

### MSOP: 8-LEADS

JEDEC Part Number: MO-187, (Variation AA and BA)

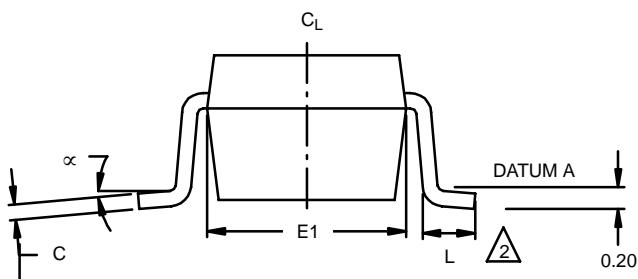
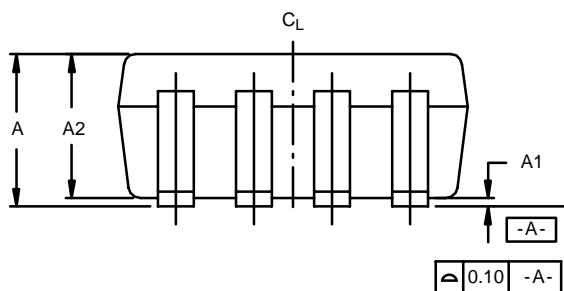
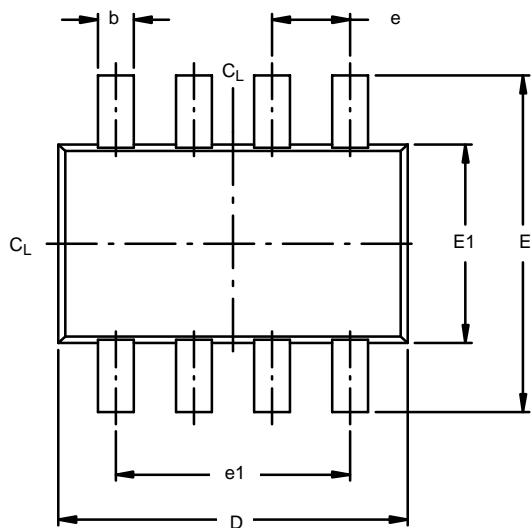


#### NOTES:

- Die thickness allowable is  $0.203 \pm 0.0127$ .
- Dimensioning and tolerances per ANSI.Y14.5M-1994.
- Dimensions "D" and "E<sub>1</sub>" do not include mold flash or protrusions, and are measured at Datum plane  $\text{[-H-]}$ , mold flash or protrusions shall not exceed 0.15 mm per side.
- Dimension is the length of terminal for soldering to a substrate.
- Terminal positions are shown for reference only.
- Formed leads shall be planar with respect to one another within 0.10 mm at seating plane.
- The lead width dimension does not include Dambar protrusion. Allowable Dambar protrusion shall be 0.08 mm total in excess of the lead width dimension at maximum material condition. Dambar cannot be located on the lower radius or the lead foot. Minimum space between protrusions and an adjacent lead to be 0.14 mm. See detail "B" and Section "C-C".
- Section "C-C" to be determined at 0.10 mm to 0.25 mm from the lead tip.
- Controlling dimension: millimeters.
- This part is compliant with JEDEC registration MO-187, variation AA and BA.
- Datums  $\text{[-A-]}$  and  $\text{[-B-]}$  to be determined Datum plane  $\text{[-H-]}$ .
- Exposed pad area in bottom side is the same as the leadframe pad size.

**N = 8L**

Dim	MILLIMETERS			Note
	Min	Nom	Max	
<b>A</b>	-	-	1.10	
<b>A<sub>1</sub></b>	0.05	0.10	0.15	
<b>A<sub>2</sub></b>	0.75	0.85	0.95	
<b>b</b>	0.25	-	0.38	8
<b>b<sub>1</sub></b>	0.25	0.30	0.33	8
<b>c</b>	0.13	-	0.23	
<b>c<sub>1</sub></b>	0.13	0.15	0.18	
<b>D</b>	3.00 BSC			3
<b>E</b>	4.90 BSC			
<b>E<sub>1</sub></b>	2.90	3.00	3.10	3
<b>e</b>	0.65 BSC			
<b>e<sub>1</sub></b>	1.95 BSC			
<b>L</b>	0.40	0.55	0.70	4
<b>N</b>	8			5
$\alpha$	0°	4°	6°	
ECN: T-02080—Rev. C, 15-Jul-02				
DWG: 5867				

**SOT-23 : 8-LEAD**


## NOTES:

1. All dimensions are in millimeters.
2. Foot length measured at intercept point between Datum A and lead surface.
3. Package outline exclusive of mold flash and metal burr.
4. Package outline inclusive of solder plating.
5. No molding flash allowed on the top and bottom lead surface.

Dim	MILLIMETERS			INCHES		
	Min	Nom	Max	Min	Nom	Max
<b>A</b>	0.90	1.27	1.45	0.035	0.05	0.057
<b>A1</b>	0.00	0.0762	0.15	0.000	0.003	0.006
<b>A2</b>	0.90	1.20	1.30	0.035	0.047	0.051
<b>b</b>	0.22	0.30	0.38	0.009	0.012	0.015
<b>C</b>	0.09	0.152	0.20	0.004	0.006	0.008
<b>D</b>	2.80	2.9	3.00	0.11	0.114	0.118
<b>E</b>	2.60	2.8	23.00	0.102	0.11	0.118
<b>E1</b>	1.50	1.65	1.75	0.059	0.065	0.069
<b>e</b>	0.65 REF			0.026 REF		
<b>e1</b>	1.95 REF			0.077 REF		
<b>L</b>	0.35	0.45	0.55	0.014	0.018	0.022
$\alpha$	0°	4°	8°	0°	4°	8°

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DWG: 5895

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# Mouser Electronics

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