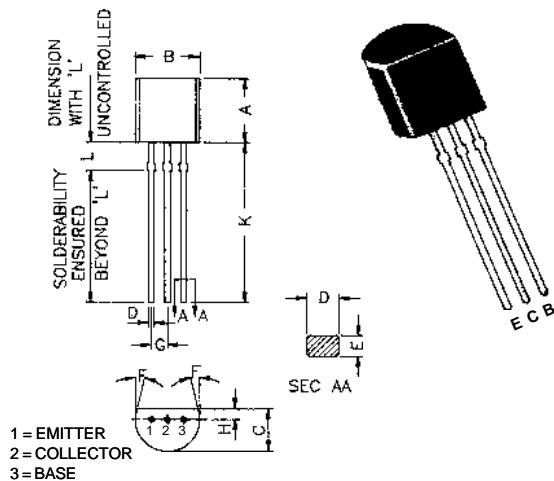


**TO-92 Plastic Package**

**CSA733**

**PNP SILICON PLANAR EPITAXIAL TRANSISTOR**

*Low Frequency Amplifier  
Complementary to CSC945*



DIM	MIN	MAX
A	4,32	5,33
B	4,45	5,20
C	3,18	4,19
D	0,41	0,55
E	0,35	0,50
F	5 DEG	
G	1,14	1,40
H	1,14	1,53
K	12,70	—
L	1,982	2,082

ALL DIMENSIONS IN M.M.

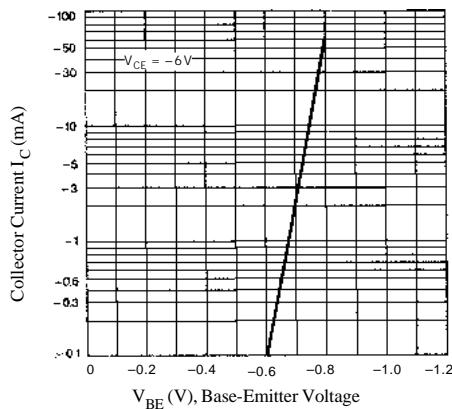
**ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-Base Voltage	$BV_{CBO}$	60	V
Collector-Emitter Voltage	$BV_{CEO}$	50	V
Emitter Base Voltage	$BV_{EBO}$	5	V
Collector Current (DC)	$I_C$	100	mA
Base Current (DC)	$I_B$	20	mA
Total Power Dissipation @ $T_{amb} = 25^\circ C$	$P_{tot}$	500	mW
Operating Storage Junction Temperature Range	$T_f, T_{stg}$	-55 to +150	°C

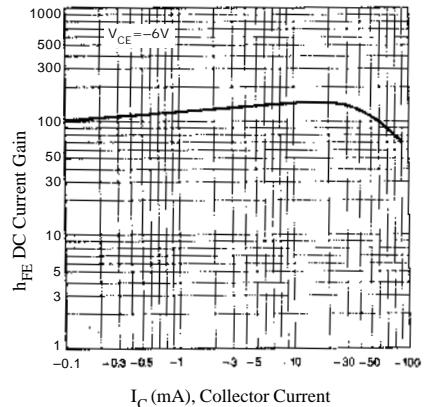
**ELECTRICAL CHARACTERISTICS** ( $T_a=25^\circ C$  unless otherwise specified)

<b>Characteristic</b>	<b>Symbol</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Unit</b>
Collector Emitter Breakdown Voltage $I_C=1mA, I_B=0$	$BV_{CEO}$	50	-	-	V
Collector Base Voltage $I_C=100\mu A, I_E=0$	$BV_{CBO}$	60	-	-	V
Emitter Base Voltage $I_E=10\mu A, I_C=0$	$BV_{EBO}$	5	-	-	V
Collector Cut-Off Current $V_{CB}=60V, I_E=0$	$I_{CBO}$	-	-	0.1	$\mu A$
Emitter Cut-Off Current $V_{EB}=5V, I_C=0$	$I_{EBO}$	-	-	0.1	$\mu A$
DC Current Gain $V_{CE}=6V, I_C=1mA$	$h_{FE}^*$	90	-	600	
Collector Emitter Saturation Voltage $I_C=100mA, I_B=10mA$	$V_{CE(sat)}$	-	-	0.3	V
Base Emitter On Voltage $I_C=1mA, V_{CE}=6V$	$V_{BE(on)}$	0.6	-	0.7	V
<b>DYNAMIC CHARACTERISTICS</b>					
Current-Gain Bandwidth Product $V_{CE}=6V, I_C=10mA$	$f_T$	100	-	-	MHz
Common Base Output Capacitance $V_{CB}=10V, I_E=0, f=1MHz$	$C_{ob}$	-	-	6	pF
Noise Figure $V_{CE}=6V, I_C=0.3mA$ $R_s=10k\Omega, f=100Hz$	NF	-	-	20	dB
<b><math>h_{FE}</math> CLASSIFICATION</b>	<b>R</b> <b>90-180</b>	<b>Q</b> <b>135=270</b>	<b>P</b> <b>200-400</b>	<b>K</b> <b>300-600</b>	

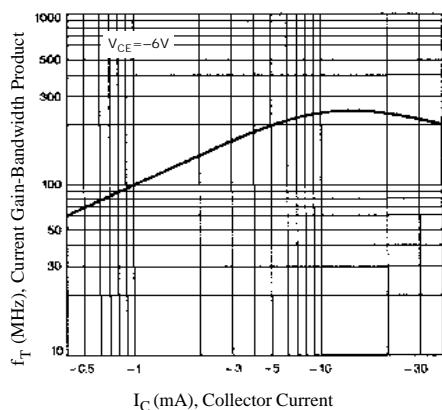
**Base Emitter On Voltage**



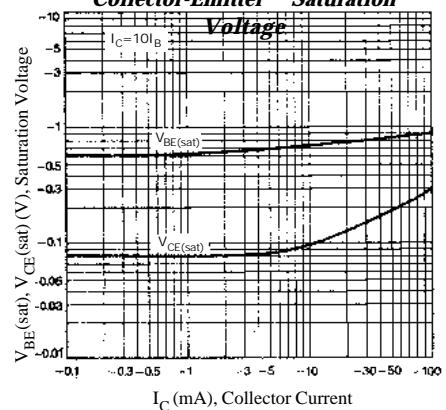
**DC Current Gain**



**Current Gain-Bandwidth Product**



**Base-Emitter Saturation Voltage  
Collector-Emitter Saturation**



## Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/ CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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