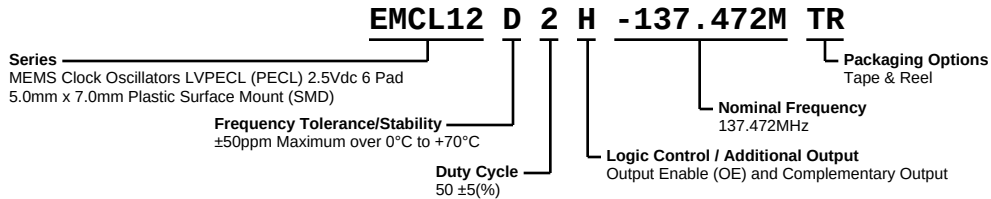


EMCL12D2H-137.472M TR



ELECTRICAL SPECIFICATIONS

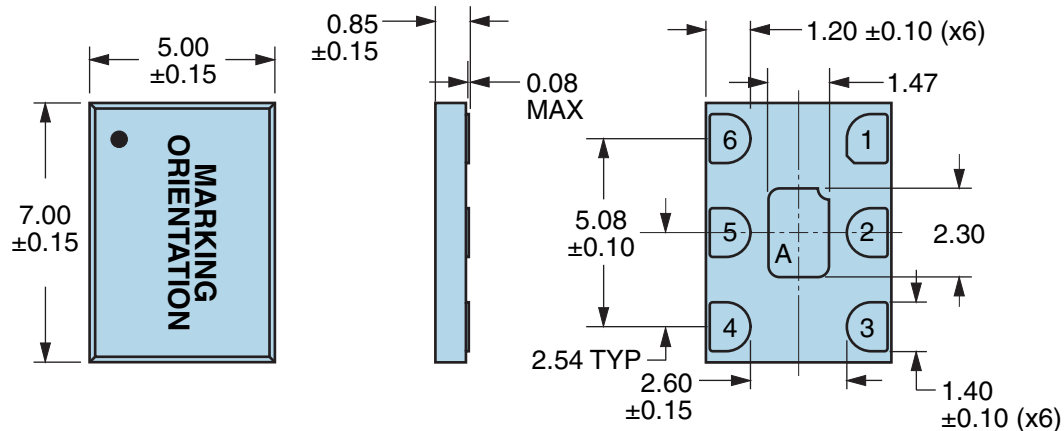
Nominal Frequency	137.472MHz
Frequency Tolerance/Stability	±50ppm Maximum over 0°C to +70°C (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C, Reflow, Shock, and Vibration)
Aging at 25°C	±1ppm First Year Maximum
Supply Voltage	+2.5Vdc ±0.125Vdc
Input Current	75mA Maximum (Excluding Load Termination Current)
Output Voltage Logic High (Voh)	Vdd-1.10Vdc Minimum, 1.60Vdc Typical, Vdd-0.70Vdc Maximum
Output Voltage Logic Low (Vol)	Vdd-2.0Vdc Minimum, 0.80Vdc Typical, Vdd-1.40Vdc Maximum
Rise/Fall Time	150pSec Typical, 300pSec Maximum (Measured over 20% to 80% of waveform)
Duty Cycle	50 ±5(%) (Measured at 50% of waveform)
Output Swing (VOpp)	600mVdc Minimum, 800mVdc Typical, 1000mVdc Maximum
Load Drive Capability	50 Ohms into Vdd-2.0Vdc
Output Logic Type	LVPECL
Logic Control / Additional Output	Output Enable (OE) and Complementary Output
Output Control Input Voltage	Vih of 70% of Vdd Minimum or No Connect to Enable Output and Complementary Output, Vil of 30% of Vdd Maximum to Disable Output and Complementary Output (High Impedance)
Output Enable Current	70mA Maximum (Without Load)
Period Jitter (Deterministic)	0.2pSec Typical
Period Jitter (Random)	2.0pSec Typical
Period Jitter (RMS)	1.5pSec Typical, 3.0pSec Maximum
Period Jitter (pk-pk)	20pSec Typical, 25pSec Maximum
RMS Phase Jitter (Fj = 637kHz to 10MHz; Random)	1.6pSec Typical
RMS Phase Jitter (Fj = 1MHz to 20MHz; Random)	1.0pSec Typical
RMS Phase Jitter (Fj = 1.875MHz to 20MHz; Random)	0.5pSec Typical
Start Up Time	10mSec Maximum
Storage Temperature Range	-55°C to +125°C

ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

ESD Susceptibility	MIL-STD-883, Method 3015, Class 2, HBM 2000V
Flammability	UL94-V0
Mechanical Shock	MIL-STD-883, Method 2002, Condition G, 30,000G
Moisture Resistance	MIL-STD-883, Method 1004
Moisture Sensitivity Level	J-STD-020, MSL 1
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K
Resistance to Solvents	MIL-STD-202, Method 215
Solderability	MIL-STD-883, Method 2003 (Six I/O Pads on bottom of package only)
Temperature Cycling	MIL-STD-883, Method 1010, Condition B
Thermal Shock	MIL-STD-883, Method 1011, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A, 20G

EMCL12D2H-137.472M TR

MECHANICAL DIMENSIONS (all dimensions in millimeters)

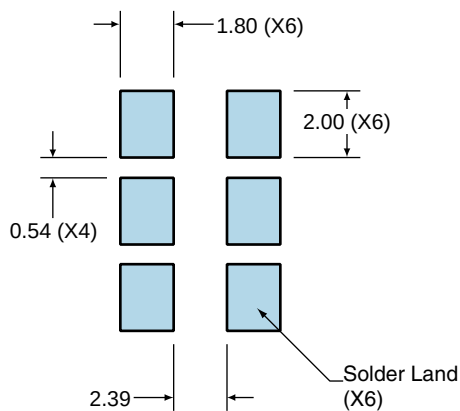


PIN	CONNECTION
1	Output Enable (OE)
2	No Connect
3	Case Ground
4	Output
5	Complementary Output
6	Supply Voltage

LINE	MARKING
1	XXXX or XXXXX XXXX or XXXXX=Ecliptek Manufacturing Identifier

Suggested Solder Pad Layout

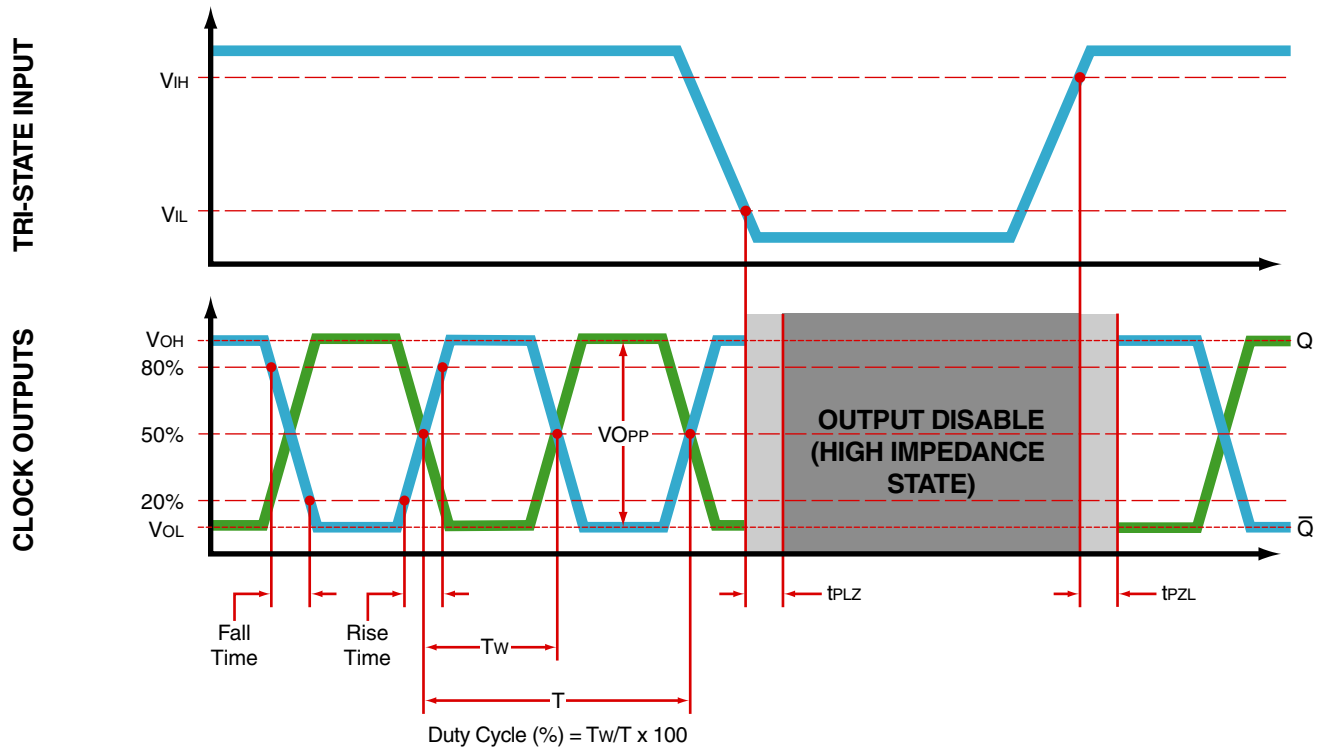
All Dimensions in Millimeters



All Tolerances are ±0.1

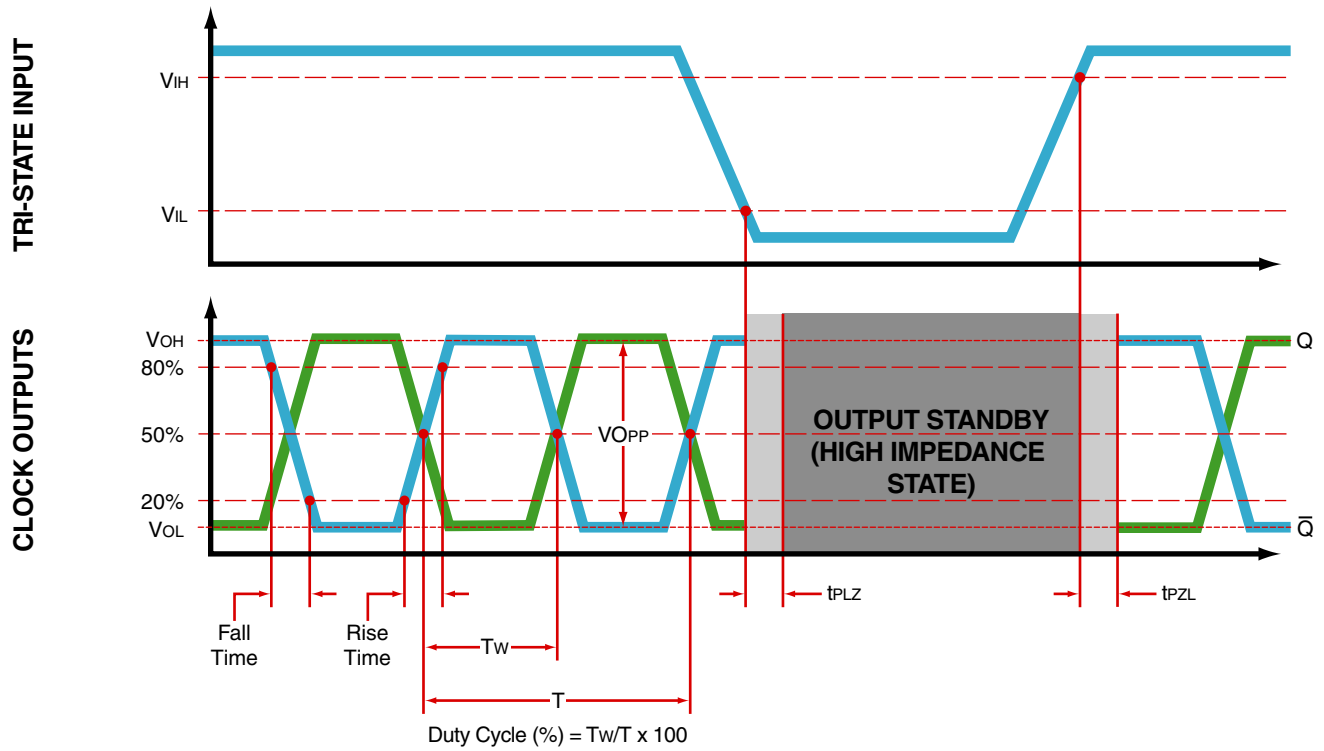
EMCL12D2H-137.472M TR

OUTPUT WAVEFORM & TIMING DIAGRAM



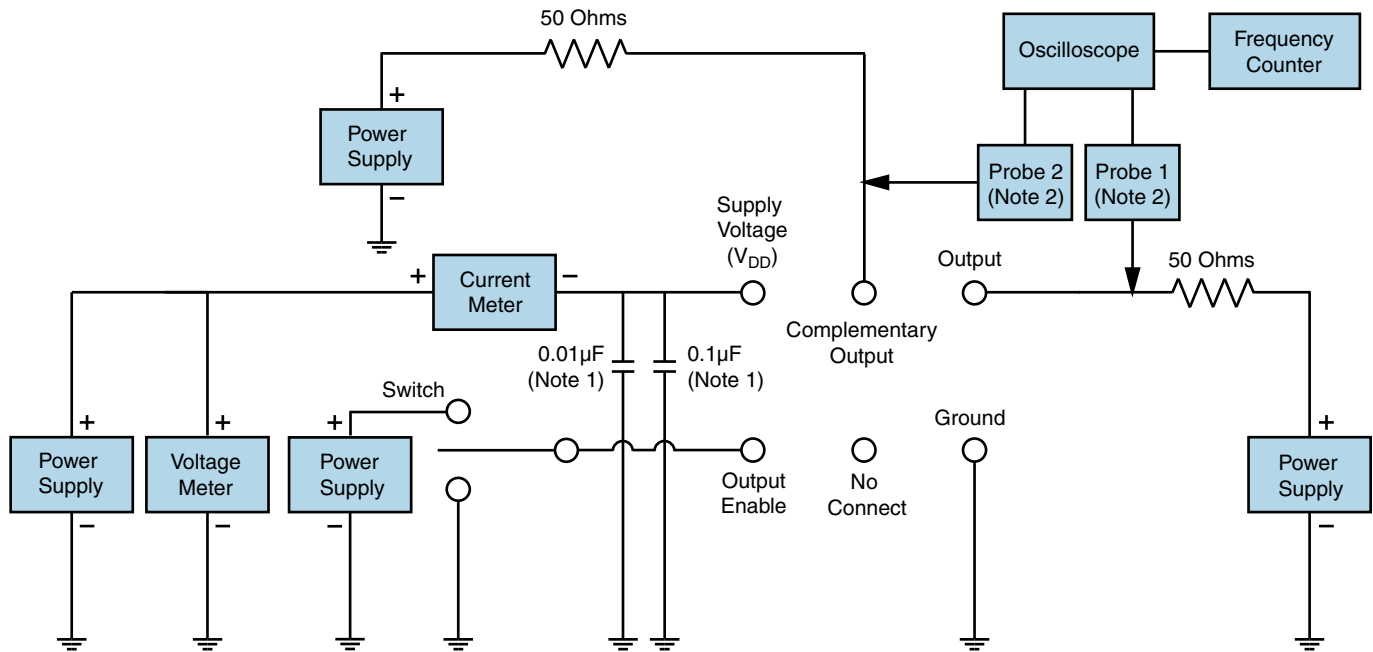
EMCL12D2H-137.472M TR

OUTPUT WAVEFORM & TIMING DIAGRAM



EMCL12D2H-137.472M TR

Test Circuit for Output Enable and Complementary Output



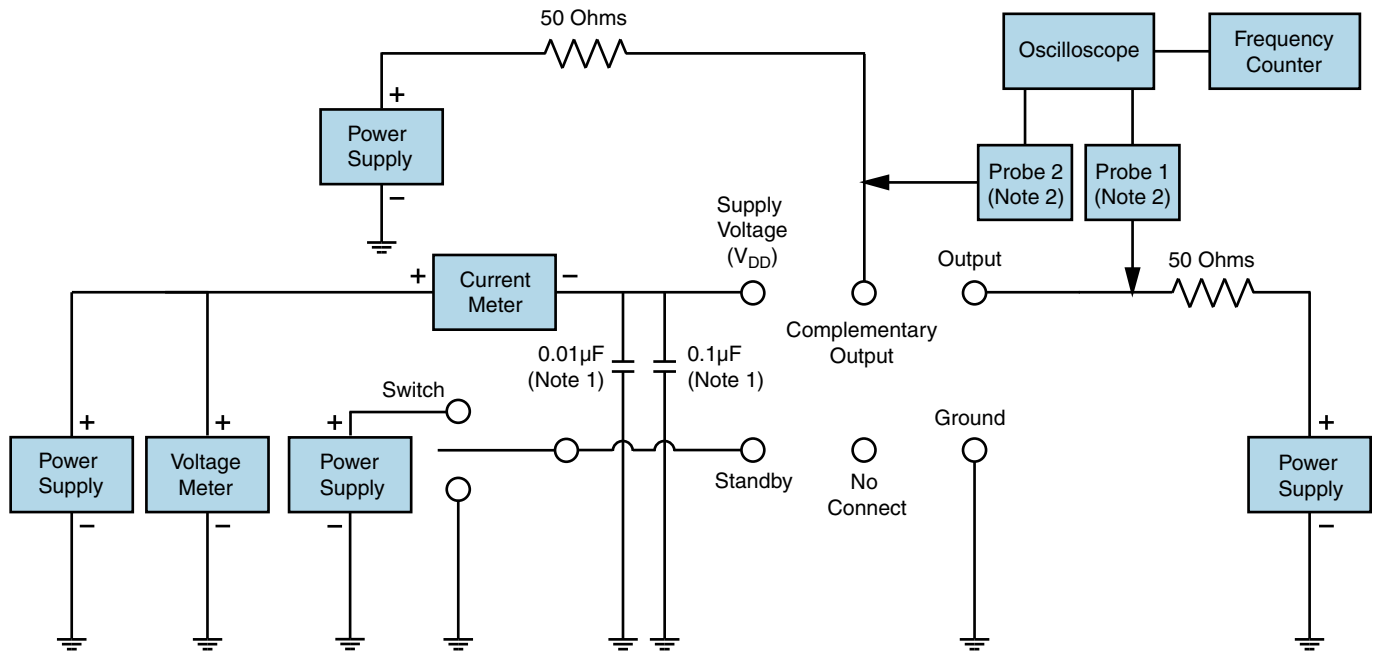
Note 1: An external 0.01µF ceramic bypass capacitor in parallel with a 0.1µF high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>500MHz) passive probe is recommended.

Note 3: Test circuit PCB traces need to be designed for a characteristic line impedance of 50 ohms.

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Test Circuit for Standby and Complementary Output



Note 1: An external $0.01\mu\text{F}$ ceramic bypass capacitor in parallel with a $0.1\mu\text{F}$ high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low capacitance ($<12\text{pF}$), 10X attenuation factor, high impedance ($>10\text{Mohms}$), and high bandwidth ($>500\text{MHz}$) passive probe is recommended.

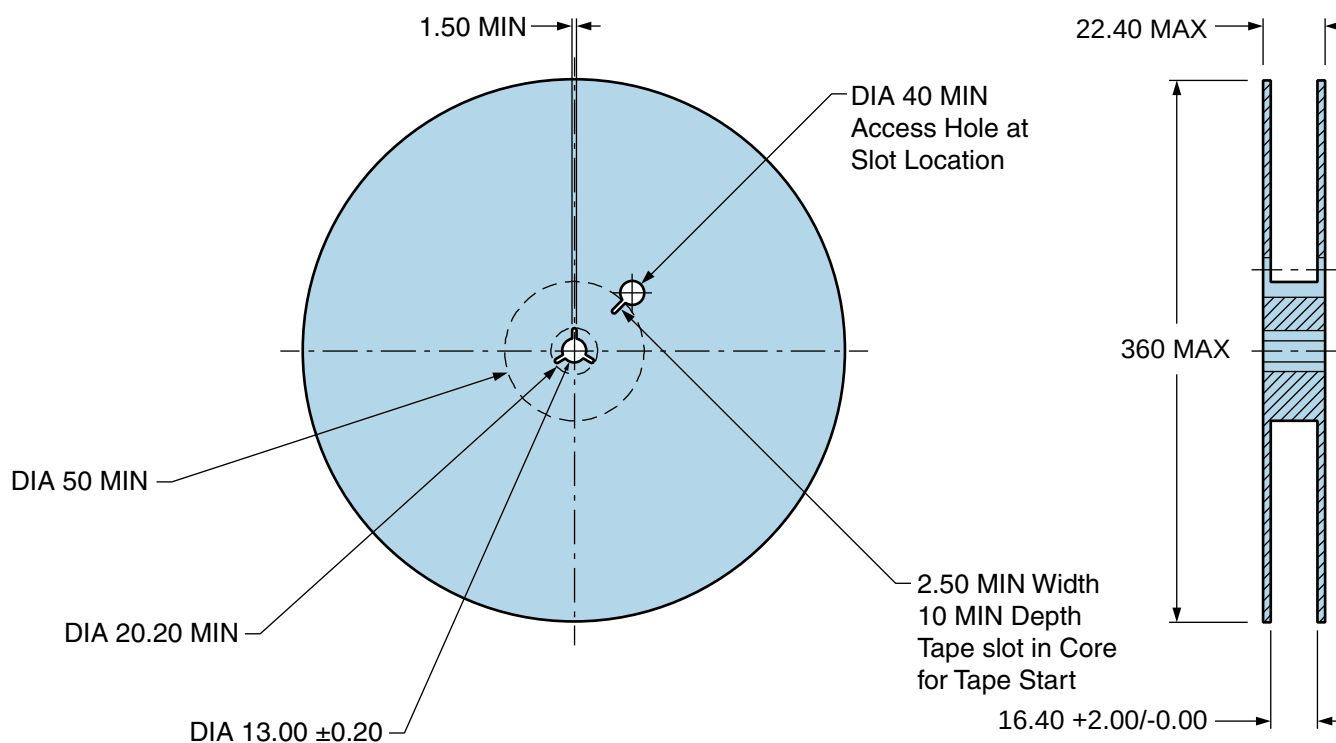
Note 3: Test circuit PCB traces need to be designed for a characteristic line impedance of 50 ohms.

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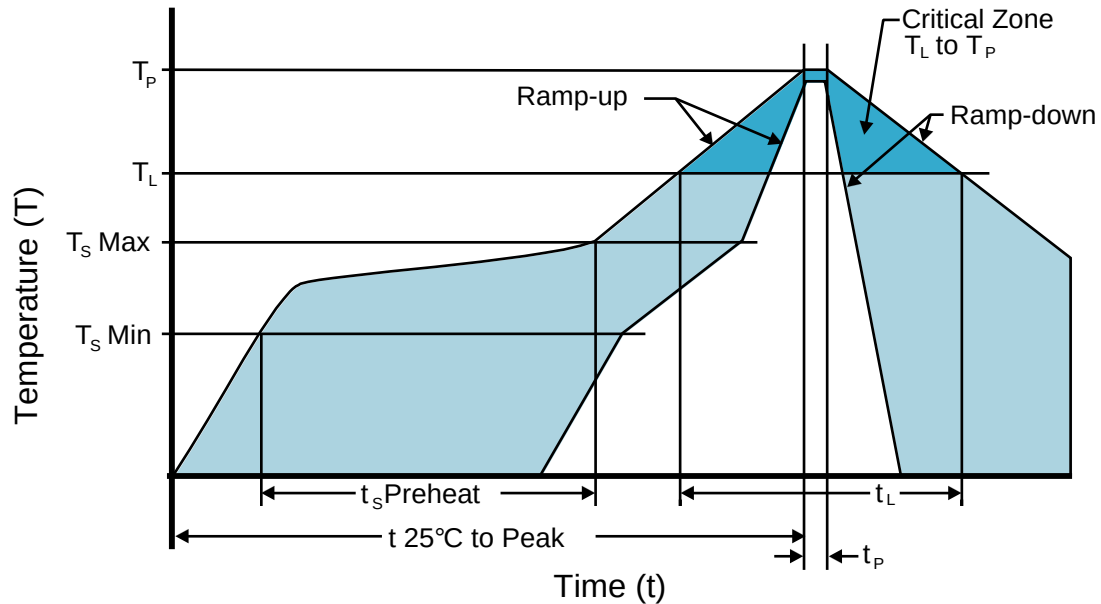
The technical drawing illustrates the dimensions and features of a film strip frame. The main view shows a rectangular frame with rounded ends, containing seven circular sprocket holes along the top edge and three larger rectangular openings along the bottom edge. A central marking area is labeled "MARKING ORIENTATION". Dimensions are provided in millimeters with tolerances:

- Total width: 16.00 ± 0.30
- Distance from top edge to first sprocket hole center: 7.50 ± 0.10
- Distance between sprocket hole centers: 4.00 ± 0.10 and 2.00 ± 0.10
- Sprocket hole diameter: DIA 1.50 +0.10/-0.00
- Minimum distance from sprocket hole to bottom edge: 1.50 MIN
- Distance between rectangular openings: 8.00 ± 0.10
- Distance from right edge to first rectangular opening: A0
- Frame height: B0
- Distance from top edge to bottom edge of rectangular openings: 1.75 ± 0.10
- Maximum thickness of the frame: 0.60 MAX
- Maximum thickness of the sprocket hole rim: 0.10 MAX
- Distance from right edge to the end of the frame: K0

A large arrow at the bottom indicates the "Direction of Unreeling".



Recommended Solder Reflow Methods



High Temperature Infrared/Convection

$T_s \text{ MAX}$ to T_L (Ramp-up Rate) 3°C/second Maximum

Preheat

- Temperature Minimum ($T_s \text{ MIN}$) 150°C
- Temperature Typical ($T_s \text{ TYP}$) 175°C
- Temperature Maximum ($T_s \text{ MAX}$) 200°C
- Time ($t_s \text{ MIN}$) 60 - 180 Seconds

Ramp-up Rate (T_L to T_p) 3°C/second Maximum

Time Maintained Above:

- Temperature (T_L) 217°C
- Time (t_L) 60 - 150 Seconds

Peak Temperature (T_p) 260°C Maximum for 10 Seconds Maximum

Target Peak Temperature ($T_p \text{ Target}$) 250°C +0/-5°C

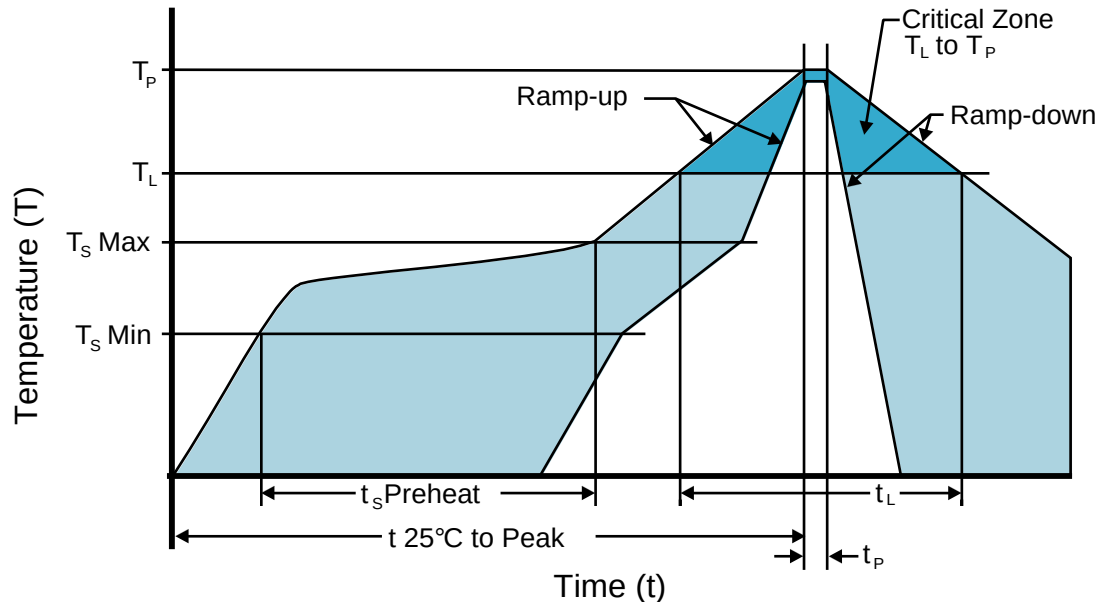
Time within 5°C of actual peak (t_p) 20 - 40 seconds

Ramp-down Rate 6°C/second Maximum

Time 25°C to Peak Temperature (t) 8 minutes Maximum

Moisture Sensitivity Level Level 1

Recommended Solder Reflow Methods



Low Temperature Infrared/Convection 240°C

T _s MAX to T _L (Ramp-up Rate)	5°C/second Maximum
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Preheat

- Temperature Minimum (T _s MIN)	N/A
- Temperature Typical (T _s TYP)	150°C
- Temperature Maximum (T _s MAX)	N/A
- Time (t _s MIN)	60 - 120 Seconds

Ramp-up Rate (T _L to T _P)	5°C/second Maximum
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Time Maintained Above:

- Temperature (T _L)	150°C
- Time (t _L)	200 Seconds Maximum

Peak Temperature (T _P)	240°C Maximum
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Target Peak Temperature (T _P Target)	240°C Maximum 2 Times / 230°C Maximum 1 Time
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Time within 5°C of actual peak (t _p)	10 seconds Maximum 2 Times / 80 seconds Maximum 1 Time
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Ramp-down Rate	5°C/second Maximum
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Time 25°C to Peak Temperature (t)	N/A
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Moisture Sensitivity Level	Level 1
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Low Temperature Manual Soldering

185°C Maximum for 10 seconds Maximum, 2 times Maximum.

High Temperature Manual Soldering

260°C Maximum for 5 seconds Maximum, 2 times Maximum.