

Status: **Active New**

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IDTs ADC1210S125F2 demoboard is suitable for dynamic performance evaluations from low to high IF configuration with LVDS DDR output variants. A data-acquisition board can be used during design and prototype to analyze ADC performance.



- Single power supply (on-board regulators)
- SMA connector for clock and analog input signals
- LVDS/DDR outputs

ORDERABLE PART ID	PART STATUS	PKG. CODE	PKG. TYPE	LEAD COUNT (#)	TEMP. GRADE	PB (LEAD) FREE	ORDER
ADC1210S125F2-DB	Active New	BD0	BOARD	0	C	N	Check Inventory

	TITLE	TYPE▲	FORMAT	FILE SIZE	DATE
	Demo Board Software ADC_1_r04	Manual - Software	ZIP	2.33 MB	07/19/2012

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