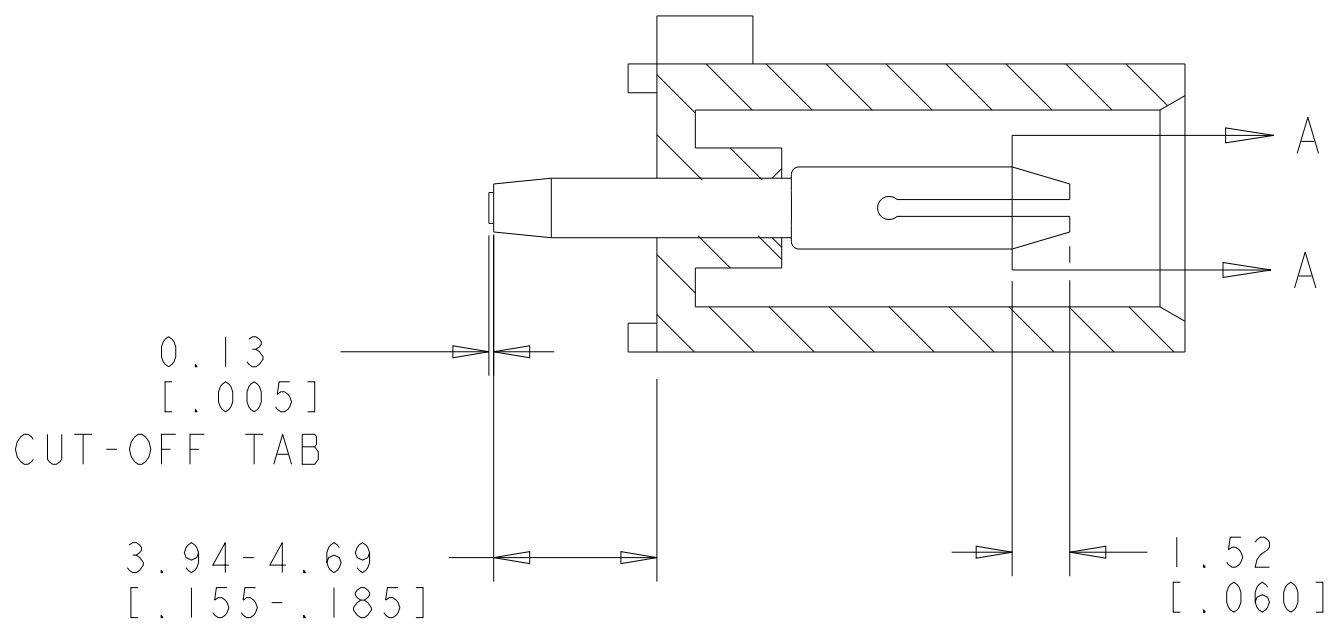
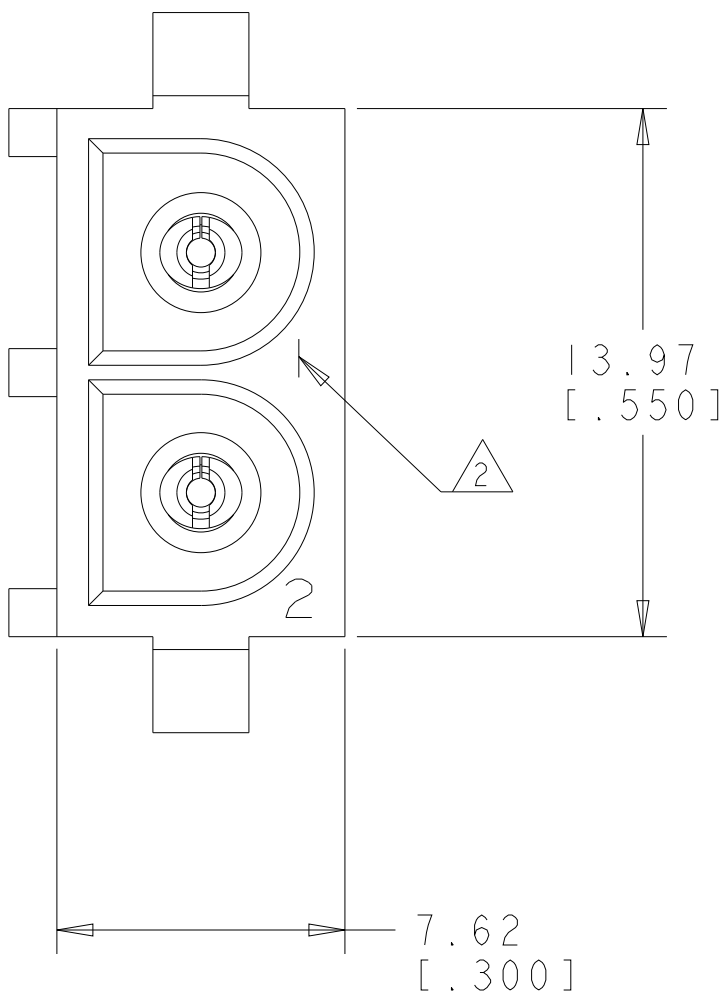
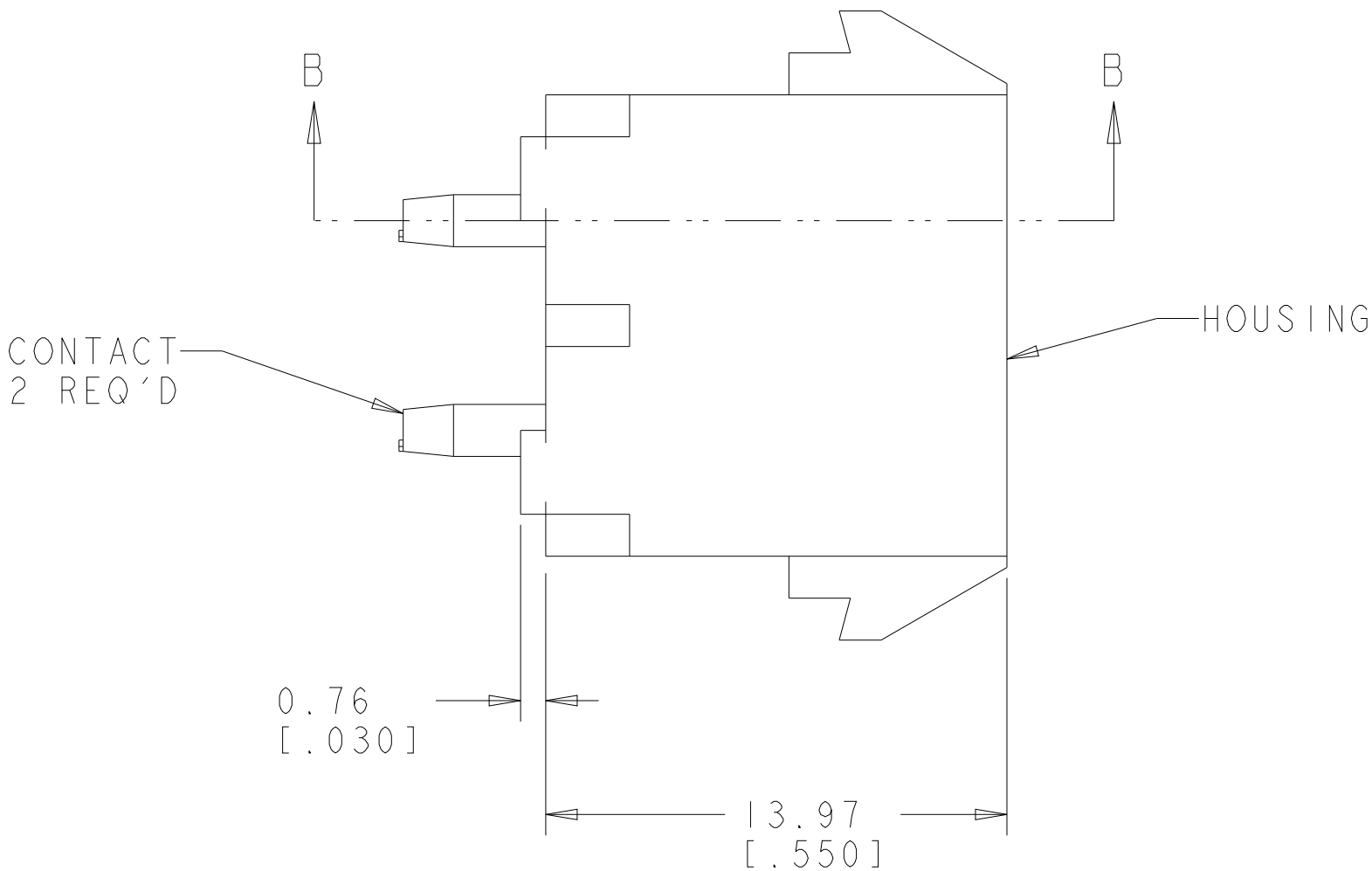
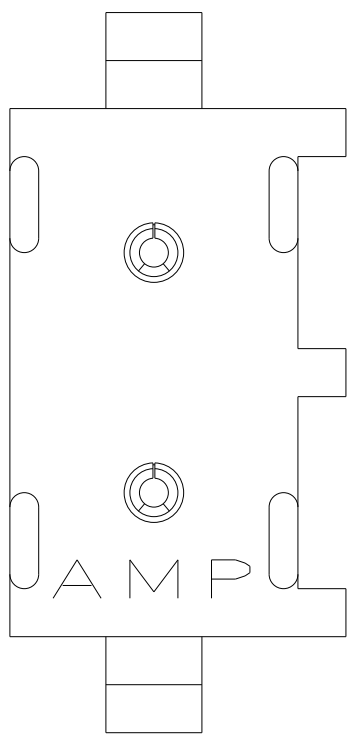
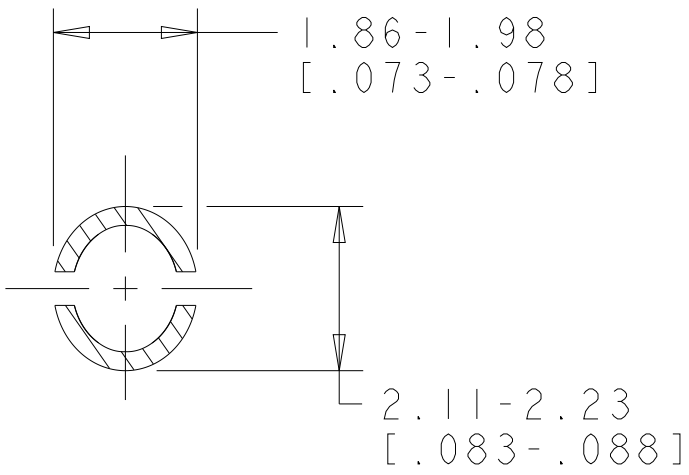


LOC	DIST	REVISIONS					
		P	LTR	DESCRIPTION	DATE	DWN	APVD
CM	00		O	REVISED PER 063B-0331-03	27-JUN-03	KW	SR
			O1	ECO-07-017458	26JUL2007	KW	DC
			A	REVISED PER ECR-21-118133	22OCT2021	SS	VS

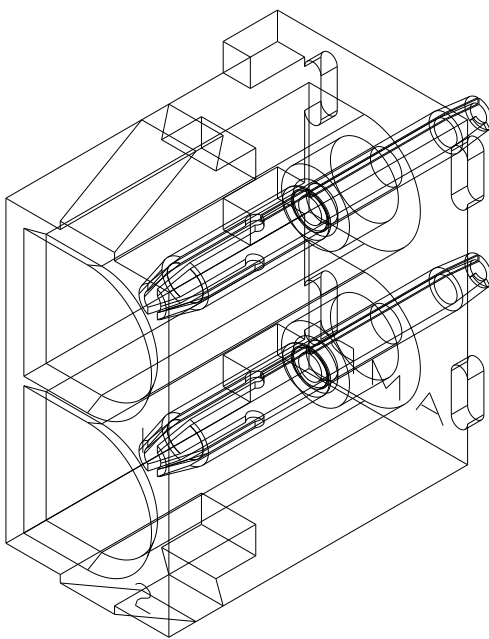
1. PARTS COMPLY TO AMP SOLDERABILITY SPECIFICATION 109-11-3.
- △ 2. CIRCUIT IDENTIFICATION CHARACTERS ARE ADJACENT TO THE INDICATED CAVITIES, BUT LOCATION AND ORIENTATION MAY DIFFER FROM PRINT.
3. DIMENSIONS IN BRACKETS ARE IN INCHES.
4. DIMENSIONS SHOWN REPRESENT PRODUCT IN DRY CONDITION. ADDITIONAL 2% GROWTH DUE TO END USE LOCATION MOISTURE ABSORPTION MAY OCCUR AND SHOULD BE ACCOUNTED FOR.



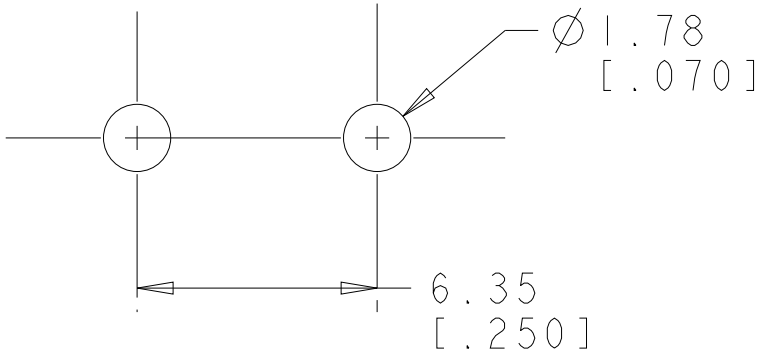
SECTION B-B



SECTION A-A
SCALE 10:1

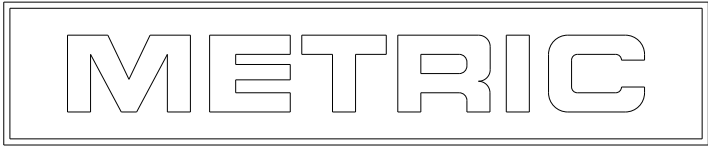


3 DIMENSIONAL VIEW
SCALE 4:1



RECOMMENDED MOUNTING HOLE PATTERN
FOR 1.57 [.062] THICK P.C. BOARD

PHOSPOR BRONZE, PRE-TIN	NYLON, UL94V-2, NATURAL	1586076-1
PIN CONTACT MATERIAL AND FINISH	HOUSING MATERIAL AND COLOR	PART NUMBER



THIS DRAWING IS A CONTROLLING DOCUMENT FOR TYCO ELECTRONICS CORPORATION. IT IS SUBJECT TO CHANGE WITHOUT NOTICE. ANY CHANGES WILL BE INDICATED BY A REVISION SYMBOL IN THE REVISIONS SECTION. SHOULD BE CONTACTED FOR THE LATEST EDITION.		DWN K. WHITAKER 27-JUN-2003	Tyco Electronics Harrisburg, PA 17105-3608		
DIMENSIONS: mm[INCHES]		CHK S. RIDGILL 27-JUN-2003	NAME		
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD S. RIDGILL 27-JUN-2003	PRODUCT SPEC PIN HEADER ASSEMBLY, 2 CIRCUIT, UNIVERSAL MATE-N-LOK(TM)		
0 PLC ±"		APPLICATION SPEC			
1 PLC ±"		WEIGHT			
2 PLC ±0.13(.005)		SIZE			
3 PLC ±"		CAGE CODE			
4 PLC ±"		DRAWING NO			
ANGLES ±0°30'		RESTRICTED TO			
FINISH SEE TABLE		00779 ©=1586076			
MATERIAL SEE TABLE		CUSTOMER DRAWING			
		SCALE 5:1 SHEET 1 OF 1 REV A			