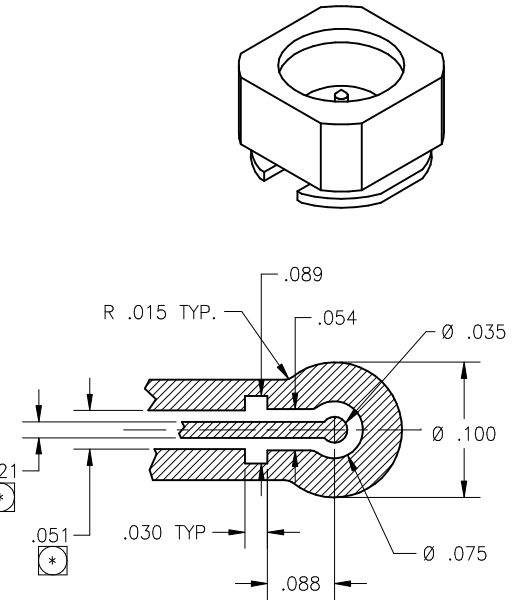
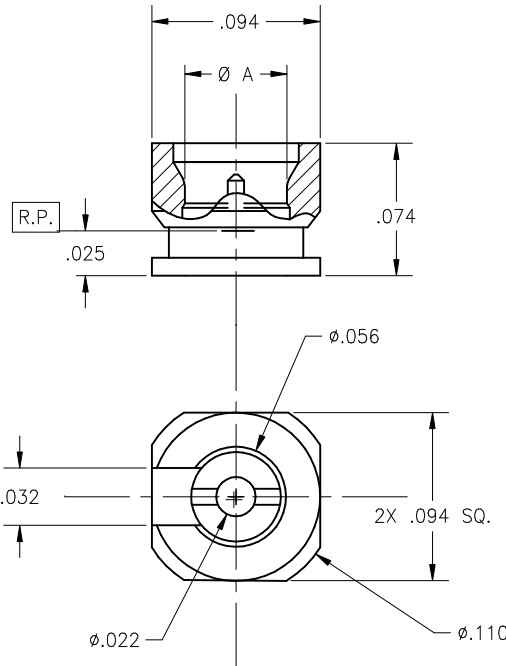
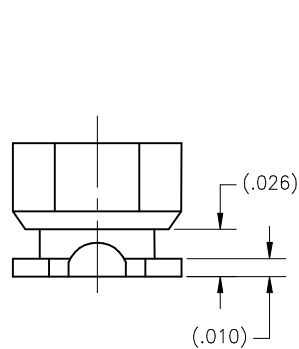


P/N	(Ø A)	INTERFACE
-1CC	.057	DETENT
-2CC	.060	NON-DETENT

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	BY
-	-	Initial release	3.14.11	-



RECOMMENDED PCB FOOTPRINT

* DIMENSIONS SHOWN ARE FOR ROGERS 4350 PCB MATERIAL.
THESE DIMENSIONS MAY VARY DEPENDING ON PCB MATERIAL USED.

MATERIAL:	ELECTRICAL:	MECHANICAL:	ENVIRONMENTAL:
BODY & CENTER CONDUCTOR: Brass alloy C360 per ASTM B-16 INSULATOR: Torlon per ASTM D-5204	Impedance: 50 Ohms Nom. Freq. Range: DC TO 50 GHz VSWR: 1.15:1 to 26.5 GHz, 1.25: to 50 GHz Insertion Loss: .12 x $\sqrt{f}$ GHz Working Voltage: 335 Vrms @ Sea Level Dielectric Withstand Voltage: 500 V rms RF HiPot Voltage: 325 Vrms Min @ 5MHz Corona Level: 125 Vrms @ 70,000 ft Insulation Resistance: 5000 Mohms RF Leakage: -(65 - f GHz) dB Contact Resistance: Center conductor: 4.0 Milliohms Outer conductor: 2.0 Milliohms	Interface Dimensions: Consult Factory Connector Durability: 100 Cycles Center Contact Retention: N/A Force to Engage and Disengage: 3.5 lbs Engage (Typical Detent) 5.0 lbs Disengage (Typical Detent)	Temp. Range: -65°C to +165°C Thermal Shock: MIL-STD-202, Method 107, Test Cond. B Moisture Resistance: MIL-STD-202, Method 106, Insulation resistance at least 200 MegaOhms within 5 minutes after removal from humidity Corrosion: MIL-STD-202, Method 101, Test Cond. B Vibration: MIL-STD-202, Method 204, Test Cond. D Shock: MIL-STD-202, Method 213, Test Cond. I

FINISH:		APPLICABLE CARLISLE IT DOCUMENTS			TOLERANCES AND NOTES EXCEPT AS NOTED		SEE NOTES		-		SEE NOTES		-					
BODY & CENTER CONDUCTOR: Gold plate per ASTM B-488 over nickel plate per AMS-QQ-N-290		WORK STD			PROD INST			ASSY INST			MATERIAL		SIZE		SPECIFICATION		PROCUREMENT	
		NA			NA			NA			APPROVAL INITIALS		DATE		CARLISLE Interconnect Technologies Long Beach, CA 90815 TITLE WMP MALE STRAIGHT SURFACE MOUNT SCALE 20:1 SUB-DIRECTORY/FILENAME OL SHEET 1 OF 1 SIZE C 30990 DRAWING NO. WP350 REV. -			
		NOTICE						1. MACHINE FINISH: 63/ RMS			DRAWN BY PCV			10.21.09				
		THIS DRAWING EMBODIES A CONFIDENTIAL PROPRIETARY DESIGN ORIGINATED BY CARLISLE INTERCONNECT TECHNOLOGIES AND ALL DESIGN, MANUFACTURING, REPRODUCTION, USE AND SALE RIGHTS REGARDING THE SAME ARE EXPRESSLY RESERVED. IT IS SUBMITTED UNDER A CONFIDENTIALITY AGREEMENT FOR A SPECIFIED PURPOSE AND THE RECIPIENT AGREES BY ACCEPTING THIS DRAWING NOT TO SUPPLY OR DISCLOSE ANY INFORMATION REGARDING IT TO ANY UNAUTHORIZED PERSON TO INCORPORATE IN OTHER PRODUCTS ANY SPECIAL FEATURE PECULIAR TO THIS DESIGN. ALL PATENT RIGHTS HERETO ARE EXPRESSLY RESERVED BY CARLISLE INTERCONNECT TECHNOLOGIES, LONG BEACH, CA 90815						2. BREAK ALL SHARP EDGES .003 MAX.			CHECKED BY							
					3. MACHINED FILLETS .005 MAX.													
					4. MACHINED SURFACES SQUARE TO RESPECTIVE AXIS WITHIN .005 INCHES PER INCH.						TEST ENGR							
					5. MACHINED DIAMETERS CONCENTRIC WITHIN .002 T.I.R.						QUALITY							
					6. DIMENSIONS TO BE MET BEFORE PLATING.						DESIGN ENGR PCV			03.14.11				
								7. CHAMFER ALL THREADS 45°.										
								8. THREADS PER H-28										
								9. REMOVE FINISHED EDGES ON TEFLON.										
								10. REMOVE ALL BURRS.										