

MN150120

■ Type		MN150120 (under development)																		
■ ROM (x8-bit)		1K																		
■ RAM (x4-bit)		64																		
■ Number of Instructions		51																		
■ Minimum Instruction Execution Time		1/8 dividing 1.0μs (at 4.5 to 5.5V, 8MHz) 1/8 dividing 4.0μs (at 2.0 to 5.5V, 2MHz) 1/8 dividing 8.0μs (at 1.8 to 5.5V, 1MHz) 1/8 dividing 8.0μs (at 3.0 to 5.5V, selecting CR Oscillation)																		
■ Interrupts		• Time Base • External • AC Zero-cross																		
■ Timer Counter		Time Base Counter : 1 Clock Source1/1 of OSC Oscillation Clock Interrupt SourceOverflow of Time Base Counter Watchdog Timer (Mask Option) <table><tr><td></td><td colspan="4">Time Base Output</td></tr><tr><td>fosc=8MHz</td><td>2kHz</td><td>4kHz</td><td>8kHz</td><td>16kHz</td></tr><tr><td>fosc=1MHz</td><td>0.25kHz</td><td>0.5kHz</td><td>1kHz</td><td>2kHz</td></tr></table>					Time Base Output				fosc=8MHz	2kHz	4kHz	8kHz	16kHz	fosc=1MHz	0.25kHz	0.5kHz	1kHz	2kHz
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■ I/O Pins	I/O	15 (16)	• Common use : 7 (8) • Specified pull-up Resistor available : 11 (12) (Mask Option) • Specified output architecture available : Nch Open-drain / Push-pull : 11 (12) (Mask Option) • 4ch LED direct drive available (20mA / 2.0V) • When CR oscillation mode is selected, OSC2 pin may be used as P33 pin																	
■ Comparator		2																		
■ Zero-cross Inputs		1																		
■ Special Ports		Buzzer Output (1kHz, 2kHz, 4kHz : fosc=at 4MHz)																		
■ Notes		CR / crystal Oscillation selectable, Auto-reset circuit selectable (Mask Option)																		
■ Package		SOP020-P-0300																		

Electrical Characteristics

Supply Current

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
Operating Supply Current	IDD1	fosc=8MHz		4.0	8.0	mA
	IDD2	fosc=32.768kHz		30	60	μA
Supply Current at HALT	IDD3	fosc=32.768kHz		15	30	μA
Supply Current at STOP	IDD4			0.5	5.0	μA
Auto reset current consumption	IDD5			30	80	μA

(Ta= -40 to +85°C, VDD=5.0V, VSS=0V)

Comparator Characteristics

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
Input offset voltage	VIQF	Vin=1.5 to 3.5V		20	100	mV
In-phase input voltage range	VISP		1.5		3.5	V

(Ta= -40 to +85°C, VDD=5.0V, VSS=0V)

Support Tool

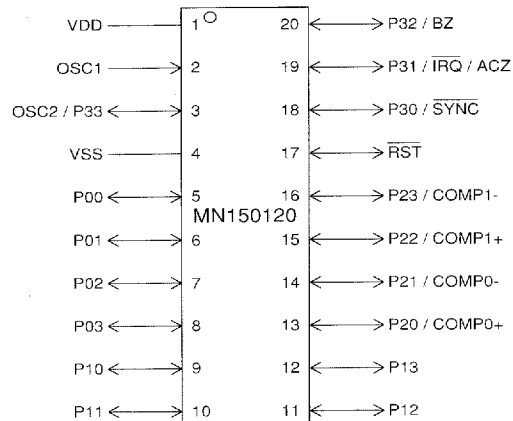
In-Circuit Emulator

PX-ICE1500 + PX-PRB150120

EPROM built-in Type

Use **MN15P0222** in SOP020-P-0300 / SDIP022-P-0300 package.

Pin Assignment



SOP020-P-0300