TOSHIBA TC7WH08FU/FK

TENTATIVE

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC7WH08FU, TC7WH08FK

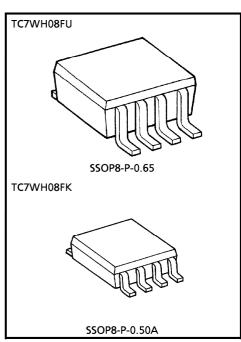
(UNDER DEVELOPMENT)

DUAL 2-INPUT AND GATE

The TC7WH08 is an advanced high speed CMOS 2-INPUT AND GATE fabricated with silicon gate CMOS technology. It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation. The internal circuit is composed of 4 stages including buffer output, which provide high noise immunity and stable output. An input protection circuit ensures that 0 to 7V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5V to 3V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

FEATURES

- High Speed $t_{pd} = 4.3$ ns (Typ.) at
- Low Power Dissipation $I_{CC} = 2\mu A$ (Max.) at $Ta = 25^{\circ}C$
- High Noise Immunity $V_{NIH} = V_{NIL} = 28\%$ V_{CC} (Min.)
- Power Down Protection is provided on all inputs.
- Balanced Propagation Delays $\cdots t_{pLH} = t_{pHL}$
- Wide Operating Voltage Range... $\dot{V}_{CC}(opr) = 2 \sim 5.5 \text{V}$

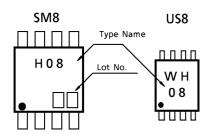


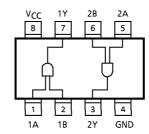
Weight

SSOP8-P-0.65 : 0.02g (Typ.) SSOP8-P-0.50A : 0.01g (Typ.)

MARKING

PIN ASSIGNMENT (TOP VIEW)





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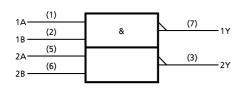
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MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage Range	V _{CC}	-0.5~7.0	V	
DC Input Voltage	VIN	-0.5~7.0	V	
DC Output Voltage	Vout	-0.5~V _{CC} +0.5	\	
Input Diode Current	lικ	– 20	mA	
Output Diode Current	lok	± 20	mΑ	
DC Output Current	lout	± 25	mA	
DC V _{CC} /Ground Current	lcc	± 50	mA	
Power Dissipation	D-	300 (SM8)	mW	
Power Dissipation	PD	200 (US8)	IIIVV	
Storage Temperature	T _{stg}	-65∼150	°C	
Lead Temperature (10 s)	TL	260	°C	

LOGIC DIAGRAM



TRUTH TABLE

Α	В	Υ
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage	Vcc	2.0~5.5	V	
Input Voltage	VIN	0~5.5	V	
Output Voltage	Vout	0~V _{CC}	V	
Operating Temperature	T _{opr}	- 40∼85	°C	
Input Rise and Fall Time	dt/dv	$0\sim100 \text{ (V}_{CC} = 3.3 \pm 0.3 \text{V)}$	ns / V	
input kise and rail fille	at/av	$0\sim20 (V_{CC} = 5 \pm 0.5V)$] 115/ V	

DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	TEST C	TEST CONDITION		1	Ta = 25°C		$Ta = -40 \sim 85^{\circ}C$		UNIT
CHARACTERISTIC	STIVIDOL	TEST CONDITION		Vсс (V)	MIN.	TYP.	MAX.	MIN.	MAX.	01411
High-Level				2.0	1.50	1	_	1.50		
Input Voltage	VIH		_	3.0~	V_{CC}			Vcc		V
Imput Voltage				5.5	×0.7			×0.7		
Low-Level						_	0.50	_	0.50	
	V _{IL}		_	3.0~			Vcc		Vcc	V
Input Voltage				5.5	_	_	×0.3	_	×0.3	
				2.0	1.9	2.0	_	1.9	_	
lliah Laval	V _{ОН}	V _{IN} = V _{IH}	$I_{OH} = -50\mu A$	3.0	2.9	3.0	_	2.9	_	V
High-Level				4.5	4.4	4.5	_	4.4	_	
Output Voltage			$I_{OH} = -4mA$	3.0	2.58	_	_	2.48	_	
			I _{OH} = -8mA	4.5	3.94	_	_	3.80	_	
		V _{IN} = V _{IH} or V _{IL}	I _{OL} = 50μΑ	2.0	_	0.0	0.1	_	0.1	V
l and land				3.0	_	0.0	0.1	_	0.1	
Low-Level	VOL			4.5	_	0.0	0.1	_	0.1	
Output Voltage			I _{OL} = 4mA	3.0	_	_	0.36	_	0.44	
			I _{OL} = 8mA	4.5	_	_	0.36	_	0.44	
Input Leakage		V _{IN} = 5.5V or GND		0~			101		±10	^
Current	IN			5.5	5 - -	_	— ± 0.1		± 1.0	μ A
Quiescent Supply Current	lcc	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	_	20.0	μ A

AC	ELECTRICAL	CHARACTERISTICS	(Input $t_r = t_f = 3ns$)
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CHARACTERISTIC	SYMBOL	TEST C		ONDITION		Ta = 25°C			Ta = -40~85°C		
CHARACTERISTIC	STIVIBOL		V _{CC} (V)	C _L (pF)	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT	
Propagation Delay Time			$- \frac{3.3 \pm 0.3}{5.0 \pm 0.5}$	15	_	6.2	8.8	1.0	10.5	ns	
	t _{pLH}			50		8.7	12.3	1.0	14.0		
	t _{pHL}			15		4.3	5.9	1.0	7.0	ns	
				50		5.8	7.9	1.0	9.0		
Input Capacitance	CIN		_			4	10	_	10	рF	
Power Dissipation	CDD	(Note 1)	•	·		18				рF	
Capacitance	C _{PD}	(14010 1)				'0				יק	

(Note 1): CpD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

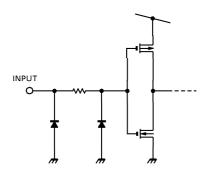
Average operating current can be obtained by the equation:

ICC (opr) = CpD · VCC · fIN + ICC

NOISE CHARACTERISTICS (Ta = 25°C, Input $t_r = t_f = 3ns$)

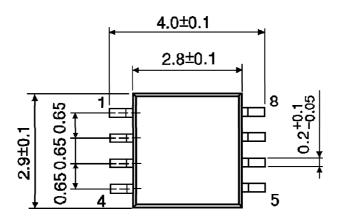
CHARACTERISTIC	SYMBOL	TEST CONDITION	V _{CC} (V)	TYP.	LIMIT	UNIT
Quiet Output Maximum Dynamic V _{OL}	V _{OLP}	C _L = 50pF	5.0	0.3	0.8	V
Quiet Output Minimum Dynamic V _{OL}	V _{OLV}	C _L = 50pF	5.0	-0.3	-0.8	V
Minimum High Level Dynamic Input Voltage	V _{IHD}	C _L = 50pF	5.0	_	3.5	V
Maximum Low Level Dynamic Input Voltage	V _{ILD}	C _L = 50pF	5.0	I	1.5	V

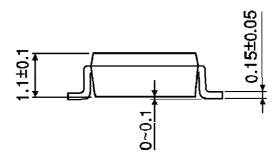
INPUT EQUIVALENT CIRCUIT



OUTLINE DRAWING SSOP8-P-0.65

Unit: mm

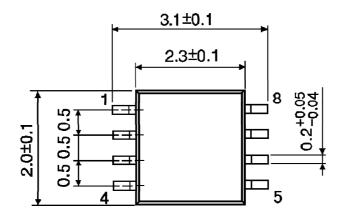


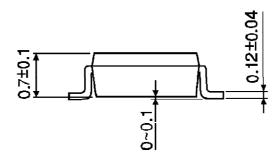


Weight: 0.02g (Typ.)

OUTLINE DRAWING SSOP8-P-0.50A

Unit: mm





Weight: 0.01g (Typ.)