- D-C Triggered by Active-High or Active-Low Inputs
- Retriggerable for Very Long Output Pulses, Up to 100% Duty Cycle
- Overriding Clear Terminates Output Pulse
- Package Options Include Both Plastic and Ceramic Chip Carriers in Addition to Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

## description

These d-c triggered multivibrators feature output pulse duration control by three methods. The basic pulse duration is programmed by selection of external resistance and capacitance values. Once triggered, the basic pulse duration may be extended by retriggering the gated low-levelactive (A) or high-level-active (B) inputs, or be reduced by use of the overriding clear. Figure 1 illustrates pulse control by retriggering and early clear.

The SN54HC123 is characterized for operation over the full military temperature range of -55°C to 125°C. The SN74HC123 is characterized for operation from -40°C to 85°C.

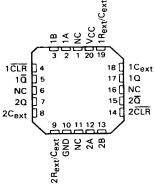
FUNCTION TABLE

INPUTS			OUTPUTS	
CLEAR	Α	В	Q	₫
L	Х	X	L	Н
X	н	Х	1.†	Н <sup>†</sup>
x	х	L.	L†	н†
н	L	1	л	ъ
н	<b>‡</b>	н	л	T
'+	L	н	l n	T

<sup>†</sup>The second and third lines each indicate the logic levels the outputs will take on after the completion of any pulse already started.

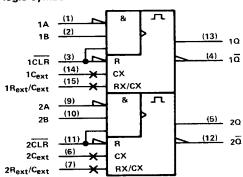
## SN54HC123 . . . J PACKAGE SN74HC123 . . . J OR N PACKAGE (TOP VIEW) 1A 1 16 VCC 15 1Rext/Cext 1B 🛮 2 1CLR []3 14 1 1Cext 10 4 13 10 12 20 20 🛮 5 11 2CLR 2Cext □6 2Rext/Cext 7 10 2B GND 8 9 2A

SN54HC123 . . . FH OR FK PACKAGE SN74HC123 . . . FH OR FN PACKAGE (TOP VIEW)



NC-No internal connection

## logic symbol



Pin numbers shown are for J and N packages.

PRODUCT PREVIEW

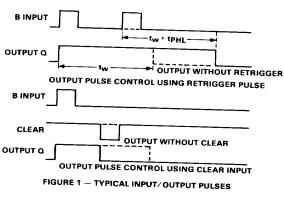
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PRODUCT PREVIEWS



RETRIGGER PULSE

maximum ratings, recommended operating conditions, and electrical characteristics See Table IV, page 2-10.

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