



CRYSTEK
CRYSTALS
A DIVISION OF CRYSTEK CORPORATION

POCKET REFERENCE OSCILLATOR

*New
PRO Series*



Actual Size

Crystek's new **PRO** Series, **P**ocket **R**eference **O**scillator is an industry first! A complete HCMOS crystal clock oscillator has been integrated in a rugged SMA housing. Simply connect +3.3Vdc to the female SMA side, and it will output a clean Low Jitter signal from the male SMA side. In effect, each **PRO** is a fixed frequency portable crystal generator. Frequency stability is $\pm 25\text{ppm}$ over 0°C to 70°C . Available off the shelf in many standard frequencies (see next page). Perfect for Lab use.

**New
PRO Series**

Part Number	Description
CPRO33-4.000	4.000MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-5.000	5.000MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-8.000	8.000MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-10.000	10.000MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-12.000	12.000MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-16.000	16.000MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-16.384	16.384MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-20.000	20.000MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-25.000	25.000MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-32.768	32.768MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-44.736	44.736MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-45.000	45.000MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-50.000	50.000MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-62.500	62.500MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-77.760	77.760MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-80.000	80.000MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-100.000	100.000MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-106.250	106.250MHz, 3.3V ± 25 ppm Max(0/70°C)
CPRO33-125.000	125.000MHz, 3.3V ± 25 ppm Max(0/70°C)

Available Accessories:

Power Supply Banana Plug
SMA to BNC Power Cable
Signal Injection Cable
DC Power Supply

Temperature Range: 0°C to +70°C
Operating Temp. -40°C to 85°C
Storage: -45°C to 90°C
Input Voltage: +3.3V $\pm$ 0.3V

Input Current:
1.54~34.0MHz 18 mA Max
35.0~50.0MHz 25 mA Max
51.0~69.0MHz 30 mA Max
70.0~156.25MHz 45 mA Max

Rise/Fall Time:
1.54~10.0MHz 5nsec Max @ 20% to 80% Vdd
10.1~30.0MHz 4nsec Max @ 20% to 80% Vdd
30.1~50.0MHz 3nsec Max @ 20% to 80% Vdd
50.1~80.1MHz 2.5nsec Max @ 20% to 80% Vdd
80.1~156.25MHz 2nsec Max @ 20% to 80% Vdd

Output: HCMOS
Symmetry: 45/55% Max @ 50% Vdd
Logic: "0" = 10% Vdd Max
"1" = 90% Vdd Min.

Load: 15pF Typical
Jitter: 12KHz~80MHz 0.5psec Typ., 1psec RMS Max

