

Optocoupler, Phototransistor Output, With Base Connection

Features

- Isolation Test Voltage 5300 V_{RMS}
- Interfaces with Common Logic Families
- Input-output Coupling Capacitance < 0.5 pF
- Industry Standard Dual-in-line 6-pin Package

Agency Approvals

- UL File #E52744 System Code H or J
- DIN EN 60747-5-2(VDE0884)
- DIN EN 60747-5-5 pending
- Available with Option 1

Applications

AC Mains Detection
 Reed relay driving
 Switch Mode Power Supply Feedback
 Telephone Ring Detection
 Logic Ground Isolation
 Logic Coupling with High Frequency Noise Rejection

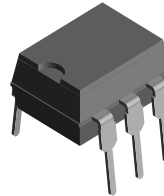
Description

The 4N25 family is an Industry Standard Single Channel Phototransistor Coupler. This family includes the 4N25/ 4N26/ 4N27/ 4N28. Each optocoupler consists of gallium arsenide infrared LED and a silicon NPN phototransistor.

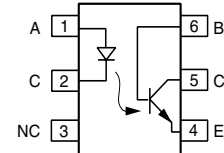
These couplers are Underwriters Laboratories (UL) listed to comply with a 5300 V_{RMS} isolation test voltage. This isolation performance is accomplished through special Vishay manufacturing process.

Compliance to DIN EN 60747-5-2(VDE0884)/ DIN EN 60747-5-5 pending partial discharge isolation specification is available by ordering option1.

These isolation processes and the Vishay ISO9001 quality program results in the highest isolation performance available for a commercial plastic phototransistor optocoupler.



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The devices are also available in lead formed configuration suitable for surface mounting and are available either on tape and reel, or in standard tube shipping containers.

Note:

For additional design information see Application Note 45 Normalized Curves

Order Information

Part	Remarks
4N25	CTR > 20 %, DIP-6
4N26	CTR > 20 %, DIP-6
4N27	CTR > 10 %, DIP-6
4N28	CTR > 10 %, DIP-6
4N25-Xxx6	CTR > 20 %, DIP-6 400 mil (option 6)
4N25-Xxx7	CTR > 20 %, SMD-6 (option 7)
4N25-Xxx9	CTR > 20 %, SMD-6 (option 9)
4N26-Xxx6	CTR > 20 %, DIP-6 400 mil (option 6)
4N26-Xxx7	CTR > 20 %, SMD-6 (option 7)
4N26-Xxx9	CTR > 20 %, SMD-6 (option 9)
4N27-Xxx7	CTR > 10 %, SMD-6 (option 7)
4N27-Xxx9	CTR > 10 %, SMD-6 (option 9)
4N28-Xxx9	CTR > 10 %, SMD-6 (option 9)

For additional option information and package dimensions see Option Section.

Absolute Maximum Ratings

$T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified

Stresses in excess of the absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute Maximum Rating for extended periods of the time can adversely affect reliability.

Input

Parameter	Test condition	Symbol	Value	Unit
Reverse voltage		V_R	6.0	V
Forward current		I_F	60	mA
Surge current	$t < 10\text{ }\mu\text{s}$	I_{FSM}	2.5	A
Power dissipation		P_{diss}	100	mW

Output

Parameter	Test condition	Symbol	Value	Unit
Collector-emitter breakdown voltage		V_{CEO}	70	V
Emitter-base breakdown voltage		V_{EBO}	7.0	V
Collector current		I_C	50	mA
Collector current	$t < 1.0\text{ ms}$	I_C	100	mA
Power dissipation		P_{diss}	150	mW

Coupler

Parameter	Test condition	Symbol	Value	Unit
Isolation test voltage		V_{ISO}	5300	V_{RMS}
Creepage			≥ 7.0	mm
Clearance			≥ 7.0	mm
Isolation thickness between emitter and detector			≥ 0.4	mm
Comparative tracking index	DIN IEC 112/VDE0303, part 1		175	
Isolation resistance	$V_{IO} = 500\text{ V}$, $T_{amb} = 25\text{ }^{\circ}\text{C}$	R_{IO}	10^{12}	Ω
	$V_{IO} = 500\text{ V}$, $T_{amb} = 100\text{ }^{\circ}\text{C}$	R_{IO}	10^{11}	Ω
Storage temperature		T_{stg}	- 55 to + 150	$^{\circ}\text{C}$
Operating temperature		T_{amb}	- 55 to + 100	$^{\circ}\text{C}$
Junction temperature		T_j	100	$^{\circ}\text{C}$
Soldering temperature	max.10 s, dip soldering: distance to seating plane $\geq 1.5\text{ mm}$	T_{sld}	260	$^{\circ}\text{C}$



Electrical Characteristics

$T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

Input

Parameter	Test condition	Symbol	Min	Typ.	Max	Unit
Forward voltage ¹⁾	$I_F = 50\text{ mA}$	V_F		1.3	1.5	V
Reverse current ¹⁾	$V_R = 3.0\text{ V}$	I_R		0.1	100	μA
Capacitance	$V_R = 0\text{ V}$	C_O		25		pF

¹⁾ Indicates JEDEC registered values

Output

Parameter	Test condition	Part	Symbol	Min	Typ.	Max	Unit
Collector-base breakdown voltage ¹⁾	$I_C = 100\text{ }\mu\text{A}$		BV_{CBO}	70			V
Collector-emitter breakdown voltage ¹⁾	$I_C = 1.0\text{ mA}$		BV_{CEO}	30			V
Emitter-collector breakdown voltage ¹⁾	$I_E = 100\text{ }\mu\text{A}$		BV_{ECO}	7.0			V
$I_{CEO}(\text{dark})$ ¹⁾	$V_{CE} = 10\text{ V}$, (base open)	4N25			5.0	50	nA
		4N26			5.0	50	nA
		4N27			5.0	50	nA
		4N28			10	100	nA
$I_{CBO}(\text{dark})$ ¹⁾	$V_{CB} = 10\text{ V}$, (emitter open)				2.0	20	nA
Collector-emitter capacitance	$V_{CE} = 0$		C_{CE}		6.0		pF

¹⁾ Indicates JEDEC registered values

Coupler

Parameter	Test condition	Part	Symbol	Min	Typ.	Max	Unit
Isolation voltage ¹⁾	Peak, 60 Hz	4N25	V_{IO}	2500			V
		4N26	V_{IO}	1500			V
		4N27	V_{IO}	1500			V
		4N28	V_{IO}	500			V
Saturation voltage, collector-emitter	$I_{CE} = 2.0\text{ mA}$, $I_F = 50\text{ mA}$		$V_{CE(sat)}$			0.5	V
Resistance, input output ¹⁾	$V_{IO} = 500\text{ V}$		R_{IO}	100			G Ω
Capacitance (input-output)	$f = 1.0\text{ MHz}$		C_{IO}		0.5		pF

¹⁾ Indicates JEDEC registered values

Current Transfer Ratio

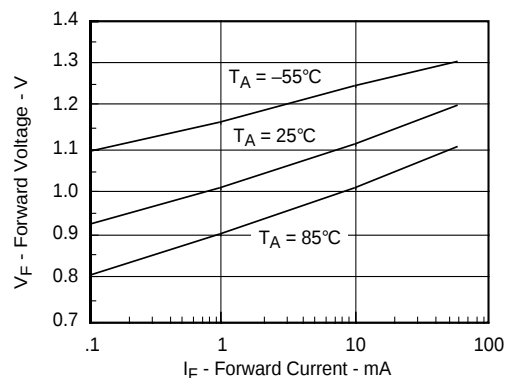
Parameter	Test condition	Part	Symbol	Min	Typ.	Max	Unit
DC Current Transfer Ratio ¹⁾	$V_{CE} = 10\text{ V}$, $I_F = 10\text{ mA}$	4N25	CTR_{DC}	20	50		%
		4N26	CTR_{DC}	20	50		%
		4N27	CTR_{DC}	10	30		%
		4N28	CTR_{DC}	10	30		%

¹⁾ Indicates JEDEC registered value

Switching Characteristics

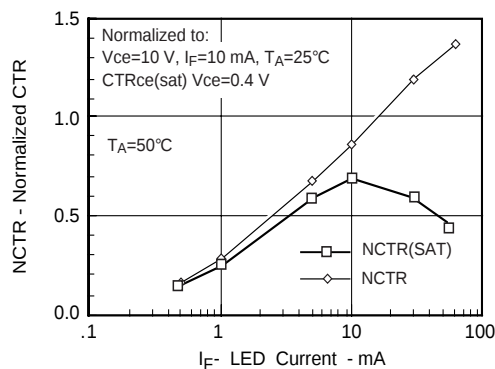
Parameter	Test condition	Symbol	Min	Typ.	Max	Unit
Rise and fall times	$V_{CE} = 10 \text{ V}$, $I_F = 10 \text{ mA}$, $R_L = 100 \Omega$	t_r , t_f		2.0		μs

Typical Characteristics ($T_{amb} = 25^\circ\text{C}$ unless otherwise specified)



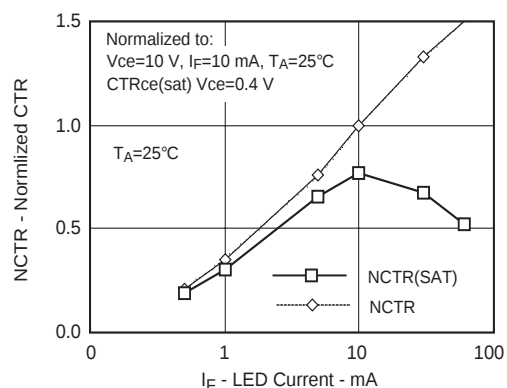
4N25_01

Figure 1. Forward Voltage vs. Forward Current



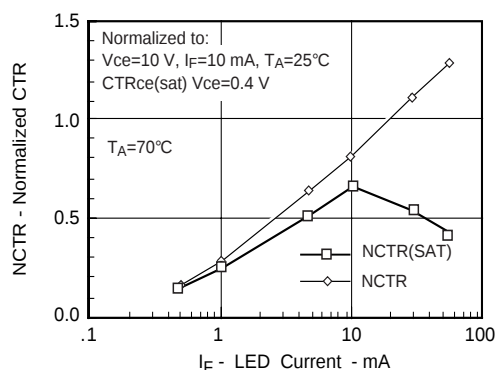
4N25_03

Figure 3. Normalized Non-saturated and Saturated CTR vs. LED Current



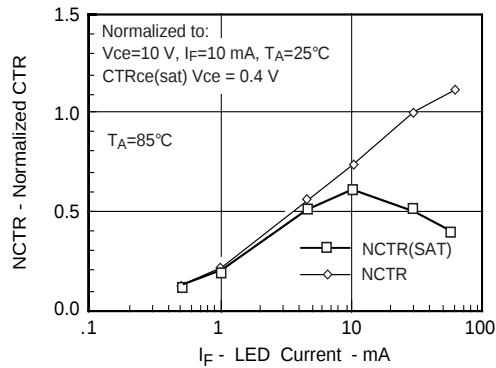
4N25_02

Figure 2. Normalized Non-saturated and Saturated CTR vs. LED Current



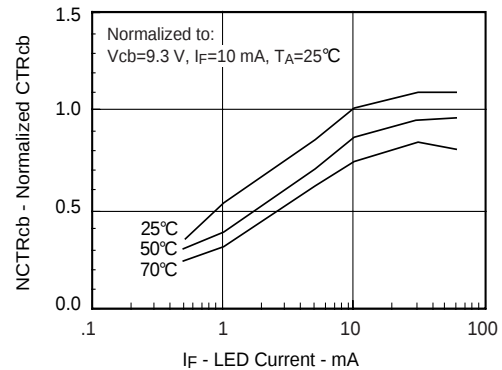
4N25_04

Figure 4. Normalized Non-saturated and saturated CTR vs. LED Current



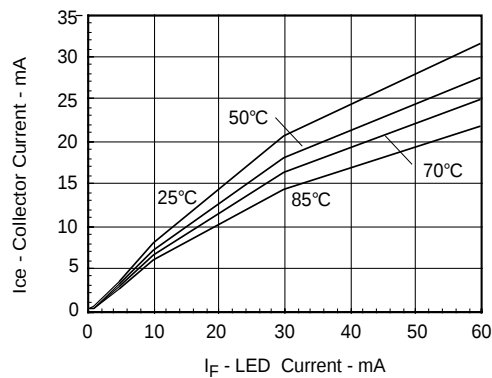
4N25_05

Figure 5. Normalized Non-saturated and saturated CTR vs. LED Current



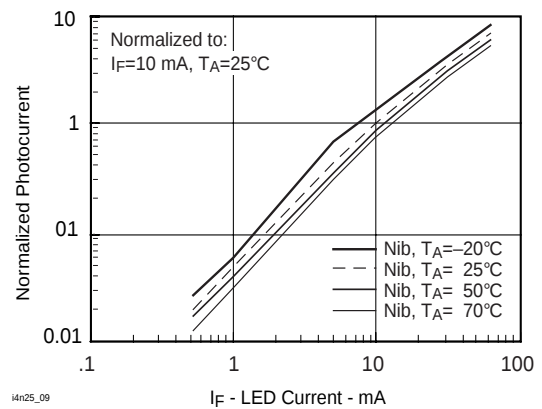
4N25_08

Figure 8. Normalized CTRcb vs. LED Current and Temp.



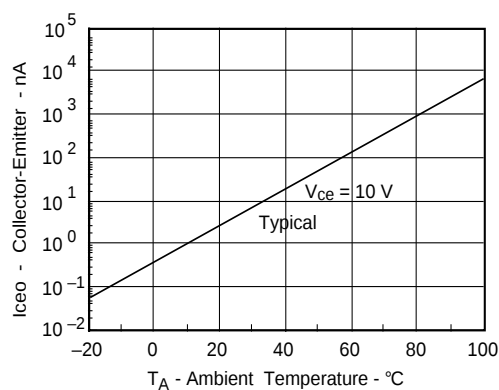
4N25_06

Figure 6. Collector-Emitter Current vs. Temperature and LED Current



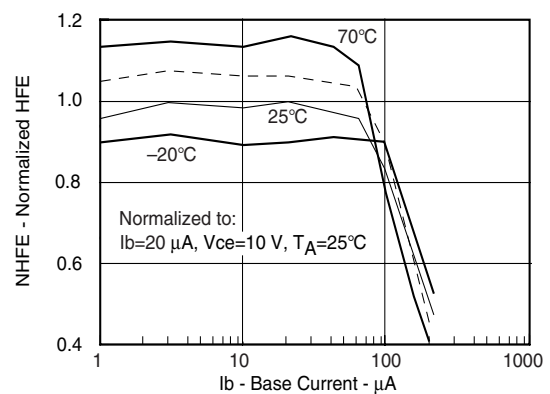
4N25_09

Figure 9. Normalized Photocurrent vs. I_F and Temp.



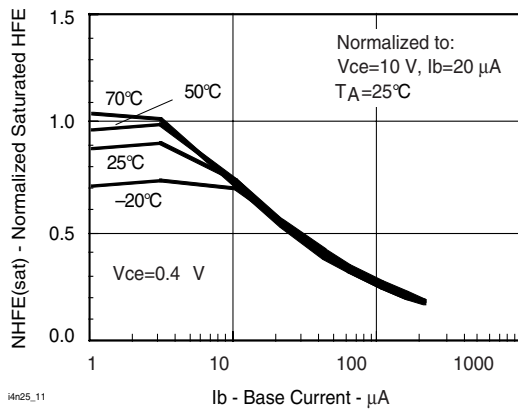
4N25_07

Figure 7. Collector-Emitter Leakage Current vs. Temp.



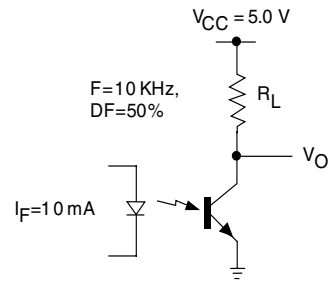
4N25_10

Figure 10. Normalized Non-saturated HFE vs. Base Current and Temperature



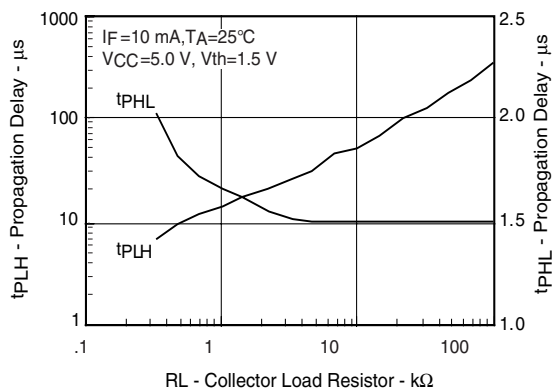
4N25_11

Figure 11. Normalized HFE vs. Base Current and Temp.



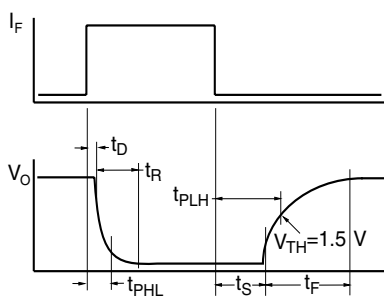
4N25_14

Figure 14. Switching Schematic



4N25_12

Figure 12. Propagation Delay vs. Collector Load Resistor



4N25_13

Figure 13. Switching Timing

Technical drawing of a 1/2" x 1/2" x 1/2" angle iron. The drawing shows the top flange, web, and overall dimensions. The dimensions are as follows:

- Top flange width: .407 (10.36)
- Top flange thickness: .391 (9.96)
- Web width: .307 (7.8)
- Web thickness: .291 (7.4)
- Overall width: .400 (10.16)
- Overall height: .430 (10.92)
- Top flange to web distance: .014 (0.35)
- Web to overall height distance: .010 (0.25)

Technical drawing of a cross-section of a mechanical part, likely a bearing or seal. The drawing shows a central shaft with a flange. Dimensions are given in inches and millimeters. Key dimensions include: overall width .300 (7.62) TYP.; flange thickness .028 (0.7) MIN.; flange height .180 (4.6) MIN.; flange outer diameter .315 (8.0) MIN.; flange inner diameter .331 (8.4) MIN.; and overall outer diameter .406 (10.3) MAX.

Technical drawing of a 1/2-20 UNF-2A x 0.500 inch Hex Flange Nut. The drawing shows a side view of the nut with various dimensions. Key dimensions include: overall width .375 (9.53) max and .395 (10.03) max; top width .300 (7.62) ref; top thickness .0040 (.102) max and .0098 (.249) max; bottom thickness .012 (.30) typ; flange thickness .020 (.51) max and .040 (1.02) max; and bottom width .315 (8.00) min. A 15° max chamfer is indicated on the bottom edge.

7

Ozone Depleting Substances Policy Statement

It is the policy of **Vishay Semiconductor GmbH** to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

**We reserve the right to make changes to improve technical design
and may do so without further notice.**

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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