

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TD62703P, TD62703F

6CH HIGH VOLTAGE SOURCE DRIVER

The TD62703P, TD62703F is comprised of six source current Transistor Array.

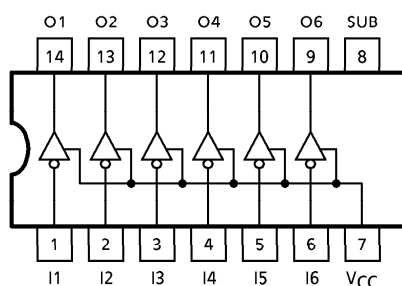
These drivers are specifically designed for fluorescent display applications.

For proper operation, the substrate (SUB) must be connected to the most negative voltage.

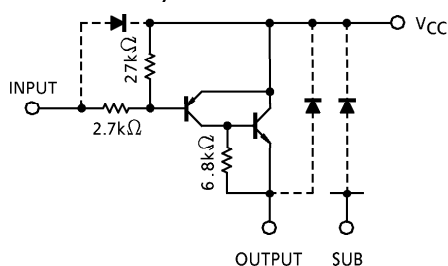
FEATURES

- High output voltage : V_{CC} , $V_{OUT} = 60V$ (Min.)
- Output current (single output) : $I_{OUT} = -50mA$ (Max.)
- Input resistor : $R_{IN} = 2.7k\Omega$
- Package type-P : DIP-14 pin
- Package type-F : SOP-14 pin

PIN CONNECTION (TOP VIEW)

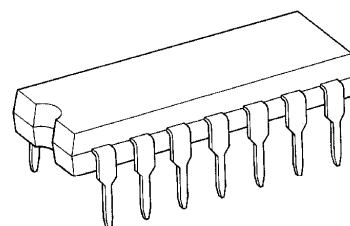


SCHEMATICS (EACH DRIVER)



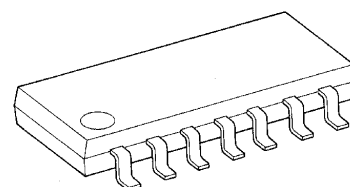
(Note) The input and output parasitic diodes cannot be used as clamp diodes.

TD62703P



DIP14-P-300-2.54

TD62703F



SOP14-P-225-1.27

Weight

DIP14-P-300-2.54 : 1.11g (Typ.)
SOP14-P-225-1.27 : 0.16g (Typ.)

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MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Supply Voltage		V _{SUB}	V _{CC} - 60	V
Output Sustaining Voltage		V _{OUT}	V _{CC} - 60	V
Input Voltage		V _{IN}	- 30~0.5	V
Output Current		I _{OUT}	- 50	mA / ch
Input Current		I _{IN}	10	mA
Power Dissipation	P	P _D (Note 2)	1.0	W
	F		0.625 (Note 1)	
Operating Temperature		T _{opr}	- 40~85	°C
Storage Temperature		T _{stg}	- 55~150	°C

(Note 1) On Glass Epoxy PCB (50×50×1.6mm Cu 50%)

(Note 2) Delated above 25°C in the proportion 8.0mW/°C (P Type), 5.0mW/°C (F Type).

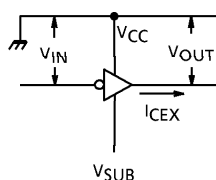
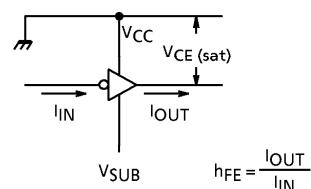
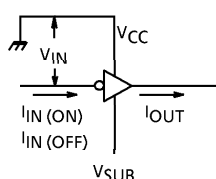
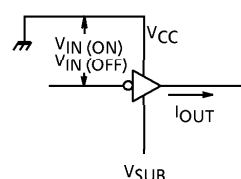
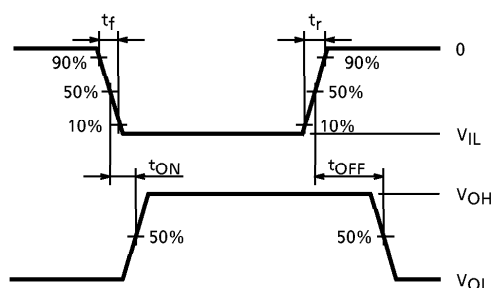
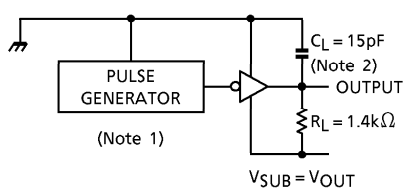
RECOMMENDED OPERATING CONDITIONS (Ta = - 40~85°C)

CHARACTERISTIC		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Voltage		V _{SUB}	V _{CC} = 0V	V _{OUT}	—	- 55	V
Output Sustaining Voltage		V _{OUT}		0	—	V _{SUB}	V
Output Current		I _{OUT}		0	—	- 40	mA / ch
Input Voltage		V _{IN}		0	—	- 7.0	V
Power Dissipation	P	P _D	—	—	—	0.36	W
	F		On PCB (Note)	—	—	0.325	

(Note) On Glass Epoxy PCB (50×50×1.6mm Cu 30%)

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

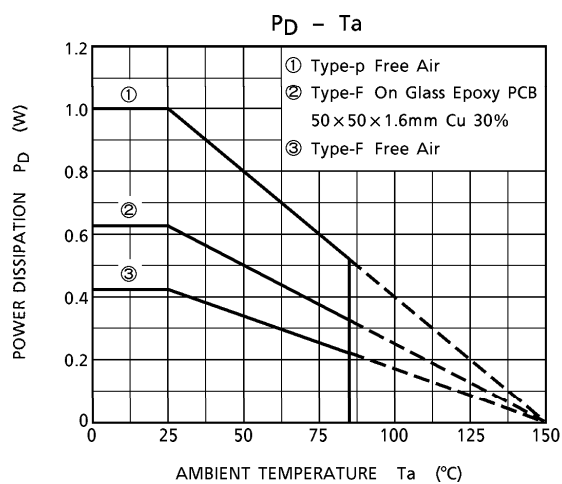
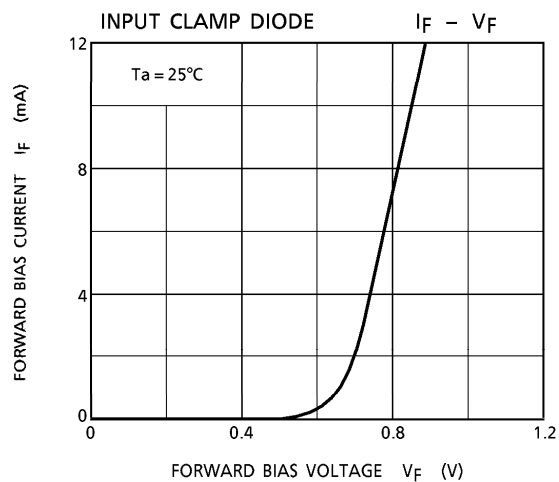
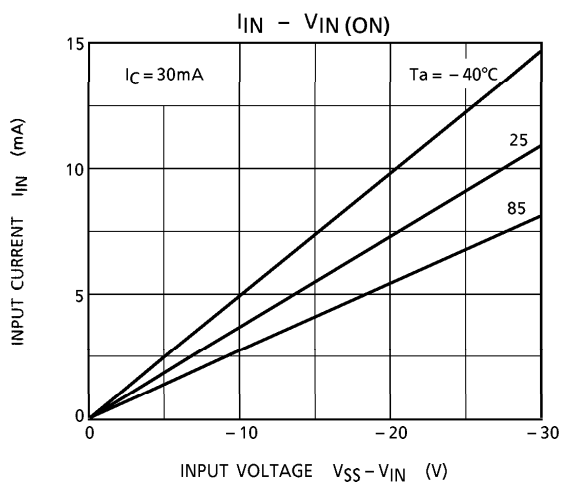
CHARACTERISTIC		SYMBOL	TEST CIRCUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Leakage Current		I _{CEX}	1	V _{CC} = 0V, V _{IN} = 0V V _{OUT} = - 55V	—	—	- 100	μA
Collector-Emitter Saturation Voltage		V _{CE} (sat)	2	I _{IN} = - 1mA, I _{OUT} = - 40mA	—	—	- 2.5	V
DC Current Transfer Ratio		h _{FE}	2	V _{CE} = - 5.0V, I _{OUT} = - 40mA	100	—	—	—
Input Current	Output On	V _{IN} (ON)	3	V _{CC} = 0V, V _{IN} = - 5.1V	—	- 1.7	- 2.4	mA
	Output Off	V _{IN} (OFF)		—	—	10	μA	
Input Voltage	Output On	V _{IN} (ON)	4	V _{CC} = 0V	- 3.0	—	—	V
	Output Off	V _{IN} (OFF)			—	—	- 0.44	
Turn-On Delay	P	t _{ON}	5	V _{CC} = 0V, V _{SUB} = V _{OUT} = - 55V R _L = 1.4kΩ, C _L = 15pF	—	1	—	μs
	F				—	0.5	—	
Turn-Off Delay	P	t _{OFF}			—	2	—	μs
	F				—	1	—	

TEST CIRCUIT
1. I_{CEX}

2. $V_{CE(sat)}$, h_{FE}

3. $I_{IN(ON)}$, $I_{IN(OFF)}$

4. $V_{IN(ON)}$, $V_{IN(OFF)}$

5. t_{ON} , t_{OFF}


- (Note 1) Pulse Width $50\mu s$, Duty Cycle 10%
Output Impedance 50Ω , $t_r \leq 10ns$, $t_f \leq 5ns$
(Note 2) C_L includes probe and jig capacitance.

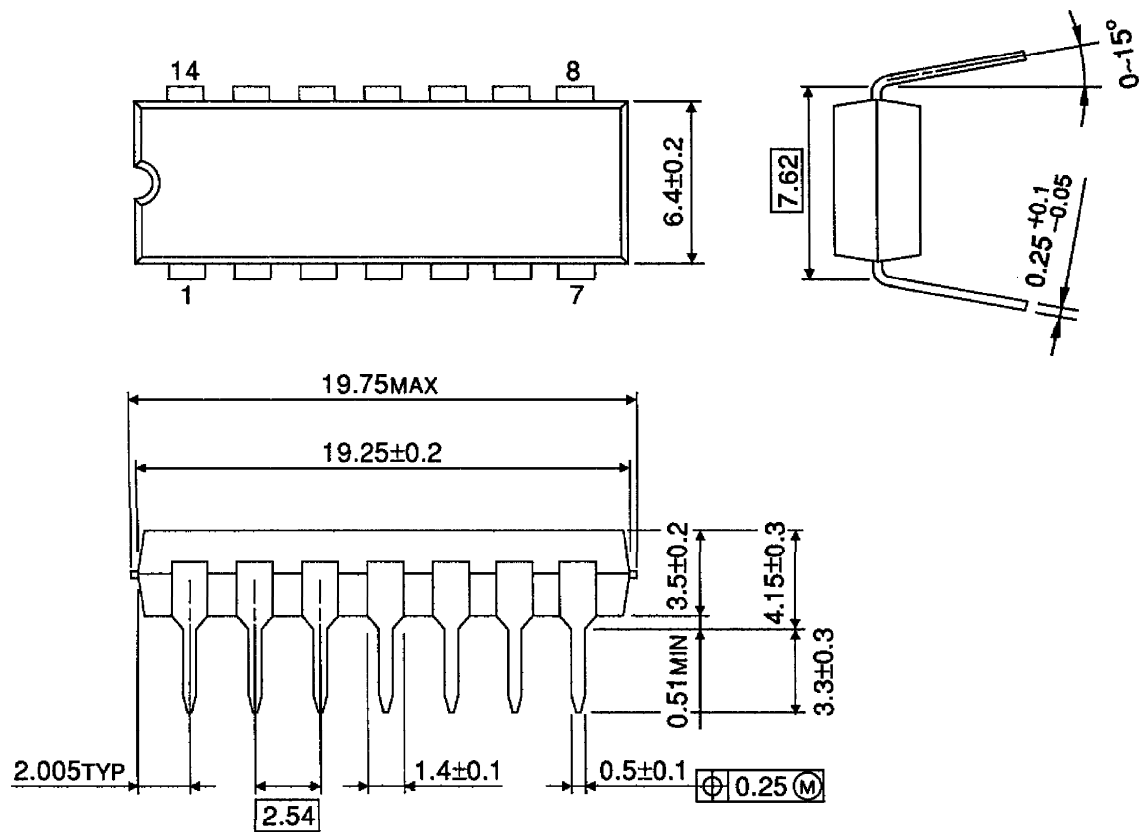
PRECAUTIONS for USING

Utmost care is necessary in the design of the output line, V_{CC} and GND (SUB) line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.



OUTLINE DRAWING
DIP14-P-300-2.54

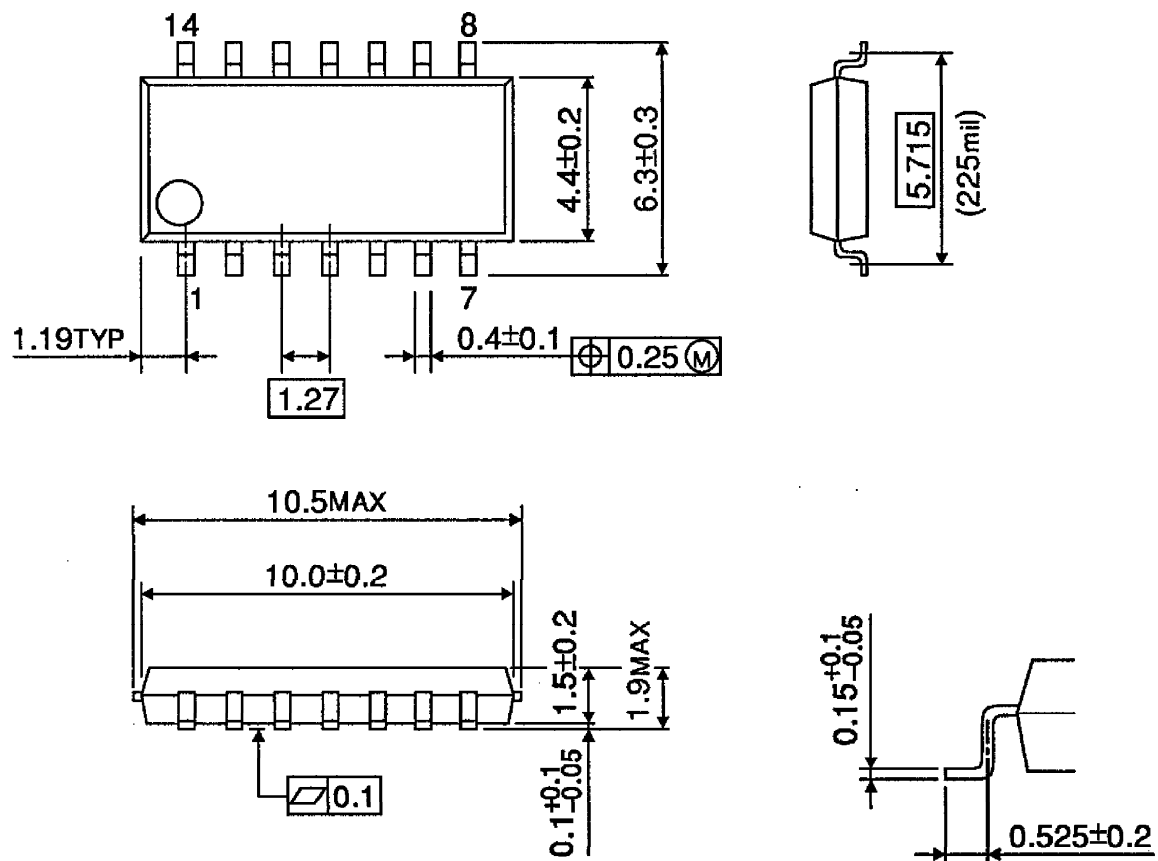
Unit : mm



Weight : 1.11g (Typ.)

OUTLINE DRAWING
SOP14-P-225-1.27

Unit : mm



Weight : 0.16g (Typ.)