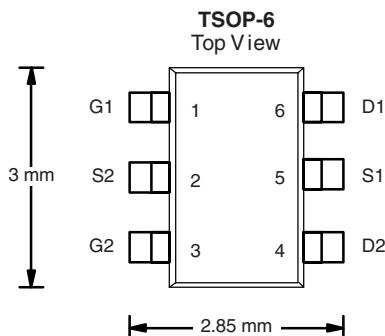


Dual P-Channel 12-V (D-S) MOSFET

PRODUCT SUMMARY		
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)
- 12	0.087 at $V_{GS} = - 4.5$ V	- 2.7
	0.120 at $V_{GS} = - 2.5$ V	- 2.3
	0.165 at $V_{GS} = - 1.8$ V	- 1.5

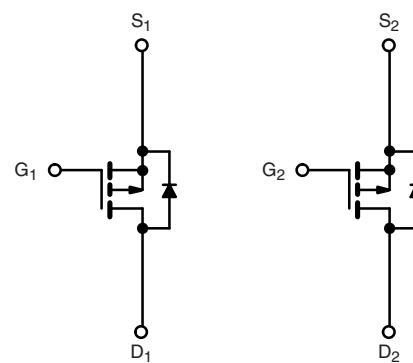
FEATURES

- Halogen free According to IEC61249-2-21 Definition
- TrenchFET® Power MOSFET
- Compliant to RoHS Directive 2002/95/EC



Ordering Information: Si3973DV-T1-E3 (Lead (Pb)-free)
Si3973DV-T1-GE3 (Lead (Pb)-free and Halogen free)

Marking Code: MBxxx



P-Channel MOSFET

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted					
Parameter	Symbol	5 s	Steady State	Unit	
Drain-Source Voltage	V_{DS}		- 12		V
Gate-Source Voltage	V_{GS}		± 8		
Continuous Drain Current ($T_J = 150$ °C) ^a	I_D ($T_A = 25$ °C)		- 2.7	- 2.4	A
	I_D ($T_A = 70$ °C)		- 2.2	- 1.9	
Pulsed Drain Current	I_{DM}		- 7		
Continuous Source Current (Diode Conduction) ^a	I_S	- 1.05	- 0.75		
Maximum Power Dissipation ^a	P_D ($T_A = 25$ °C)	1.15	0.83		W
	P_D ($T_A = 70$ °C)	0.73	0.53		
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	$t \leq 5$ s	R_{thJA}	93	110	°C/W
	Steady State		130	150	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	75	90	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

SPECIFICATIONS $T_J = 25^\circ\text{C}$, unless otherwise noted

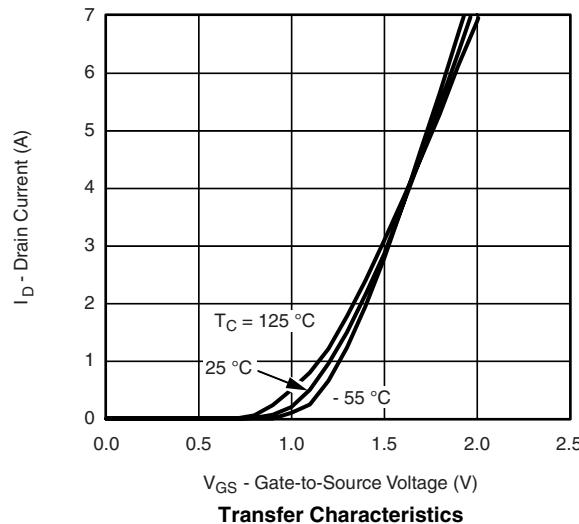
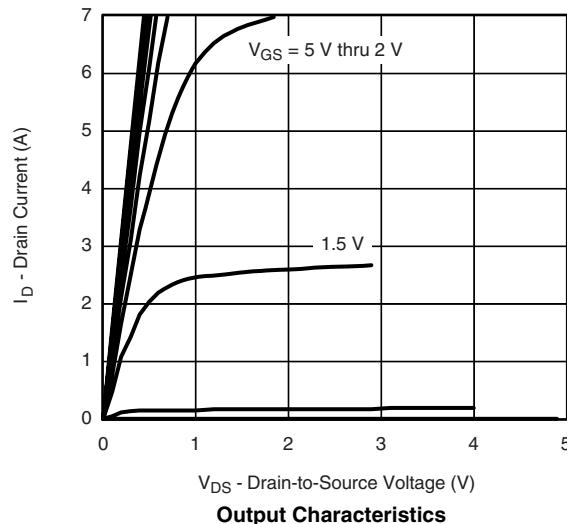
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$, $I_D = -250 \mu\text{A}$	- 0.40		- 0.9	V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}$, $V_{GS} = \pm 8 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -12 \text{ V}$, $V_{GS} = 0 \text{ V}$		- 1		μA
		$V_{DS} = -12 \text{ V}$, $V_{GS} = 0 \text{ V}$, $T_J = 55^\circ\text{C}$			- 5	
On-State Drain Current ^a	$I_{D(\text{on})}$	$V_{DS} \leq -5 \text{ V}$, $V_{GS} = -4.5 \text{ V}$	- 5			A
Drain-Source On-State Resistance ^a	$R_{DS(\text{on})}$	$V_{GS} = -4.5 \text{ V}$, $I_D = -2.7 \text{ A}$		0.070	0.087	Ω
		$V_{GS} = -2.5 \text{ V}$, $I_D = -2.3 \text{ A}$		0.096	0.120	
		$V_{GS} = -1.8 \text{ V}$, $I_D = -1 \text{ A}$		0.130	0.165	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -4.5 \text{ V}$, $I_D = -2.7 \text{ A}$		7		S
Diode Forward Voltage ^a	V_{SD}	$I_S = -1.05 \text{ A}$, $V_{GS} = 0 \text{ V}$		- 0.75	- 1.1	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = -6 \text{ V}$, $V_{GS} = -4.5 \text{ V}$, $I_D = -2.7 \text{ A}$		5.5	8.5	nC
Gate-Source Charge	Q_{gs}			0.8		
Gate-Drain Charge	Q_{gd}			1.6		
Gate Resistance	R_g			7.6		Ω
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{DD} = -6 \text{ V}$, $R_L = 6 \Omega$ $I_D \equiv -1 \text{ A}$, $V_{GEN} = -4.5 \text{ V}$, $R_g = 6 \Omega$		30	45	ns
Rise Time	t_r			60	90	
Turn-Off Delay Time	$t_{d(\text{off})}$			55	85	
Fall Time	t_f			45	70	
Source-Drain Reverse Recovery Time	t_{rr}	$I_F = -1.05 \text{ A}$, $di/dt = 100 \text{ A}/\mu\text{s}$		27	45	

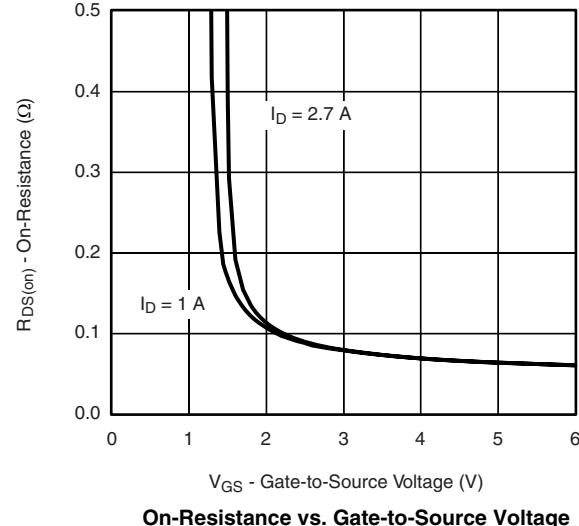
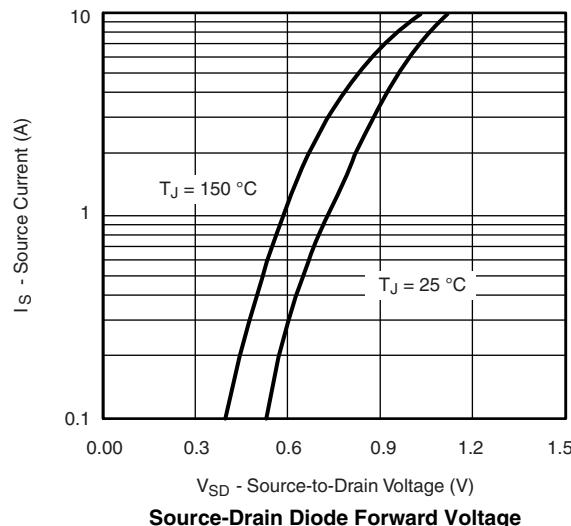
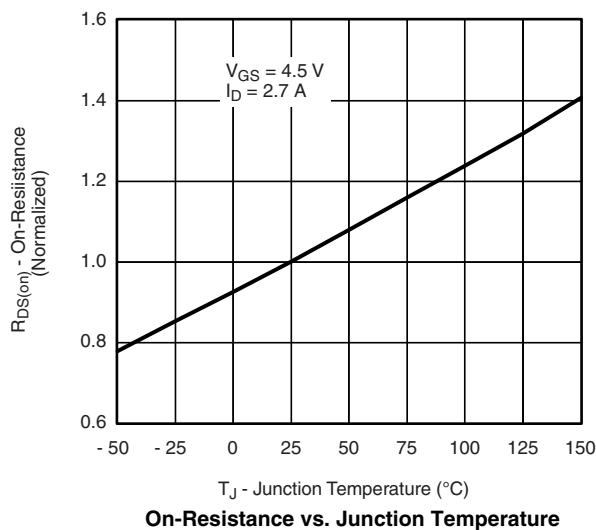
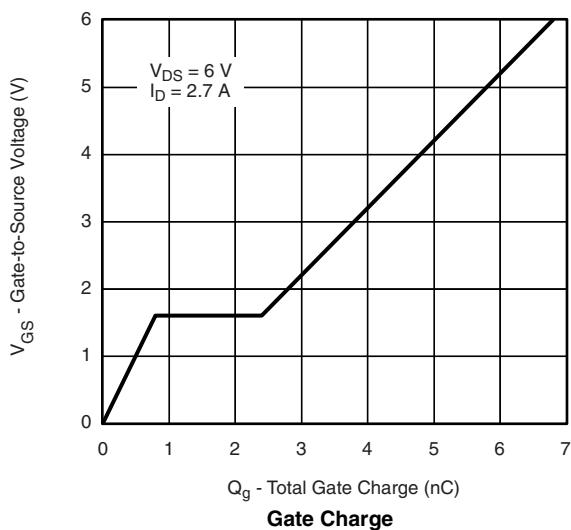
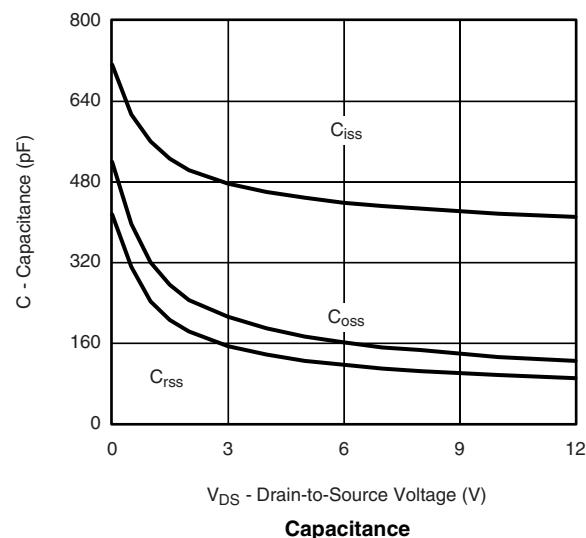
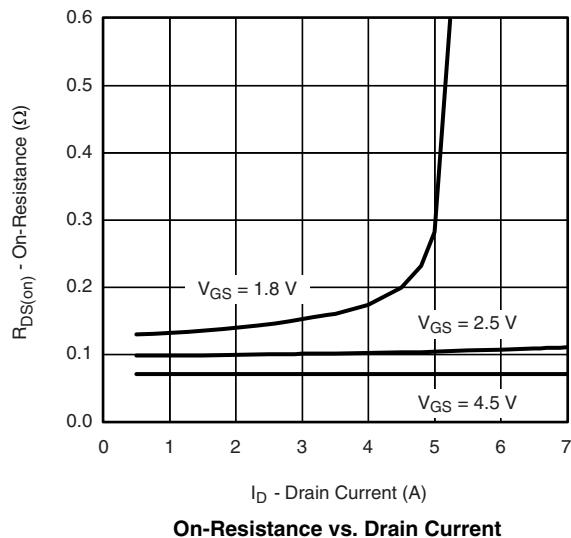
Notes:

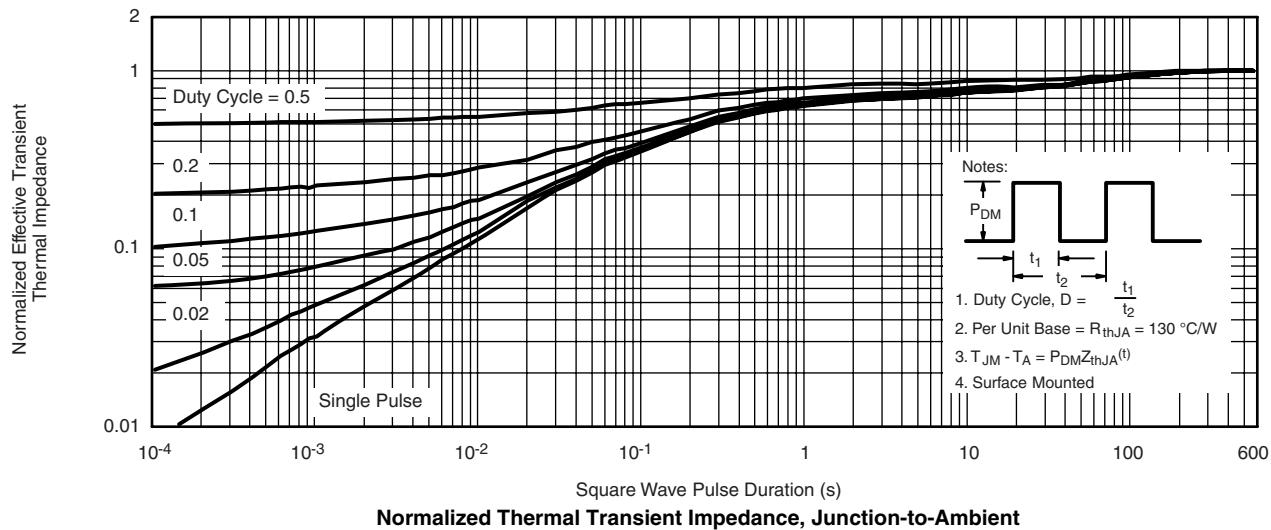
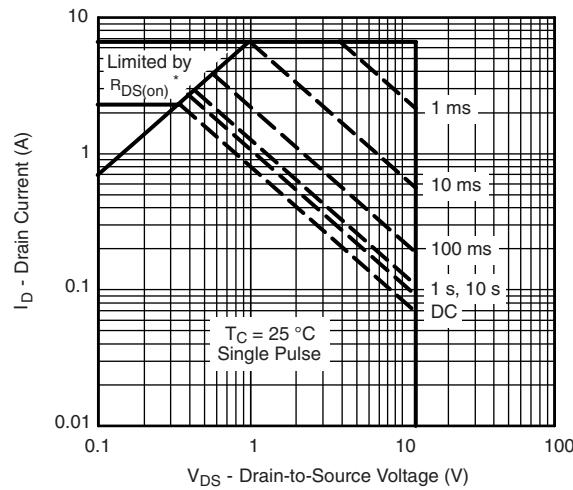
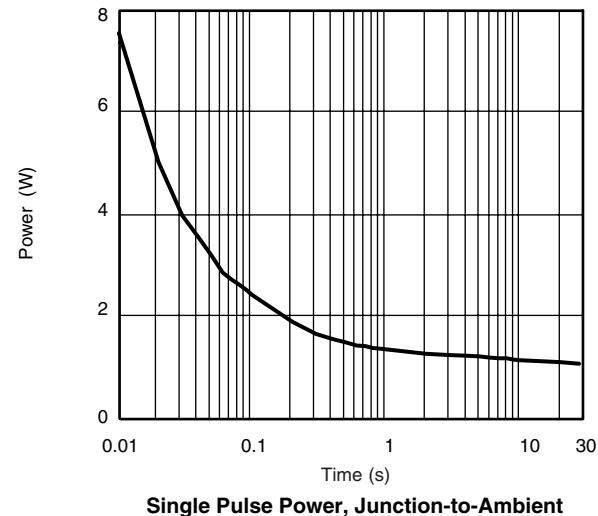
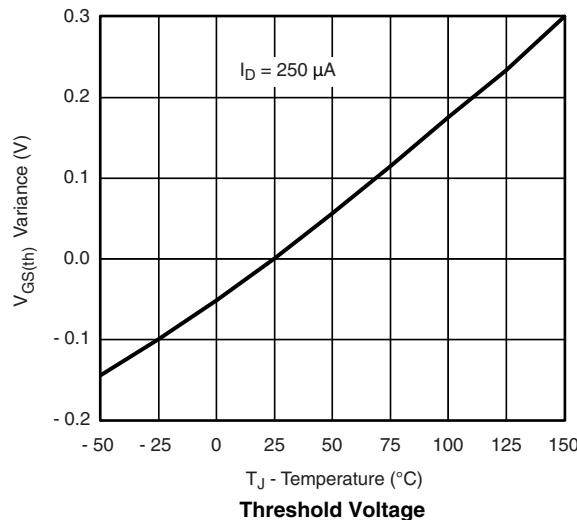
a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.

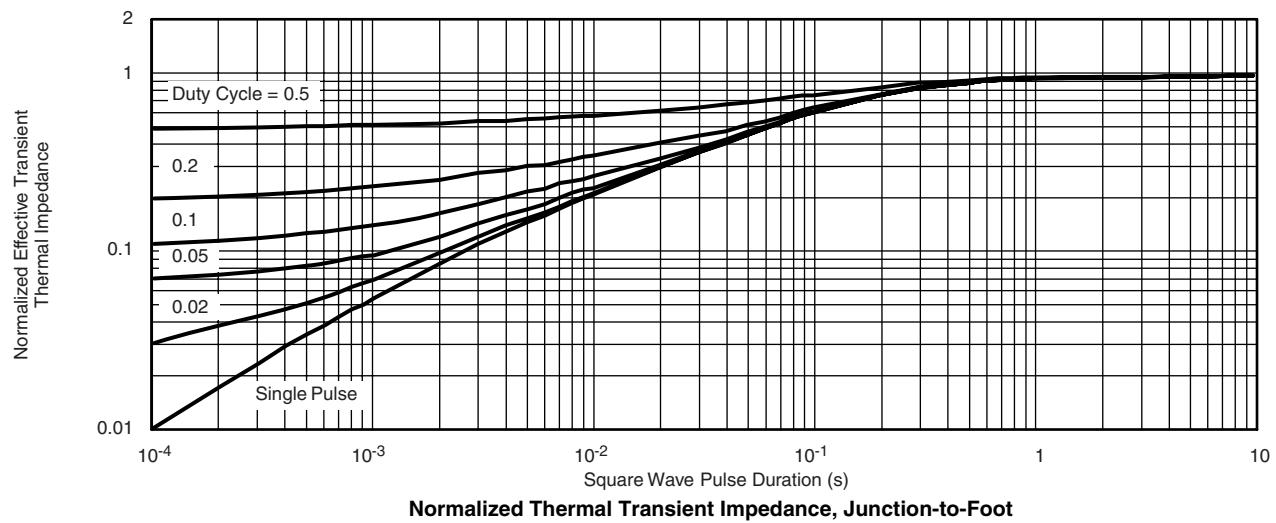
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25°C , unless otherwise noted


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