

## Product Summary

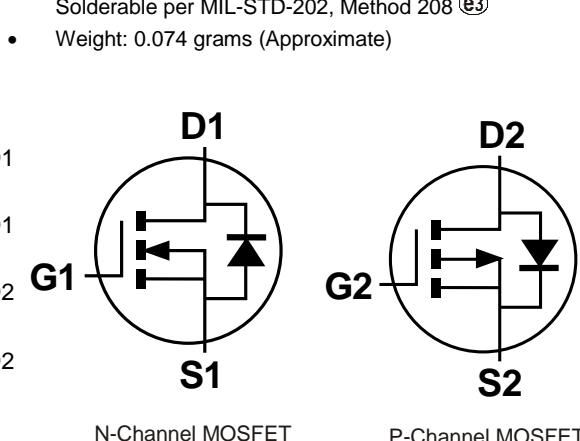
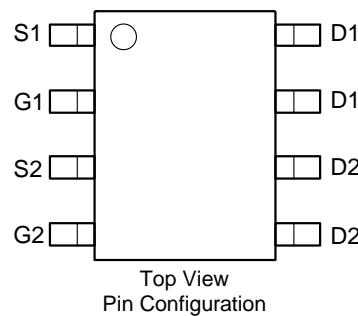
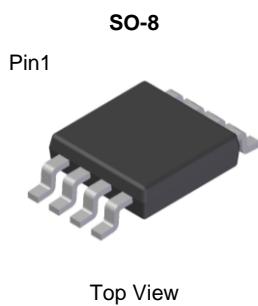
Device	$V_{(BR)DSS}$	$R_{DS(ON)}$ Max	$I_D$ $T_A = +25^\circ C$
Q1	30V	16m $\Omega$ @ $V_{GS} = 10V$	8.2A
		20m $\Omega$ @ $V_{GS} = 4.5V$	7.3A
Q2	-30V	28m $\Omega$ @ $V_{GS} = -10V$	-6.2A
		38m $\Omega$ @ $V_{GS} = -4.5V$	-5.2A

## Description

This new generation MOSFET is designed to minimize the on-state resistance ( $R_{DS(ON)}$ ), yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

## Applications

- DC-DC Converters
- Power Management Functions
- Backlighting



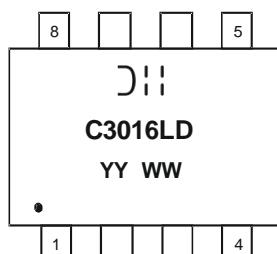
## Ordering Information (Note 4)

Part Number	Case	Packaging
DMC3016LSD-13	SO-8	2,500/Tape & Reel

## Notes:

- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

## Marking Information



○ = Manufacturer's Marking  
 C3016LD = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY or YY = Year (ex: 15 = 2015)  
 WW = Week (01 - 53)

**Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic			Symbol	Value Q1	Value Q2	Units
Drain-Source Voltage			$V_{DSS}$	30	-30	V
Gate-Source Voltage			$V_{GSS}$	$\pm 20$	$\pm 20$	V
Continuous Drain Current (Note 6) $V_{GS} = 10\text{V}$	Steady State	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	$I_D$	8.2 6.5	-6.2 -5.0	A
	$t < 10\text{s}$	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	$I_D$	10.5 8.4	-8.0 -6.4	A
Maximum Body Diode Forward Current (Note 6)			$I_S$	2.5	-2.5	A
Pulsed Drain Current (10 $\mu\text{s}$ Pulse, Duty Cycle = 1%)			$I_{DM}$	80	-40	A
Avalanche Current (Note 7) $L = 0.1\text{mH}$			$I_{AS}$	22	-22	A
Avalanche Energy (Note 7) $L = 0.1\text{mH}$			$E_{AS}$	25	25	mJ

**Thermal Characteristics**

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	$T_A = +25^\circ\text{C}$	$P_D$	1.2	W
	$T_A = +70^\circ\text{C}$		0.8	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	102	°C/W
	$t < 10\text{s}$		62	
Total Power Dissipation (Note 6)	$T_A = +25^\circ\text{C}$	$P_D$	1.6	W
	$T_A = +70^\circ\text{C}$		1.0	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	78	°C/W
	$t < 10\text{s}$		47	
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	12	
Operating and Storage Temperature Range		$T_J, T_{STG}$	-55 to +150	°C

**Electrical Characteristics** (N-Channel Q1) (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b> (Note 8)						
Drain-Source Breakdown Voltage	$BV_{DSS}$	30	—	—	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
Zero Gate Voltage Drain Current	$I_{DSS}$	—	—	1	$\mu\text{A}$	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	$I_{GSS}$	—	—	$\pm 100$	nA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
<b>ON CHARACTERISTICS</b> (Note 8)						
Gate Threshold Voltage	$V_{GS(th)}$	1.0	—	3.0	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	12	16	$\text{m}\Omega$	$V_{GS} = 10\text{V}, I_D = 12\text{A}$
		—	15	20		$V_{GS} = 4.5\text{V}, I_D = 10\text{A}$
Diode Forward Voltage	$V_{SD}$	—	0.7	1.0	V	$V_{GS} = 0\text{V}, I_S = 1\text{A}$
<b>DYNAMIC CHARACTERISTICS</b> (Note 9)						
Input Capacitance	$C_{ISS}$	—	1,415	—	pF	$V_{DS} = 15\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Output Capacitance	$C_{OSS}$	—	119	—		
Reverse Transfer Capacitance	$C_{RSS}$	—	82	—		
Gate Resistance	$R_G$	—	2.6	3.2	$\Omega$	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Total Gate Charge ( $V_{GS} = 4.5\text{V}$ )	$Q_G$	—	11.3	—	nC	$V_{DS} = 15\text{V}, I_D = 12\text{A}$
Total Gate Charge ( $V_{GS} = 10\text{V}$ )	$Q_G$	—	25.1	—		
Gate-Source Charge	$Q_{GS}$	—	3.5	—		
Gate-Drain Charge	$Q_{GD}$	—	3.6	—	ns	$V_{DD} = 15\text{V}, V_{GS} = 10\text{V}, R_L = 1.25\Omega, R_G = 3\Omega$
Turn-On Delay Time	$t_{D(ON)}$	—	4.8	—		
Turn-On Rise Time	$t_R$	—	16.5	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	26.1	—		
Turn-Off Fall Time	$t_F$	—	5.6	—		

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.

7. UIS in production with  $L = 0.1\text{mH}$ , starting  $T_A = +25^\circ\text{C}$ .

8. Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.

Electrical Characteristics (Continued) (P-Channel Q2) (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b> (Note 8)						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	-30	—	—	V	$\text{V}_{\text{GS}} = 0\text{V}$ , $\text{I}_D = -250\mu\text{A}$
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	—	—	-1	$\mu\text{A}$	$\text{V}_{\text{DS}} = -30\text{V}$ , $\text{V}_{\text{GS}} = 0\text{V}$
Gate-Source Leakage	$\text{I}_{\text{GSS}}$	—	—	$\pm 100$	nA	$\text{V}_{\text{GS}} = \pm 20\text{V}$ , $\text{V}_{\text{DS}} = 0\text{V}$
<b>ON CHARACTERISTICS</b> (Note 8)						
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	-1.0	—	-3.0	V	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}$ , $\text{I}_D = -250\mu\text{A}$
Static Drain-Source On-Resistance	$\text{R}_{\text{DS(ON)}}$	—	21	28	$\text{m}\Omega$	$\text{V}_{\text{GS}} = -10\text{V}$ , $\text{I}_D = -7\text{A}$
		—	30	38		$\text{V}_{\text{GS}} = -4.5\text{V}$ , $\text{I}_D = -6.2\text{A}$
Diode Forward Voltage	$\text{V}_{\text{SD}}$	—	-0.7	-1.2	V	$\text{V}_{\text{GS}} = 0\text{V}$ , $\text{I}_S = -2.1\text{A}$
<b>DYNAMIC CHARACTERISTICS</b> (Note 9)						
Input Capacitance	$\text{C}_{\text{ISS}}$	—	1,241	—	pF	$\text{V}_{\text{DS}} = -15\text{V}$ , $\text{V}_{\text{GS}} = 0\text{V}$ $f = 1.0\text{MHz}$
Output Capacitance	$\text{C}_{\text{OSS}}$	—	147	—		
Reverse Transfer Capacitance	$\text{C}_{\text{RSS}}$	—	110	—		
Gate Resistance	$\text{R}_{\text{G}}$	—	15	—	$\Omega$	$\text{V}_{\text{DS}} = 0\text{V}$ , $\text{V}_{\text{GS}} = 0\text{V}$ , $f = 1.0\text{MHz}$
Total Gate Charge ( $\text{V}_{\text{GS}} = -4.5\text{V}$ )	$\text{Q}_{\text{G}}$	—	10.9	—	nC	$\text{V}_{\text{DS}} = -15\text{V}$ , $\text{I}_D = -7\text{A}$
Total Gate Charge ( $\text{V}_{\text{GS}} = -10\text{V}$ )	$\text{Q}_{\text{G}}$	—	22	—		
Gate-Source Charge	$\text{Q}_{\text{GS}}$	—	3.5	—		
Gate-Drain Charge	$\text{Q}_{\text{GD}}$	—	4.7	—		
Turn-On Delay Time	$\text{t}_{\text{D(ON)}}$	—	9.7	—	ns	$\text{V}_{\text{DS}} = -15\text{V}$ , $\text{I}_D = -7\text{A}$ $\text{V}_{\text{GS}} = -10\text{V}$ , $\text{R}_{\text{G}} = 6\Omega$
Turn-On Rise Time	$\text{t}_{\text{R}}$	—	17.1	—		
Turn-Off Delay Time	$\text{t}_{\text{D(OFF)}}$	—	60.5	—		
Turn-Off Fall Time	$\text{t}_{\text{F}}$	—	40.4	—		

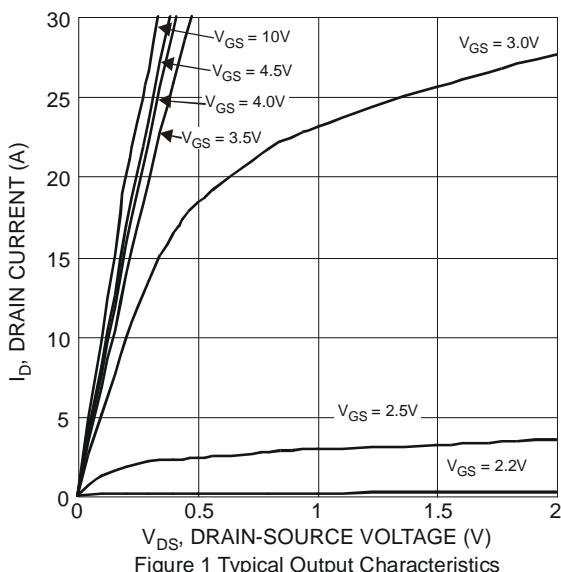


Figure 1 Typical Output Characteristics

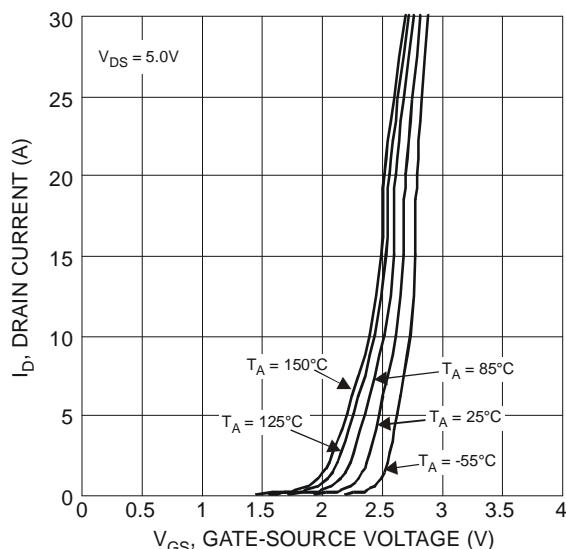


Figure 2 Typical Transfer Characteristics

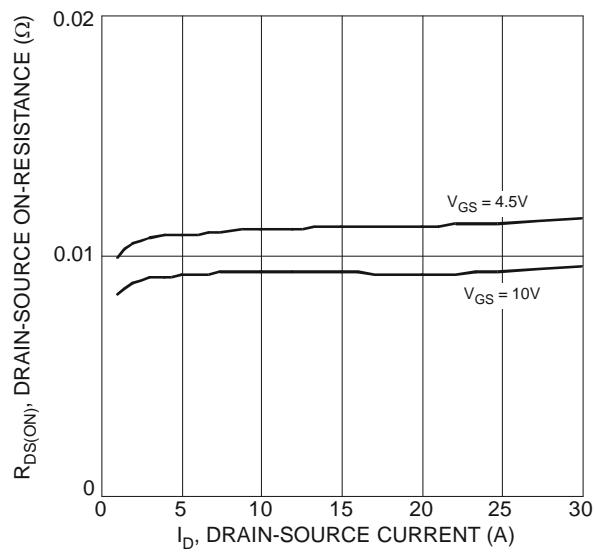


Figure 3 Typical On-Resistance vs.  
Drain Current and Gate Voltage

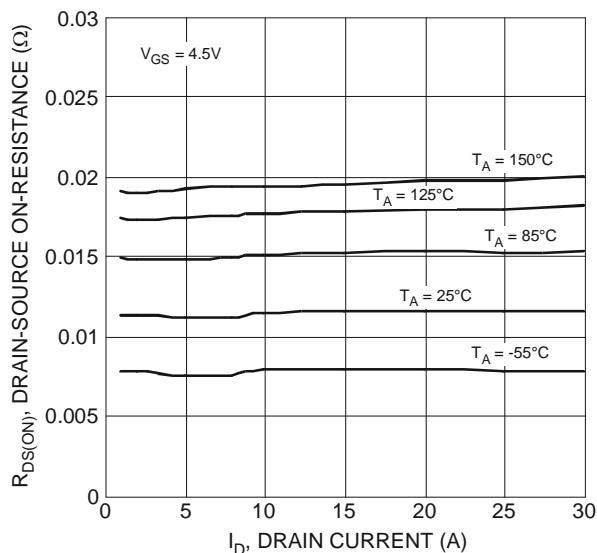


Figure 4 Typical On-Resistance vs.  
Drain Current and Temperature

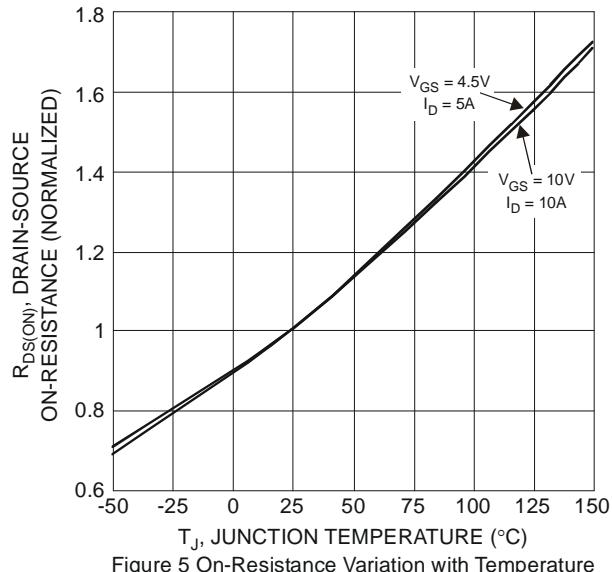


Figure 5 On-Resistance Variation with Temperature

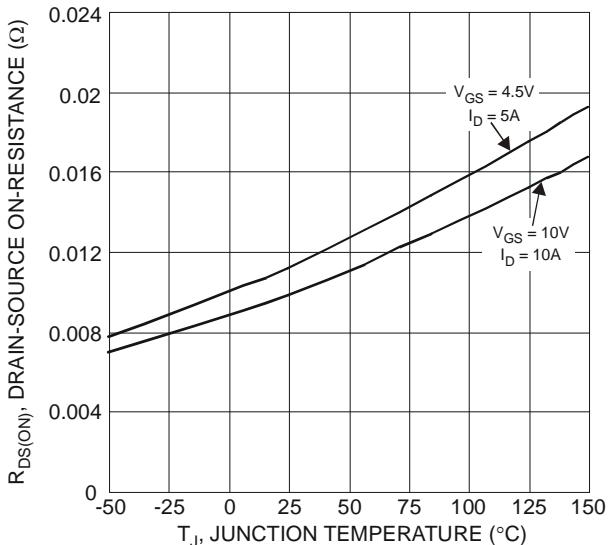


Figure 6 On-Resistance Variation with Temperature

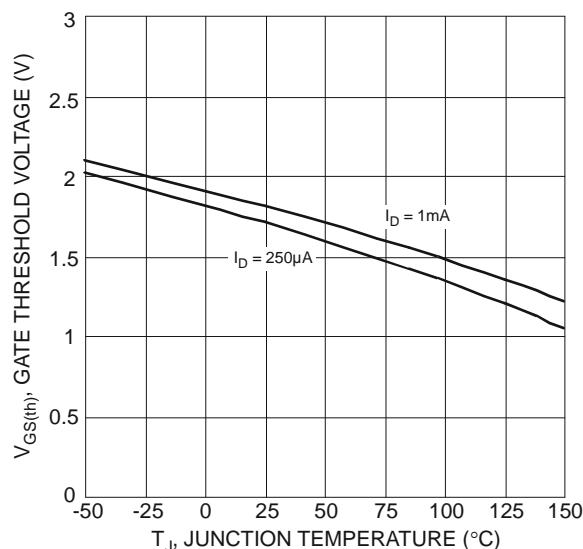


Figure 7 Gate Threshold Variation vs. Ambient Temperature

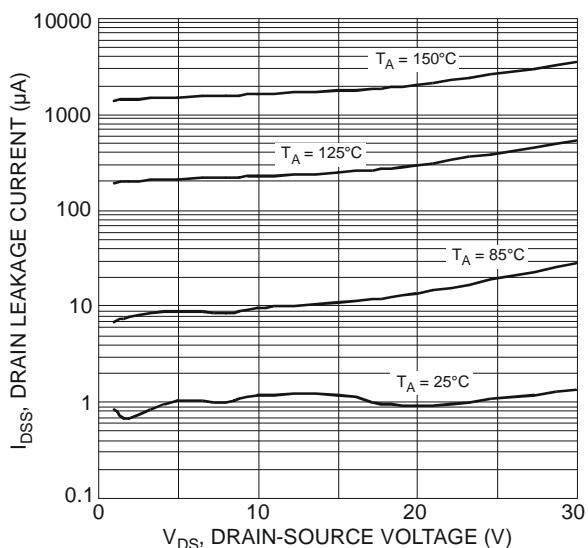


Figure 9 Typical Drain-Source Leakage Current vs. Voltage

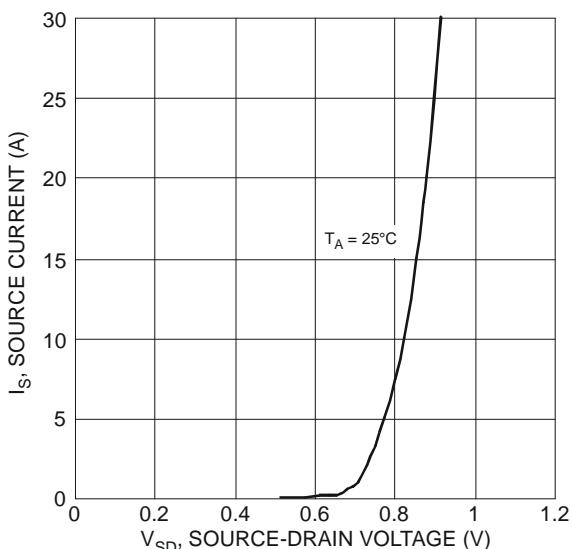


Figure 8 Diode Forward Voltage vs. Current

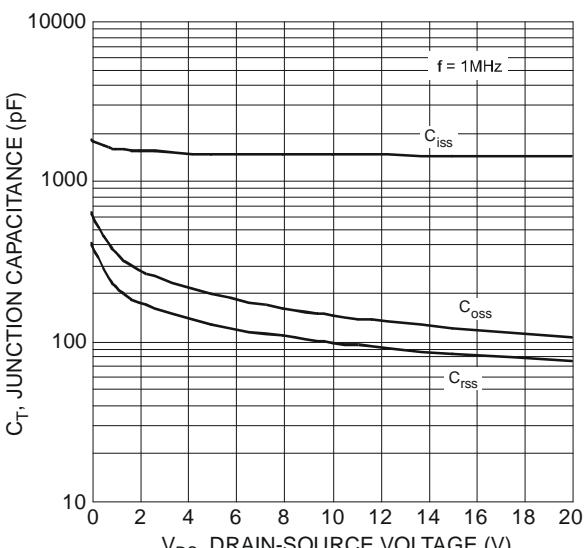


Figure 10 Typical Junction Capacitance

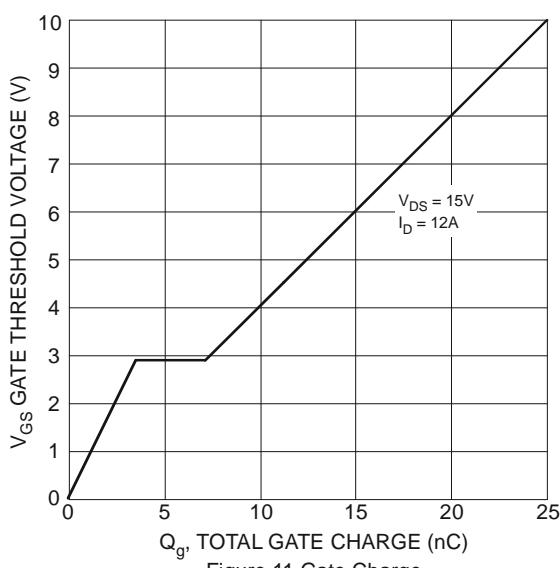


Figure 11 Gate Charge

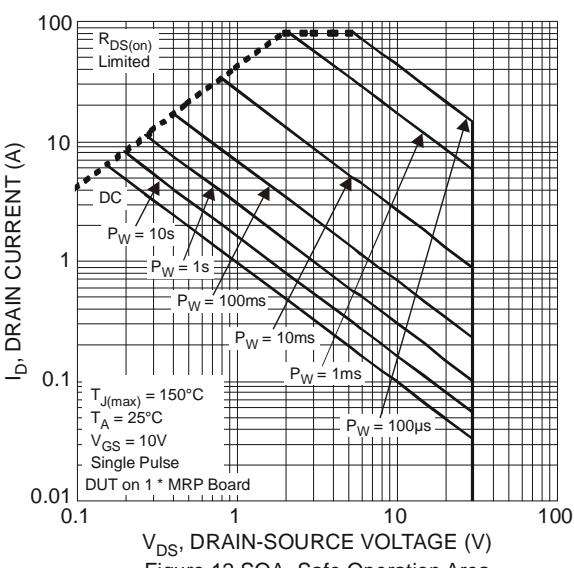


Figure 12 SOA, Safe Operation Area

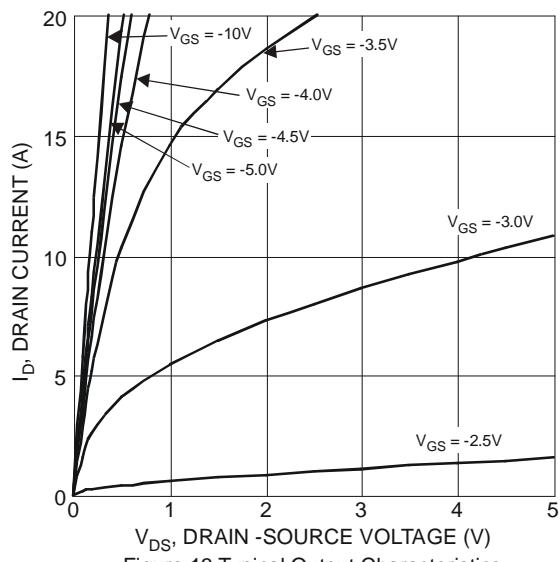


Figure 13 Typical Output Characteristics

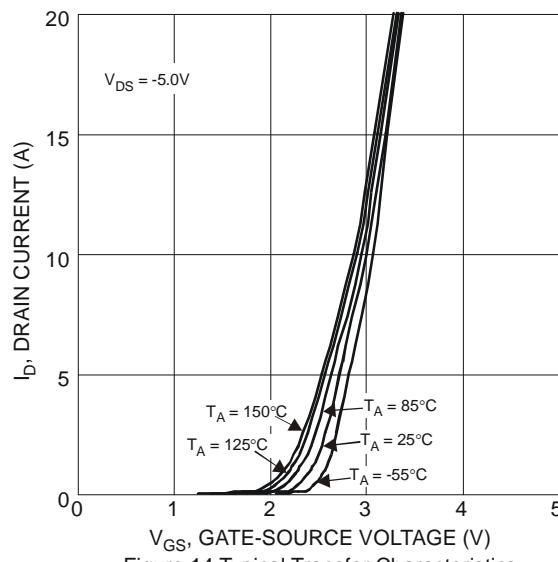


Figure 14 Typical Transfer Characteristics

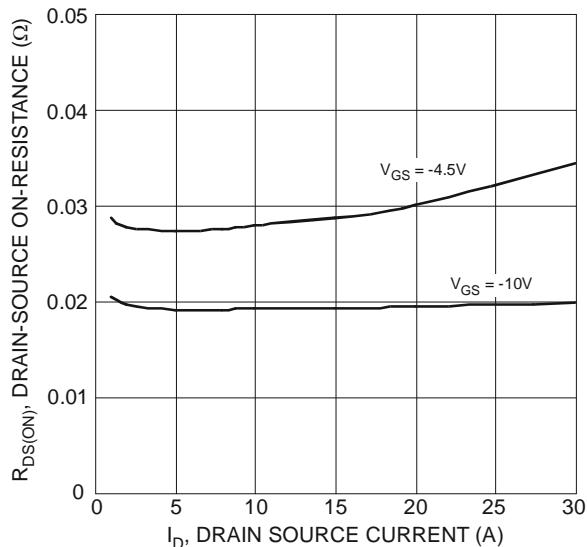


Figure 15 Typical On-Resistance vs.  
Drain Current and Gate Voltage

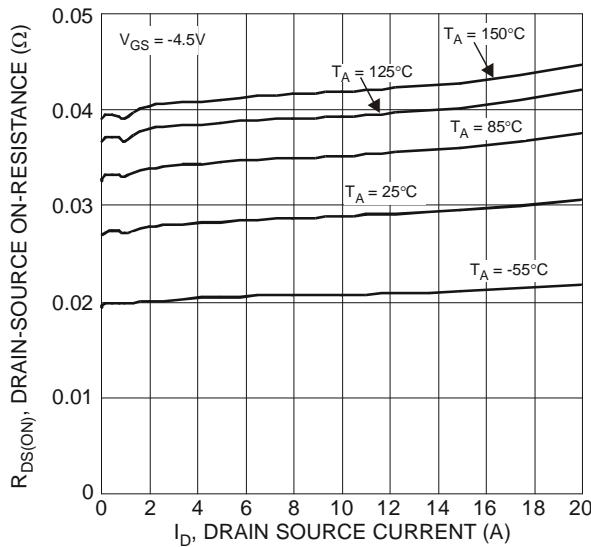


Figure 16 Typical On-Resistance vs.  
Drain Current and Temperature

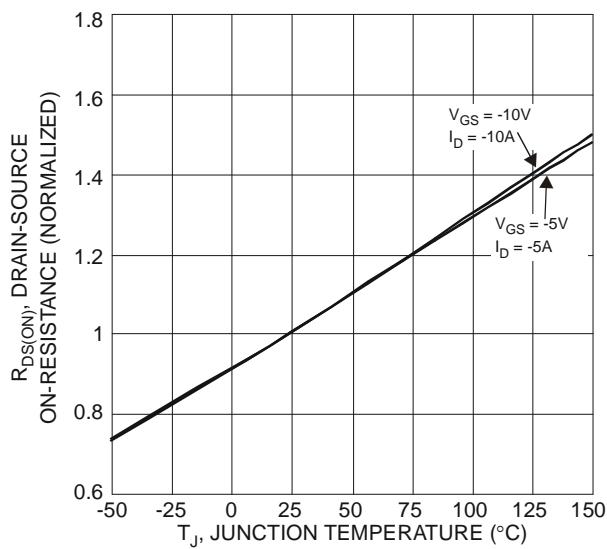


Figure 17 On-Resistance Variation with Temperature

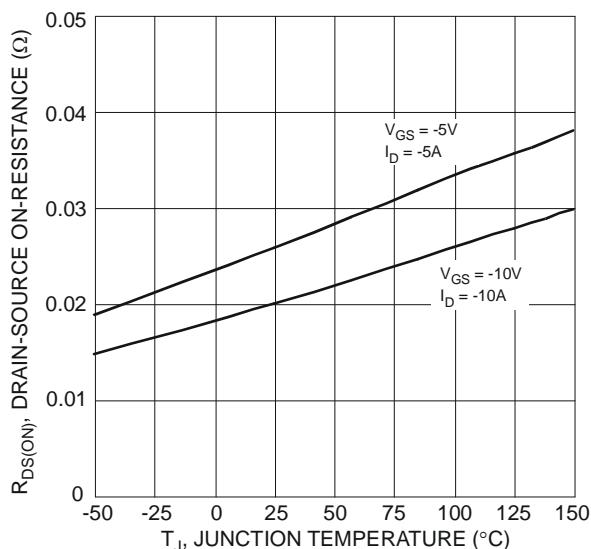


Figure 18 On-Resistance Variation with Temperature

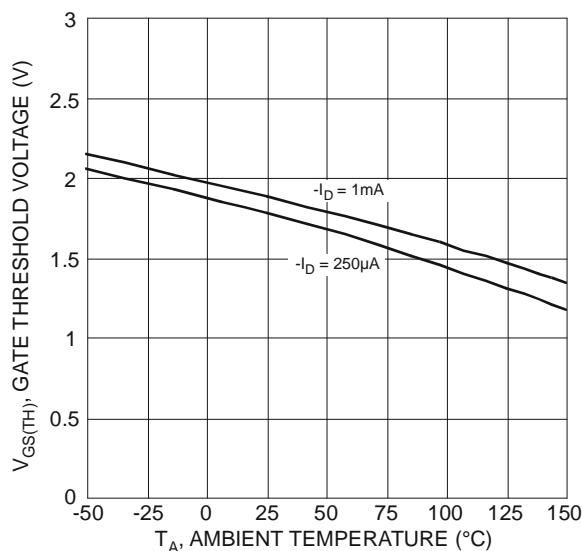


Figure 19 Gate Threshold Variation vs. Ambient Temperature

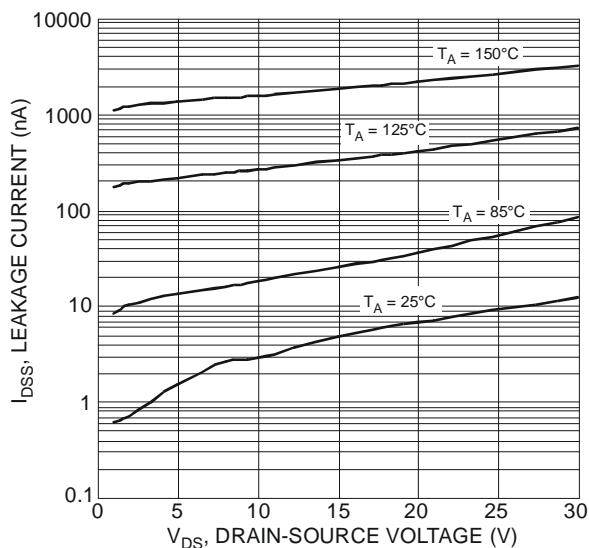


Figure 21 Typical Drain-Source Leakage Current vs. Voltage

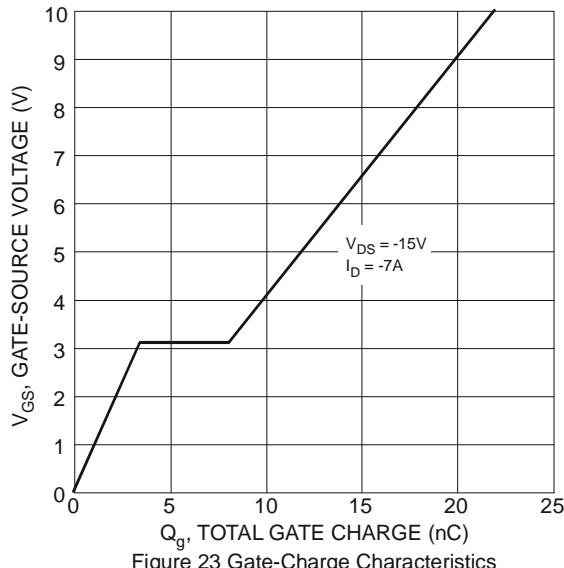


Figure 23 Gate-Charge Characteristics

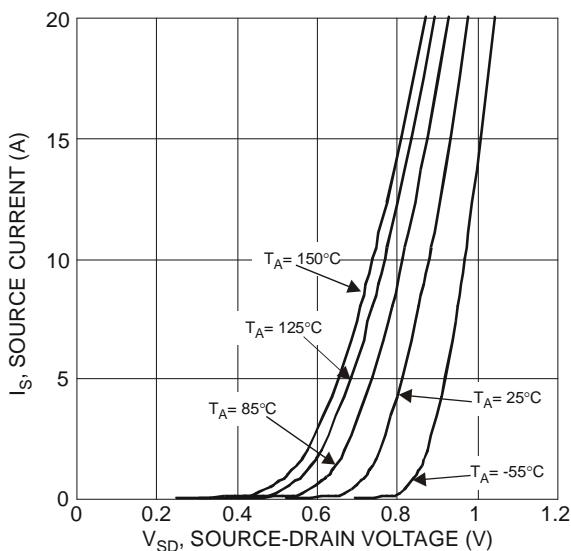


Figure 20 Diode Forward Voltage vs. Current

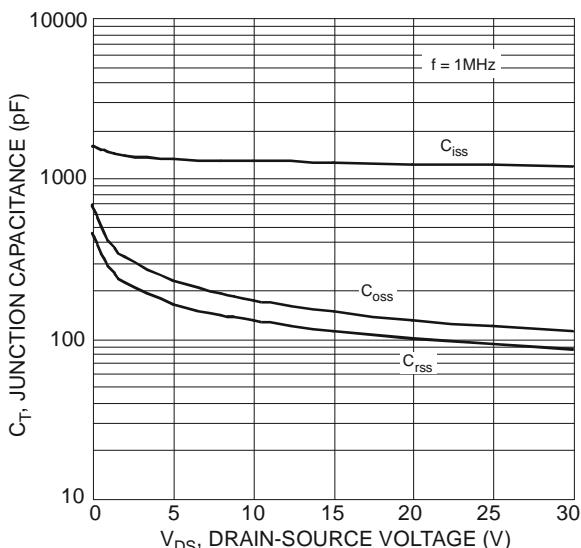


Figure 22 Typical Junction Capacitance

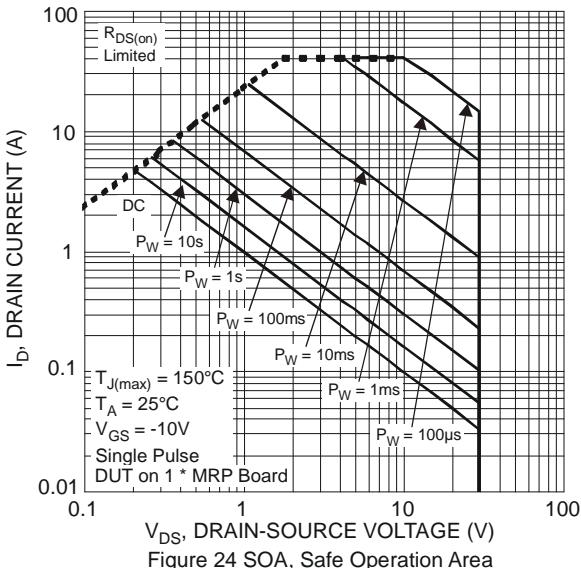
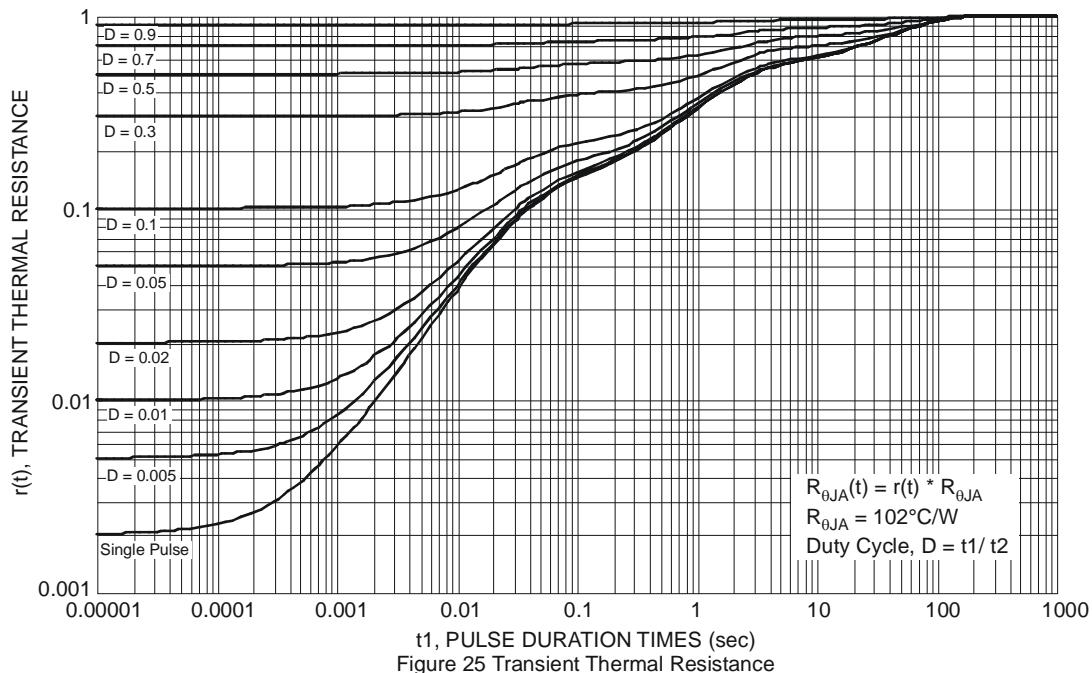
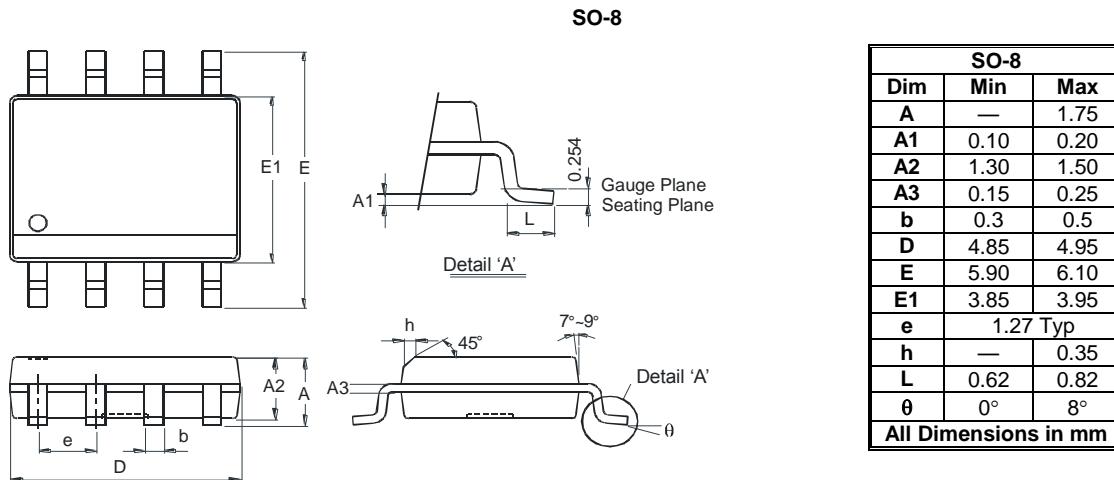


Figure 24 SOA, Safe Operation Area



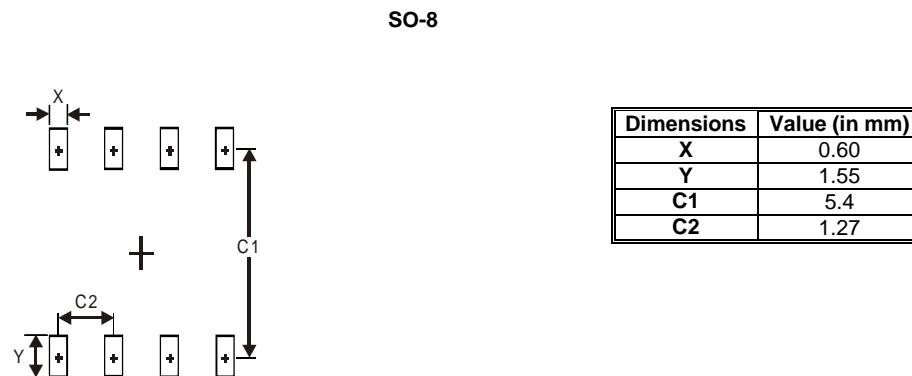
## Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



## Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



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