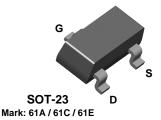


PN4117 **PN4118 PN4119**

MMBF4117 MMBF4118 MMBF4119





NOTE: Source & Drain are interchangeable

N-Channel Switch

This device is designed for low current DC and audio applications. These devices provide excellent performance as input stages for sub-picoamp instrumentation or any high impedance signal sources. Sourced from Process 53.

Absolute Maximum Ratings* TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{DG}	Drain-Gate Voltage	40	V
V _{GS}	Gate-Source Voltage	- 40	V
I _{GF}	Forward Gate Current	50	mA
T _J ,T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

^{*}These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

1) These ratings are based on a maximum junction temperature of 150 degrees C.
2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics TA = 25°C unless otherwise noted

Symbol	Characteristic	Max		Units
		PN4117-4119	*MMBF4117-4119	
P_D	Total Device Dissipation Derate above 25°C	350 2.8	225 1.8	mW mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125		°C/W
R _{θJA}	Thermal Resistance, Junction to Ambient	357	556	°C/W

^{*}Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

N-Channel Switch

(continued)

μmhos

μmhos

μmhos pF

рF

Symbol	Parameter	Test Conditions	Min	Max	Units
OFF CHA	RACTERISTICS				
V _{(BR)GSS}	Gate-Source Breakdown Voltage	I _G = - 1.0 μA, V _{DS} = 0	- 40		V
I _{GSS}	Gate Reverse Current	V _{GS} = - 20 V, V _{DS} = 0 V _{GS} = - 20 V, V _{DS} = 0, T _A = 150°C		- 10 - 25	pA nA
$V_{GS(off)}$	Gate-Source Cutoff Voltage	V _{DS} = - 10 V, I _D = 1.0 nA 4117 4118 4119	- 0.6 - 1.0 - 2.0	- 1.8 - 3.0 - 6.0	V V
	RACTERISTICS		00		1 .
	ZACTERISTICS Zero-Gate Voltage Drain Current*	V _{DS} = 10 V, V _{GS} = 0 4117 4118 4119	30 80 200	90 240 600	μΑ μΑ μΑ
I _{DSS}		4118	80	240	
I _{DSS}	Zero-Gate Voltage Drain Current*	4118 4119	80	240	μA

 $V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 30 \text{ MHz}$

 $V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1.0 \text{ kHz}$

 $V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1.0 \text{ MHz},$

4117

4118

4119

60

70

90

3.0

1.5

R_{e(yfs)}

 C_{iss}

 C_{rss}

Common-Source Forwad

Reverse Transfer Capacitance

Transconductance

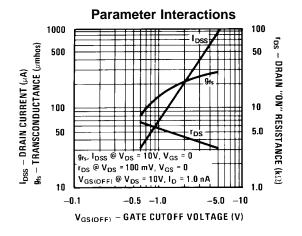
Input Capacitance

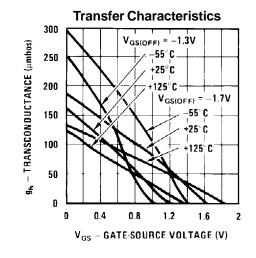
^{*}Pulse Test: Pulse Width $\leq\!300\,\mu\text{s},\,\text{Duty Cycle}\,\!\leq\!1.0\%$

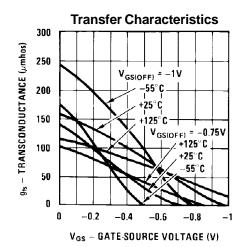
N-Channel Switch

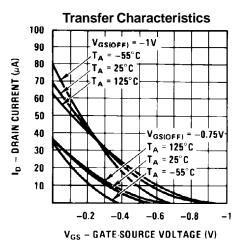
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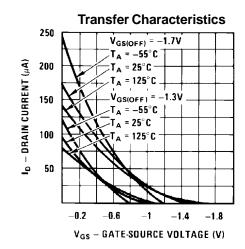
Typical Characteristics

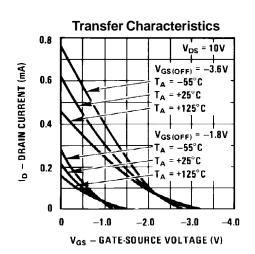








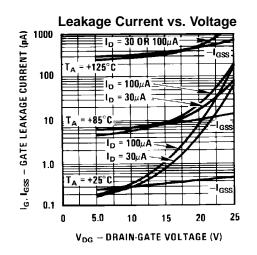


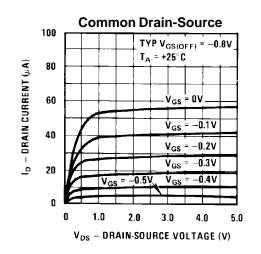


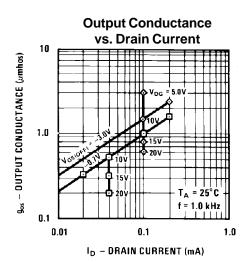
N-Channel Switch

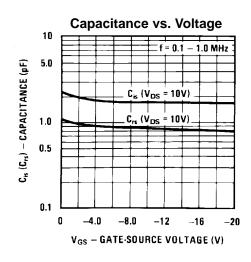
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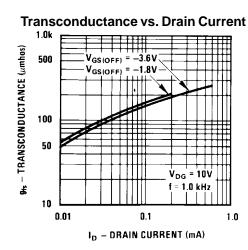
Typical Characteristics (continued)

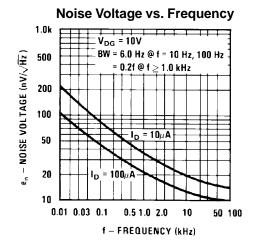












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