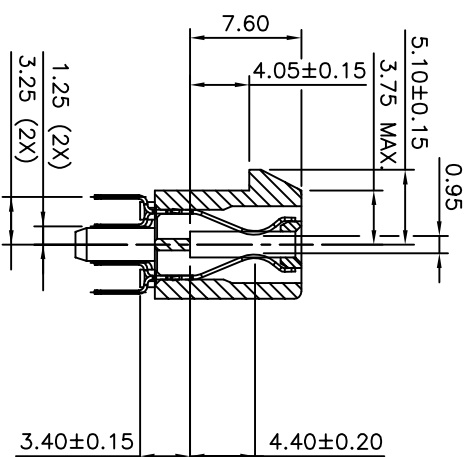
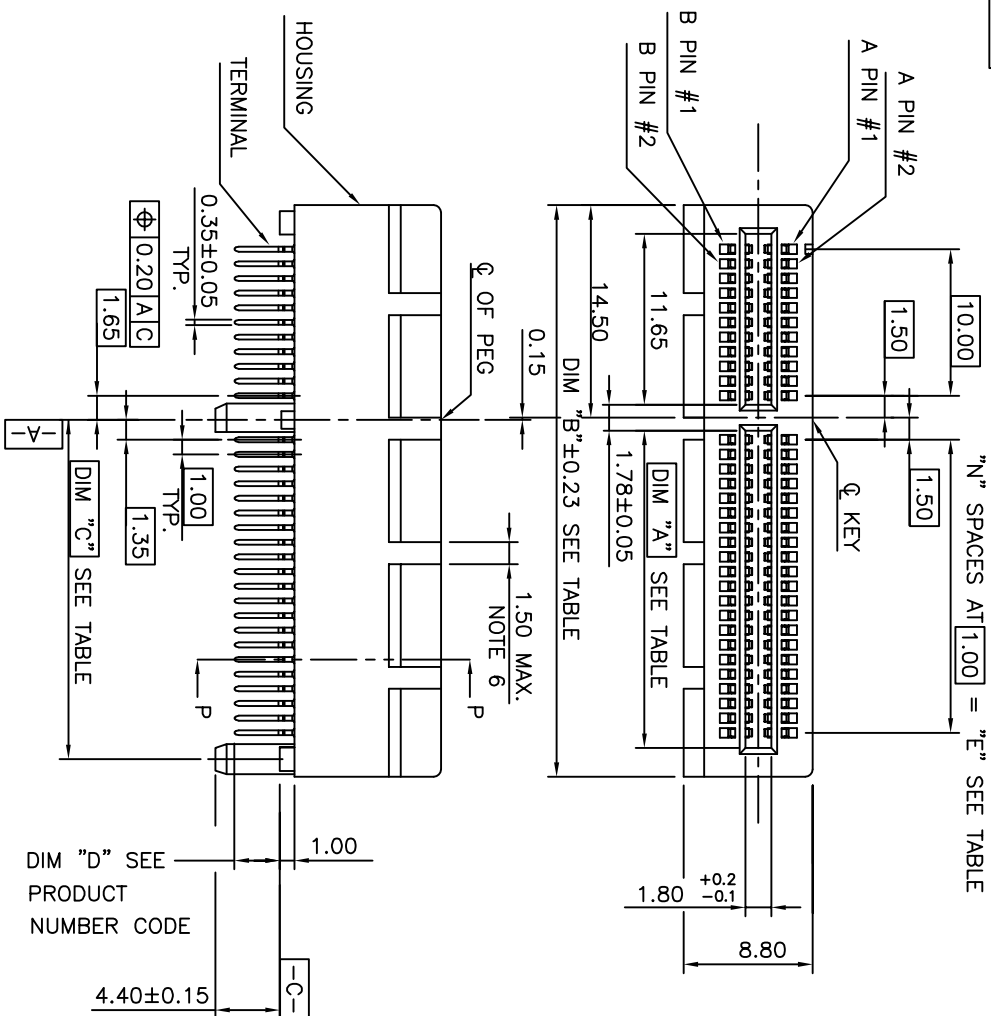




SECTION P-P

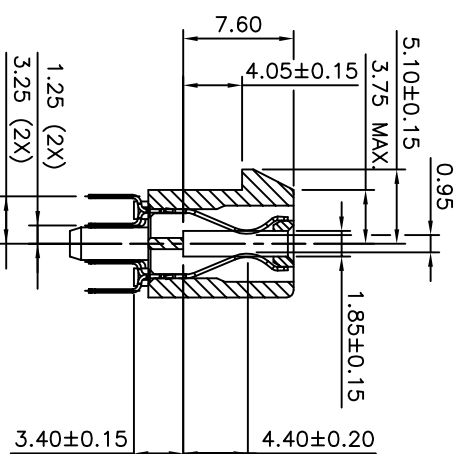
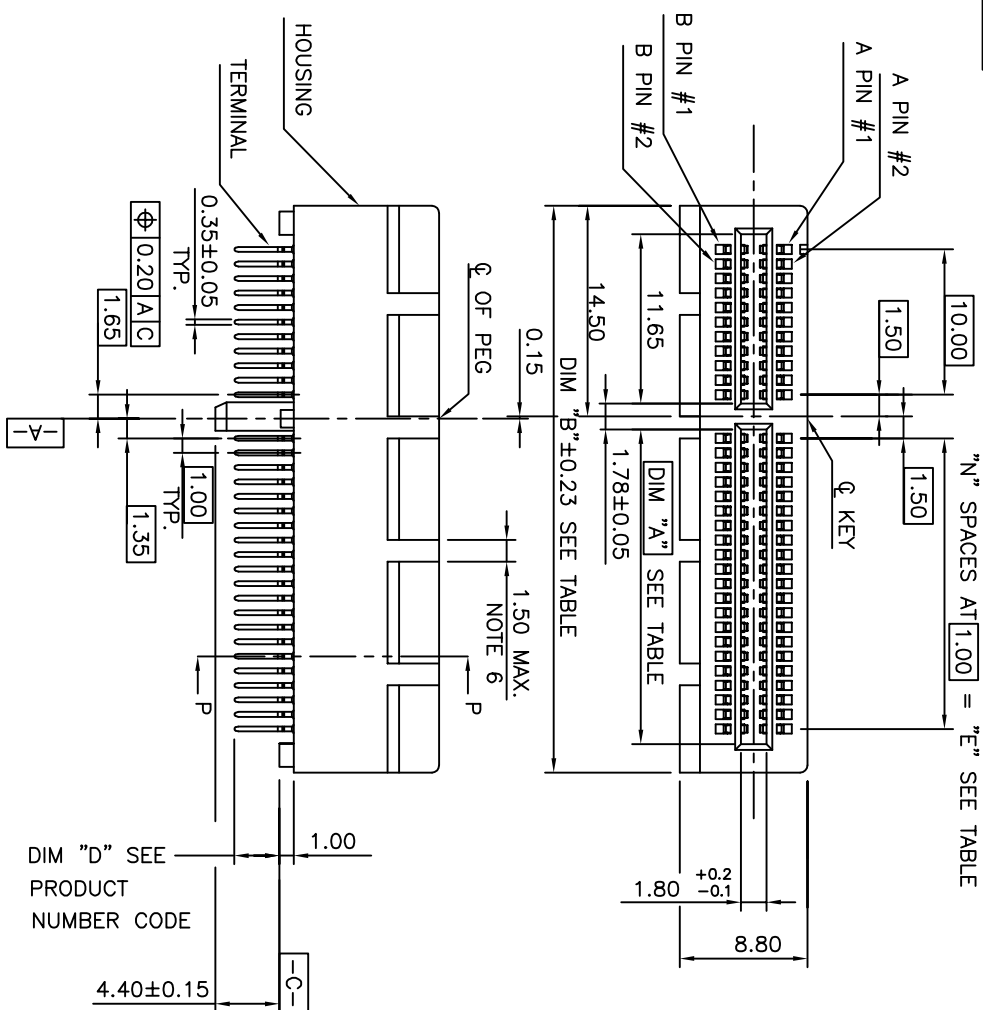


FCI LOGO SIDE

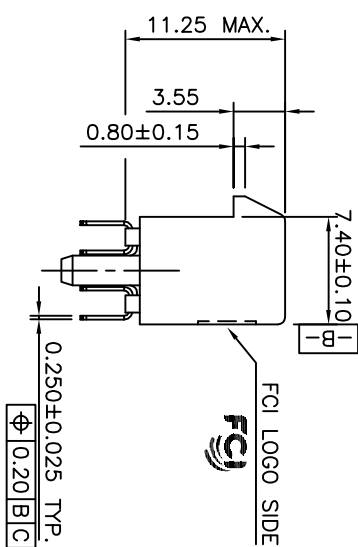
± 0.025 ТР.

TWIN PLASTIC PEG TYPE

mat'l. code				tolerances unless otherwise specified				CUSTOMER COPY		FCI www.fciconnect.com											
ltr	ecn no	dr	dtle	.X ± 0.30				projection		title											
A	TD9-1162	J H	10/29/09	.XX ± 0.20						PCI EXPRESS											
B	T-004920	J H	6/8/11	.XXX ± 0.10						CARD EDGE ASSY											
C	06-011582	HT	5/10/12	0° ±2°																	
D ELX-06-013206 WL			07/24/12	dr	JASON HSU	10/29/09															
					engr JASON HSU	10/29/09															
					chr GARY HSEH	10/29/09															
					ppd JOSEPH HSIA	10/29/09															
sheet index	revision sheet	D A	A A	A A	D A	A A	scale 2:1				product family		EDGE CARD		code						
											size		dwg no		TWN						
											A4		10108777		sheet 1 of 7						

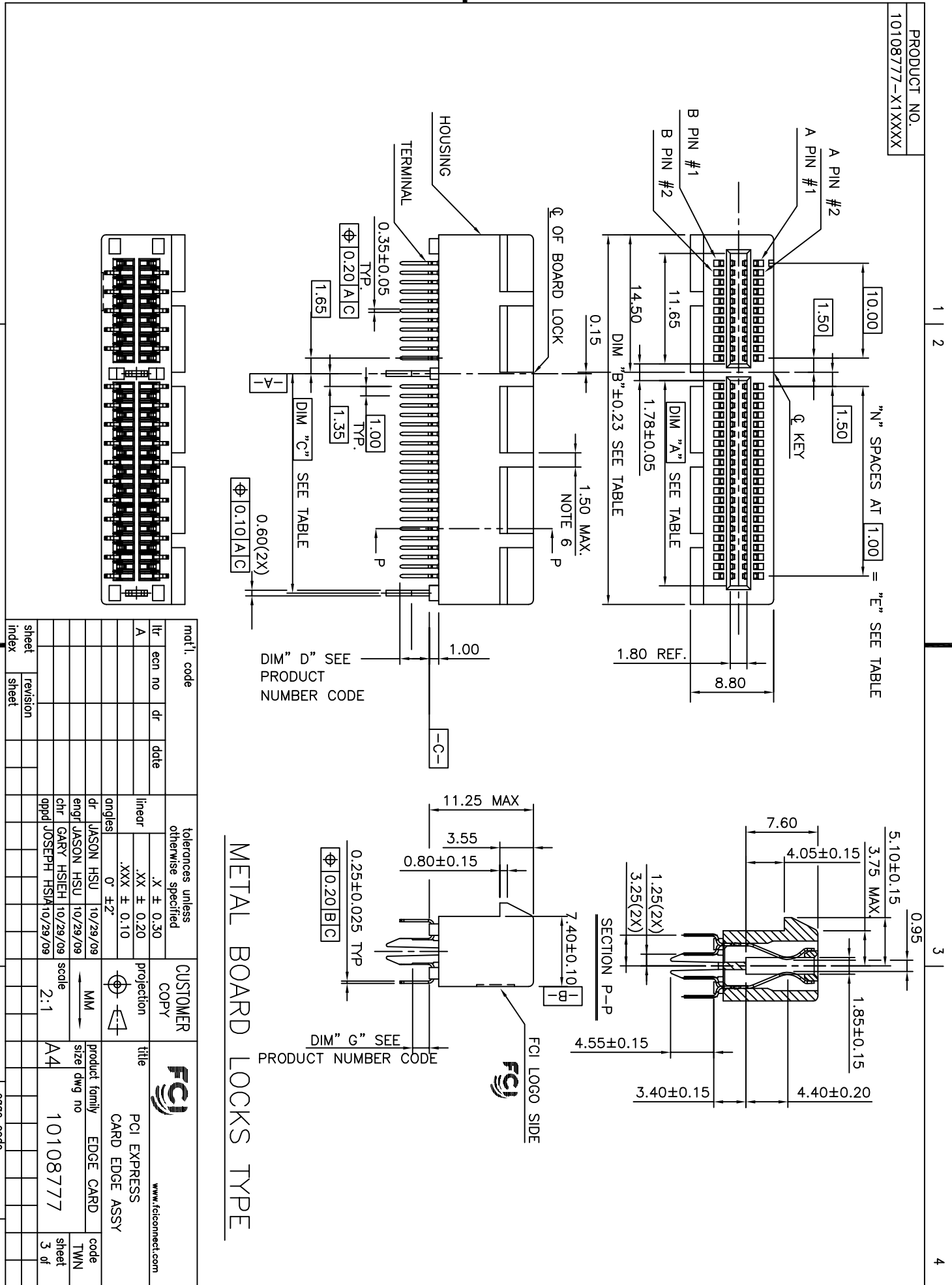


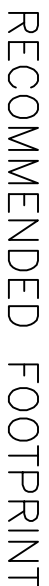
SECTION P-P

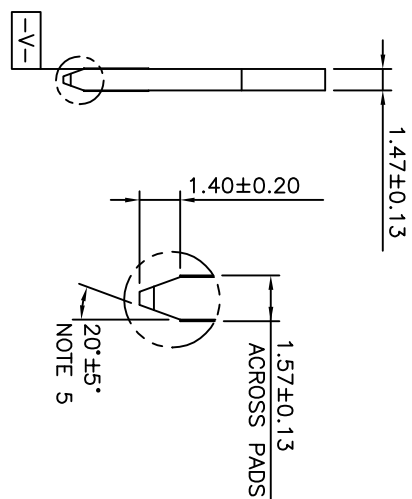
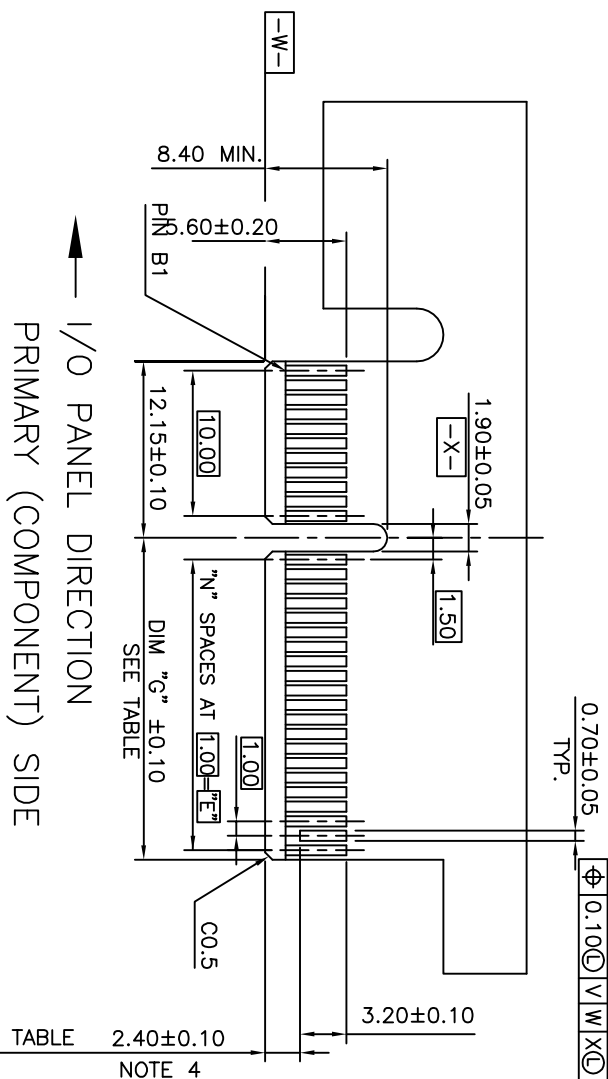


SINGLE PLASTIC PEG TYPE

[illegible]



cage code



SCALE 2:1

Diagram illustrating the I/O panel direction and primary/secondary sides of a component. The diagram shows a component with a central vertical axis. The left side is labeled "I/O PANEL DIRECTION" with an arrow pointing left, and the right side is labeled "PRIMARY (COMPONENT) SIDE" with an arrow pointing right. The component has a central section labeled "PIN A1" and a bottom section labeled "3.20". The overall width is indicated as 0 ± 0.10 .

#	POS	REF.	N	DIM "G"	"F"
36			6	8.15	B17
64			20	22.15	B31
98			37	39.15	B48
164			70	72.15	B81

PIN "F" SEE TABLE 2.40±0.10

NOTE 4

PIN "F" SEE TABLE	2.40±0.10
	NOTE 4

ADD-IN CARD EDGE-FINGER DIMENSIONS

mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY		 www.pciconnect.com	
ltr	ecn no	dr	date	.X ±		projection		title	
A				.XX ±		 		PCI EXPRESS CARD EDGE ASS'Y	
				.XXX ±					
				0° ±2°					
				dr JASON HSU		 MM		product family EDGE CARD	
				engr JASON HSU		 scale		size dwg no	
				chr GARY HSIEH		2:1		10108777	
				qppd JOSEPH HSIA				code	
sheet	revision								sheet
index	sheet								5 of



NOTES:

1.MATERIAL:

HOUSING: HIGH TEMPERATURE NYLON, GLASS FILLED UL94V-0 RATED.
TERMINAL: COPPER ALLOY.

PLATING: 50u" NICKEL UNDERPLATE ALL OVER

CONTACT AREA PLATING - SEE PRODUCT NUMBER CODE
SOLDER TAIL: TIN OR TIN /LEAD(90/10) - SEE PRODUCT

METAL BOARD LOCKS: COPPER ALLOY,
NUMBER CODE 100u" MIN. OVER 50u" NICKEL.

FINISH: 100u" TIN OR TIN/LEAD(90/10) OVER 50u" NICKEL

UNDERPLATE SEE PRODUCT NUMBER CODE.

2.PRODUCT SPECIFICATION: GS-12-233.

③THE HORIZONTAL AXIS FOR THE HOLE PATTERN IS ESTABLISHED BY
A LINE THROUGH THE CENTER OF THE TWO Ø2.35 HOLES.

THE VERTICAL AXIS IS 90° TO THE HORIZONTAL AXIS, THROUGH THE CENTER

OF DATUM Z.

④NO TIE BAR PERMITTED FROM CARD EDGE TO LEADING EDGE OF PAD FOR PINS A1 AND PIN NUMBERS "T".

⑤CHAMFER EDGES MUST BE FREE OF CUTTING BURRS.

⑥FREQUENCY & LOCATION AT SUPPLIER DISCRETION. RIDGE MAY BE CONTINUOUS WITH NO BREAKS.

7.RoHS COMPATIBLE PRODUCT SPECIFICATIONS:

a - PLATING:

- "L" MEANS THE PRODUCT IS LEAD-FREE, 2um MINIMUM MATTE TIN OVER 1.27um

MINIMUM NICKEL UNDERPLATE.

b - MANUFACTURING PROCESS COMPATIBILITY:

- THE HOUSING WILL WITHSTAND EXPOSURE TO 260C±5°C TEMPERATURE FOR

10 SECONDS IN RE-FLOW APPLICATION, SEE NOTE 8 FOR APPLICATION.

PRODUCT NUMBER CODE

10108777 -X X X X X X X X

HOUSING COLOR OPTIONS

0-NATURAL

1-BLACK

PEGS OPTIONS

0-PLASTIC PEGS

1-METAL BOARD LOCKS

2-SINGLE PLASTIC PEGS

TERMINAL PLATING OPTIONS

0-50u" Ni UNDERPLATE

30u" Au CONTACT AREA

100u" TIN TAIL AREA ----- COMPATIBLE RoHS

1-50u" Ni UNDERPLATE

15u" Au CONTACT AREA

100u" TIN TAIL AREA ----- COMPATIBLE RoHS

2-50u" Ni UNDERPLATE

GOLD FLASH CONTACT AREA

100u" TIN TAIL AREA ----- COMPATIBLE RoHS

3-50u" Ni UNDERPLATE

30u" Au CONTACT AREA

100u" TIN/LEAD TAIL AREA ----- INCOMPLIANT RoHS

4-50u" Ni UNDERPLATE

15u" Au CONTACT AREA

100u" TIN/LEAD TAIL AREA ----- INCOMPLIANT RoHS

5-50u" Ni UNDERPLATE

GOLD FLASH CONTACT AREA

100u" TIN/LEAD TAIL AREA ----- INCOMPLIANT RoHS

LEAD FREE OPTION

LEAVE BLANK FOR TERMINAL PLATING 3,4,5

"L" FOR TERMINAL PLATING OPTION 0,1,2

POS OPTIONS

0-36

1-64

2-98

3-164

PACKAGING OPTIONS

T-SOFT TRAY PACKAGING

M-SOFT TRAY PACKAGING & MYLAR TAPE. SEE FIGURE 1

Y-HARD TRAY PACKAGING (FOR 164P ONLY)

Z-HARD TRAY PACKAGING WITH MYLAR (FOR 164P ONLY)

R-TAPE & REEL PACKAGING WITHOUT MYLAR (FOR 98P ONLY)

W-TAPE & REEL PACKAGING WITHOUT MYLAR

(FOR 98P ONLY), WITH 18.0MM CAVITY WIDTH

TAIL LENGTH OPTIONS

	DIM "D"	PCB THICKNESS	BOARD LOCKS DIM "G"
0	2.30 ±0.13	1.56±0.10	1.70±0.15
1	3.10 ±0.23	2.36±0.10	2.50±0.15
2	2.54±0.25	1.56±0.10	1.70±0.15
3	1.90±0.25	1.56±0.10	1.70±0.15
4	3.10 ±0.23	2.0±0.10	1.70±0.15

8.AN ADEQUATE PROCEDURE SET FORTH IN THE FOLLOWING ENSURE THAT THE PRODUCT RELIABILITY CAN BE ACHIEVED DURING AND AFTER PCB ASSEMBLY OPERATION.
8.1IF PARTIAL LOT ARE USED, THE REMAINING CONNECTORS MUST BE RESEALED AND PLACED IN SAFE STORAGE WITHIN SIX HOURS OF BAG OPENING.
8.2THE SAFE STORAGE CONDITION IS RECOMMENDED AT 25°C, 40%RH.
8.3NOT FOLLOWING THE PROCEDURE MAY CAUSE BLISTER DAMAGE DURING RE-FLOW PROCESS.
8.4IF BAKING IS REQUIRED, CONNECTOR SHALL BE BAKED FOR 4 HOURS AT 125°C.
8.5REFERENCE SPECIFICATION: IPC/JEDEC J-STD-033A.

CONNECTOR	#	POS REF.	N	DIM "A"	DIM "B"	DIM "C"	"E"
1 PORT	36	6	7.65	25.00	9.15	6.00	
4 PORT	64	20	21.65	39.00	23.15	20.00	
8 PORT	98	37	38.65	56.00	40.15	37.00	
16 PORT	164	70	71.65	89.00	73.15	70.00	

mat'l. code	ecn no	dr	date	linear	tolerances unless otherwise specified	projection	title	product family	EDGE CARD	code
D				.XX ±	.X ±	MM	PCI EXPRESS	size	dwg no	TWIN
				.XXX ±	0° ±2°		engr JASON HSU	10/29/09		
							chr GARY HSIEH	10/29/09		
							appr JOSEPH HSIA	10/29/09		
sheet	revision							scale		sheet
index	sheet							1:1	A4	6 of 7

1 | 2

3

4

ACAD

cage code

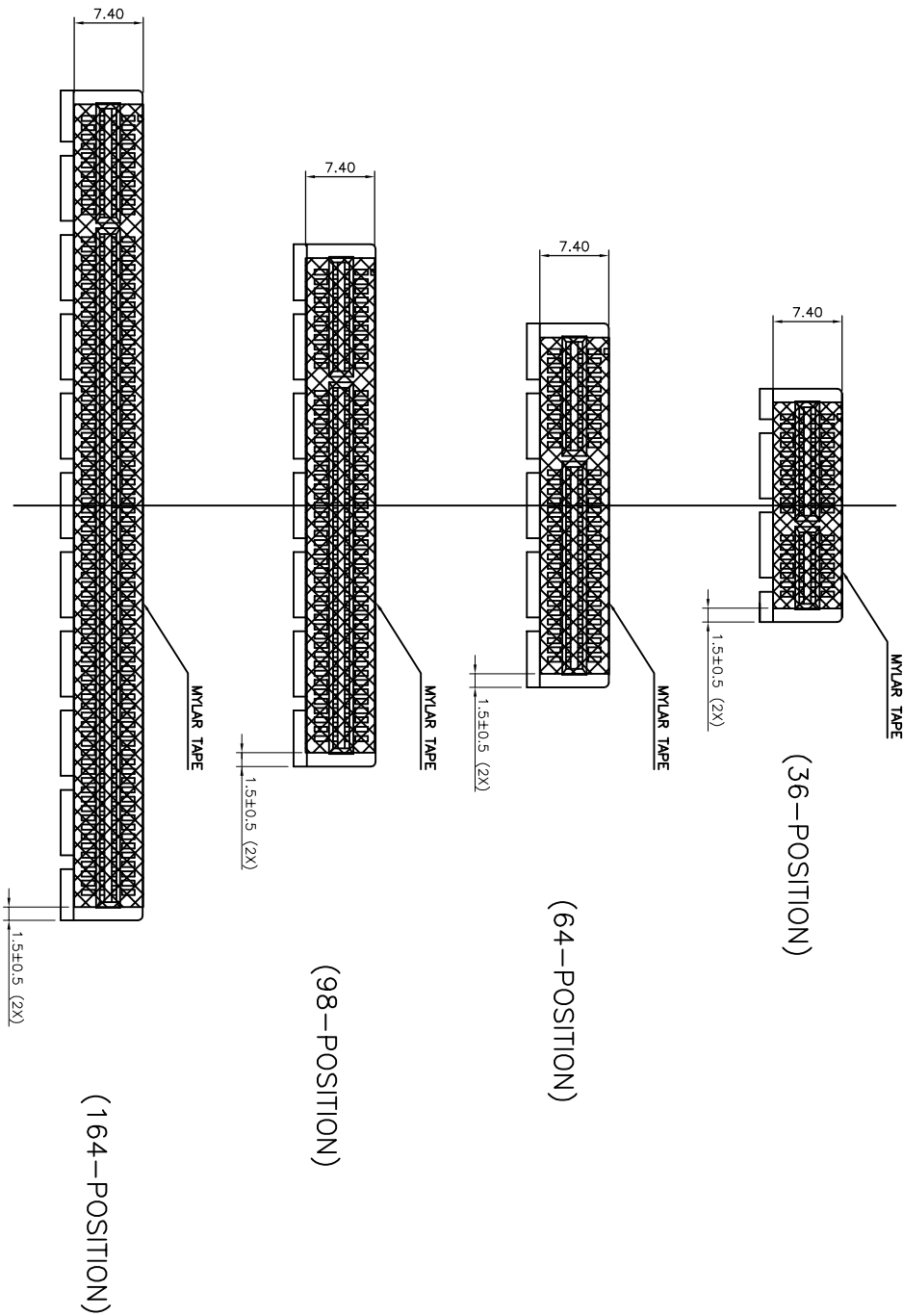


FIGURE 1

mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY		FCI	
itr	ecn no	dr	date	lineor	.XX ± 0.25	projection	title	product family	code
A				angles	.XXX ± 0.05	MM	PCI EXPRESS CARD EDGE ASS'Y	EDGE CARD	TWN
				dr	0° ± 2°			size dwg no	
				engr	JASON HSU 10/29/09				
				chr	GARY HSIEH 10/29/09				
				qppd	JOSEPH HSIA 10/29/09				
sheet	revision			scale	1.5:1				sheet
Index	sheet								7 of