

Maxtane™ CX955xx

ADSL/ADSL2/ADSL2+ CO

Target Markets

- Central office equipment

Applications

- DSLAMs
- Remote access servers: DLCs/ONUs
- MxU data services
- Ethernet over first mile (EFM) applications

Features

- High density ADSL2+ solution
- G.INP support for industry leading impulse noise protection
- On-chip bonding for both packet and cell-based applications
- Extended interleaving memory for maximum INP to support the new amendments to ADSL2+ standard
- Dual interleaving for triple play features
- Enhanced dynamic rate repartitioning (DRR)
- Mask on Demand (MoD) support for superior rate/reach performance
- Dynamic spectrum management (DSM) support for better cross-talk performance
- IP DSLAM ready, with built-in AAL5 bridging support and packet interfaces
- On-chip segmentation and reassembly for ATM cell and IP packet convergence
- Packet over DSL (PTM) mode support



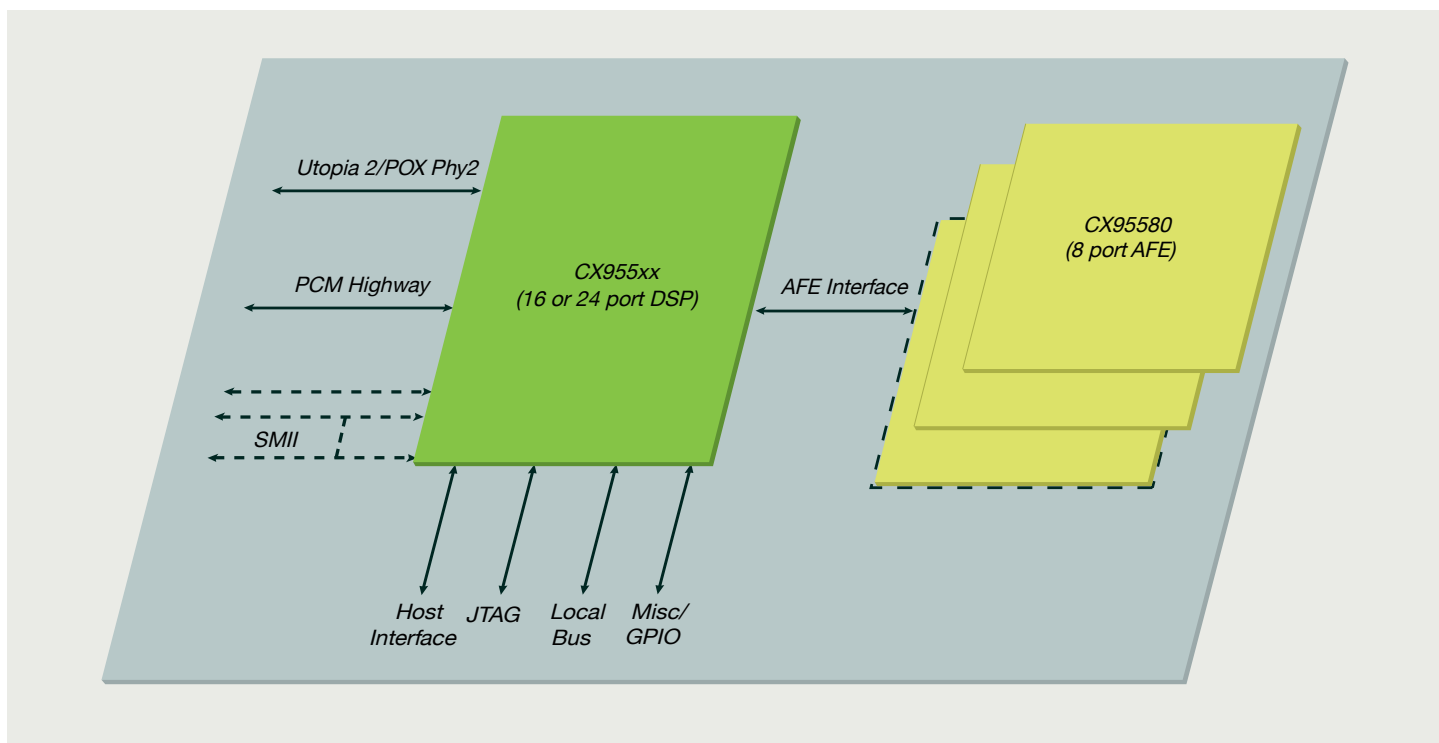
Ikanos' Maxtane™ CX95516/CX95524 chipsets are advanced ADSL/ADSL2/ADSL2+ central office (CO) transceiver devices that meet the new operator requirements for better triple play services, while lowering overall system cost for network equipment manufacturers.

The Maxtane™ CX95516/CX95524 chipsets offer next generation triple play features, such as enhanced impulse noise protection (INP) and dual interleaving for simultaneous video, voice, and data services with appropriate protection for each service. In addition to these advanced features, the CX95516/CX95524 chipsets enable lower system cost with the highest level of system integration and superior performance, making it industry's most competitive solution. The design capitalizes on Ikanos' many years of successful real world DSL field experience, comprehensive understanding of standards-based xDSL technology, and network equipment manufacturer/Operator feedback.

Advanced features of CX95516/CX95524 enable better service levels for triple play services, including Internet protocol-based television (IPTV), high-speed Internet access,

video on demand (VOD), high definition television (HDTV), voice over IP (VoIP), video conferencing, remote office connectivity, and telecommuting. CX95516/CX95524 devices enable feature rich IPTV line card solutions for next-generation exchange (CO), remote unit (DLC/ONU), or multiple dwelling/tenant unit (MxU) applications.

The CX95516 and CX95524 chipsets support 16 and 24 channels of ADSL2/ADSL2+/ADSL2+, respectively. The CX95516 chipsets includes a 16-port DSP and two analog front-end (AFE) chips. The DSP is packaged in a 472-pin plastic ball grid array (PBGA), and the AFE is packaged in a 144-pin low-profile, fine pitch ball grid array (LFBGA). The CX95524 chipset includes a DSP in a 24-port configuration and three AFEs. The 24-port digital signal processor (DSP) is packaged in a 668-pin PBGA.



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