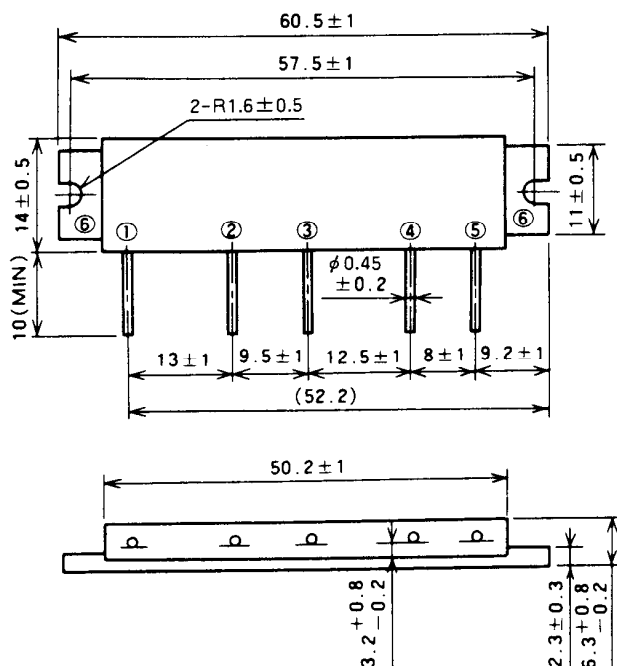
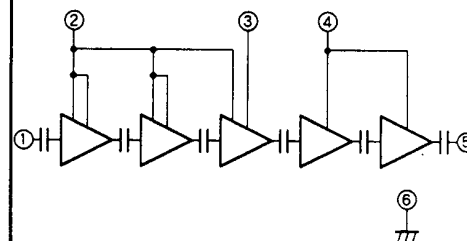


OUTLINE DRAWING

Dimensions in mm

**BLOCK DIAGRAM**

PIN :

- ① Pin : RF INPUT
- ② Vcc1 : 1st. DC SUPPLY
- ③ Vcc2 : 2nd. DC SUPPLY
- ④ Vcc3 : 3rd. DC SUPPLY
- ⑤ Po : RF OUTPUT
- ⑥ GND : FIN

ABSOLUTE MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Ratings	Unit
$V_{cc1,2,3}$	Supply voltage		9.2	V
I_{cc}	Total current	$Z_G = Z_L = 50 \Omega$	4	A
$P_{in(max)}$	Input power	$Z_G = Z_L = 50 \Omega$, $V_{cc1} \leq 7.2\text{V}$	4	mW
$P_{o(max)}$	Output power	$Z_G = Z_L = 50 \Omega$, $V_{cc1} \leq 7.2\text{V}$	8	W
$T_{c(OP)}$	Operation case temperature	$Z_G = Z_L = 50 \Omega$, $V_{cc1} \leq 7.2\text{V}$	-30 to 100	$^\circ\text{C}$
T_{stg}	Storage temperature		-40 to 110	$^\circ\text{C}$

Note. Above parameters are guaranteed independently.

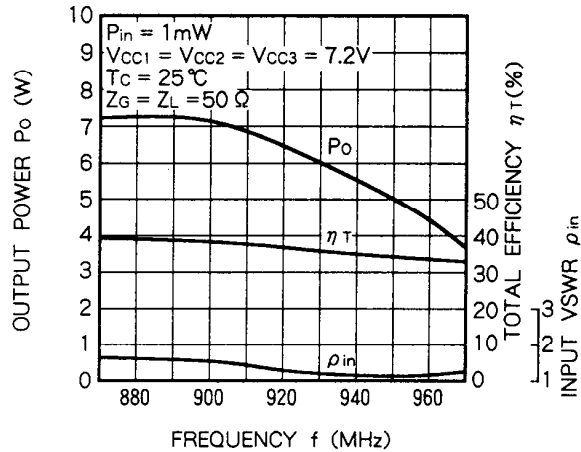
ELECTRICAL CHARACTERISTICS ($T_c = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test conditions	Limits		Unit
			Min	Max	
f	Frequency range	$P_{in} = 1\text{mW}$ $V_{cc1} = V_{cc2} = V_{cc3} = 7.2\text{V}$ $Z_G = Z_L = 50 \Omega$	896	941	MHz
P_o	Output power		5		W
η_T	Total efficiency		30		%
$2f_o$	2nd. harmonic			-30	dBc
$3f_o$	3rd. harmonic			-30	dBc
ρ_{in}	Input VSWR			3.0	-
-	Load VSWR tolerance	$V_{cc2}=V_{cc3}=9.2\text{V}$, $P_o=5.0\text{W}$ (V_{cc1} controlled), Load VSWR=20:1 (All phase), 2sec. $Z_G = 50 \Omega$	No degradation or destroy		-

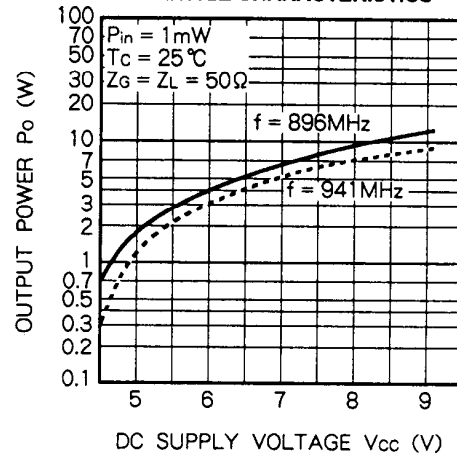
Note. Above parameters, ratings, limits and conditions are subject to change.

TYPICAL PERFORMANCE DATA

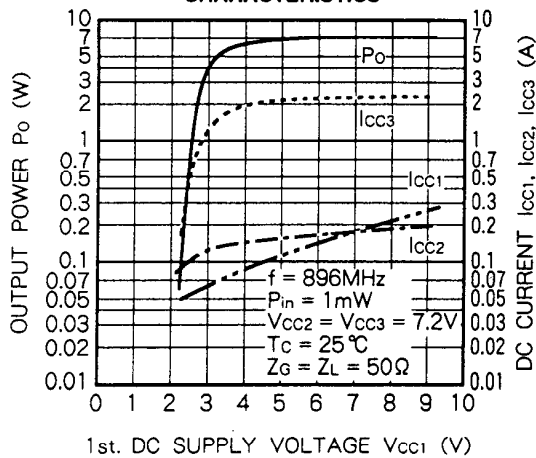
OUTPUT POWER, TOTAL EFFICIENCY, INPUT VSWR VS. FREQUENCY CHARACTERISTICS



OUTPUT POWER VS. DC SUPPLY VOLTAGE CHARACTERISTICS



OUTPUT POWER, DC CURRENT VS. 1st. DC SUPPLY VOLTAGE CHARACTERISTICS



OUTPUT POWER, DC CURRENT VS. 1st. DC SUPPLY VOLTAGE CHARACTERISTICS

