
SPB-3760WG / SPB-3760BWG / SPB-3760LWG / SPB-3760BLWG (RoHS Compliant)

1550 nm TX / 1310 nm RX , 3.3V / 155 Mbps Digital Diagnostic Single-Fiber SFP Transceiver

FEATURES

- | 1-Fiber Bi-Directional SFP Optical Transceiver
- | Complaint with IEEE 802.3ah 100BASE-BX
- | Simplex SC Connector: SPB-3760WG
- | Simplex LC Connector: SPB-3760LWG
- | **SFF-8472 Digital Diagnostic Function**
- | 1550 nm DFB LD Transmitter
- | 1310 nm Receiver
- | Distance Up to 60 km
- | AC/AC Coupling according to MSA
- | Single +3.3 V Power Supply
- | RoHS Compliant
- | 0 to 70°C Operating : SPB-3760WG
- | -10 to 85°C Operating : SPB-3760BWG
- | Class 1 Laser International Safety Standard IEC 60825 Compliant

APPLICATIONS

- | WDM Fast Ethernet Links
- | SONET/SDH Equipment Interconnect
- | Fiber Channel Links

DESCRIPTION

The SPB-3760WG series is small form factor pluggable module for IEEE 802.3ah 100BASE-BX and OC-3/STM-1 SONET/SDH single fiber communications by using 1550 nm transmitter and 1310 nm receiver. It is with the SFP 20-pin connector to allow hot plug capability. Digital diagnostic functions are available via an I²C series bus specified in the SFP MSA SFF-8472. The transmitter section uses a multiple quantum well 1550 nm laser and is a class 1 laser compliant according to International Safety Standard IEC 60825. The receiver section uses an integrated 1310 nm detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC.

LASER SAFETY

This single mode transceiver is a Class 1 laser product. It complies with IEC 60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

ORDER INFORMATION

P/No.	Bit Rate (Mb/s)	Distance (km)	TX (nm)	RX (nm)	Package	Temp (°C)	TX Power (dBm)	RX Sens. (dBm)	RoHS Compliant
SPB-3760WG	125/155	60	1550 DFB	1310	SC SFP with DMI	0 to 70	0 to -5	-34	Yes
SPB-3760BWG	125/155	60	1550 DFB	1310	SC SFP with DMI	-10 to 85	0 to -5	-34	Yes
SPB-3760LWG	125/155	60	1550 DFB	1310	LC SFP with DMI	0 to 70	0 to -5	-34	Yes
SPB-3760BLWG	125/155	60	1550 DFB	1310	LC SFP with DMI	-10 to 85	0 to -5	-34	Yes

Absolute Maximum Ratings					
Parameter	Symbol	Min	Max	Units	Notes
Storage Temperature	Tstg	-40	85	°C	
Operating Case Temperature	Topr	0 -10	70 85	°C	SPB-3760WG SPB-3760BWG
Power Supply Voltage	Vcc	0	3.6	V	
Input Voltage	---	GND	Vcc	V	
Output Current	Iout	0	30	mA	

Recommended Operating Conditions					
Parameter	Symbol	Min	Typ	Max	Units / Notes
Power Supply Voltage	Vcc	3.1	3.3	3.5	V
Power Supply Current	I _{CC(TX+RX)}		200	300	mA
Operating Case Temperature	Topr	0 -10		70 85	°C / SPB-3760WG °C / SPB-3760BWG
Data Rate			125/155	200	Mb/s

Transmitter Specifications (0°C < Topr < 70°C, 3.1V < Vcc < 3.5V)						
Parameter	Symbol	Min	Typ	Max	Units	Notes
Optical						
Optical Transmit Power	Po	-5	---	0	dBm	1
Output Center Wavelength	λ	1480		1580	nm	
Output Spectrum Width	Δλ	---	---	1	nm	-20 dB width
Extinction Ratio	E _R	10	---	---	dB	
Output Eye	Compliant with Bellcore GR-253-CORE and ITU recommendation G.957					
Optical Rise Time	t _r			2	ns	10% to 90% Values
Optical Fall Time	t _f			2	ns	10% to 90% Values
Relative Intensity Noise	RIN			-116	dB/Hz	
Total Jitter	TJ			1.0	ns	2
Electrical						
Data Input Current – Low	I _{IL}	-350			μA	
Data Input Current – High	I _{IH}			350	μA	
Differential Input Voltage	V _{IH} - V _{IL}	0.5		2.4	V	Peak-to-Peak
TX Disable Input Voltage – Low	T _{DIS, L}	0		0.5	V	3
TX Disable Input Voltage – High	T _{DIS, H}	2.0		Vcc	V	3
TX Disable Assert Time	T _{ASSERT}			10	μs	
TX Disable Deassert Time	T _{DEASSERT}			1	ms	
TX Fault Output Voltage -- Low	T _{FaultL}	0		0.5	V	4
TX Fault Output Voltage -- High	T _{FaultH}	2.0		Vcc+0.3	V	4

Notes: 1. Output power is power coupled into a 9/125 μm single mode fiber.

2. Measured with a 2²³-1 PRBS.

3. There is an internal 4.7K to 10K ohm pull-up resistor to VccTX.

4. Open collector compatible, 4.7K to 10K ohm pull-up to Vcc (Host Supply Voltage).

Optical

Electrical

7. Open collector compatible, 4.7K to 10K ohm pull-up to V_{CC} (Host Supply Voltage).

The diagram illustrates the internal circuitry of an SFP module and its connections to an external system. The module contains an LD Driver, a POST Amp, an EEPROM, and a SerDes IC.

Pin Connections:

- Pin 1:** VeeT (1,17,20) - Ground connection.
- Pin 2:** Tx_DISABLE - Connected to Tx_FAULT.
- Pin 3:** Tx_FAULT - Connected to Tx_DISABLE.
- Pin 4:** MOD_DEF(2) - Connected to SDA.
- Pin 5:** MOD_DEF(1) - Connected to SCL.
- Pin 6:** MOD_DEF(0) - Connected to VeeR.
- Pin 7:** LOS - Connected to LOSS OF SIGNAL.
- Pin 8:** 4.7K to 10KΩ - Pull-up resistor to VccT.
- Pin 9:** VccR (9,10,11,14) - Ground connection.
- Pin 10:** VccR - Ground connection.
- Pin 11:** VccR - Ground connection.
- Pin 12:** RD - Connected to RD+.
- Pin 13:** RD+ - Connected to RD-.
- Pin 14:** VccR - Ground connection.
- Pin 15:** VccR - Ground connection.
- Pin 16:** VccT - Connected to Vcc 3.3V.
- Pin 17:** VccT - Connected to Vcc 3.3V.
- Pin 18:** TD+ - Connected to TD-.
- Pin 19:** TD- - Connected to TD+.
- Pin 20:** VccT - Connected to Vcc 3.3V.

Internal Components:

- LD Driver:** Laser driver circuit with a 100Ω resistor and a 4.7K to 10KΩ pull-up resistor.
- POST Amp:** Post-amplifier circuit with a 100Ω resistor and a 4.7K to 10KΩ pull-up resistor.
- EEPROM:** EEPROM memory with MOD_DEF(0), MOD_DEF(1), and MOD_DEF(2) pins.
- SerDes IC:** SerDes IC with TD+, TD-, RD+, and RD- pins. It includes a 100Ω resistor and a 4.7K to 10KΩ pull-up resistor.

External Connections:

- Tx_DISABLE:** Connected to Tx_FAULT.
- LOSS OF SIGNAL:** Connected to LOS.
- SDA:** Connected to MOD_DEF(2).
- SCL:** Connected to MOD_DEF(1).
- Vcc 3.3V:** Connected to VccT and VccR.

CONNECTION DIAGRAM



PIN	Signal Name	Description	PIN	Signal Name	Description
1	TX GND	Transmitter Ground	11	RX GND	Receiver Ground
2	TX Fault	Transmitter Fault Indication	12	RX DATA OUT-	Inverse Receiver Data Out
3	TX Disable	Transmitter Disable (Module disables on high or open)	13	RX DATA OUT+	Receiver Data Out
4	MOD-DFE2	Modulation Definition 2 – Two wires serial ID Interface	14	RX GND	Receiver Ground
5	MOD-DEF1	Modulation Definition 1 – Two wires serial ID Interface	15	Vcc RX	Receiver Power – 3.3V±5%
6	MOD-DEF0	Modulation Definition 0 – Ground in Module	16	Vcc TX	Transmitter Power – 3.3V±5%
7	N/C	Not Connected	17	TX GND	Transmitter Ground
8	LOS	Loss of Signal	18	TX DATA IN+	Transmitter Data In
9	RX GND	Receiver Ground	19	TX DATA IN-	Inverse Transmitter Data In
10	RX GND	Receiver Ground	20	TX GND	Transmitter Ground

Module Definition

Module Definition	MOD-DEF2 PIN 4	MOD-DEF1 PIN 5	MOD-DEF0 PIN 6	Interpretation by Host
4	SDA	SCL	LV-TTL Low	Serial module definition protocol

Module Definition 4 specifies a serial definition protocol. For this definition, upon power up, MOD-DEF(1:2) appear as no connector (NC) and MOD-DEF(0) is TTL LOW. When the host system detects this condition, it activates the serial protocol. The protocol uses the 2-wire serial CMOS E²PROM protocol of the ATMEL AT24C01A/02/04 family of components.

PACKAGE DIAGRAM

Units in mm

A) SC SFP

Top View

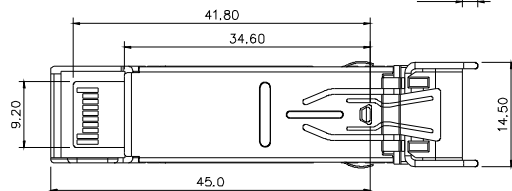


Front View



Side View

Bottom View

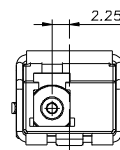


A) LC SFP

Top View



Front View



Side View

Bottom View



Note: Specifications subject to change without notice.