

NC7SZ05

TinyLogic™ UHS Inverter (Open Drain Output)

General Description

The NC7SZ05 is a single Inverter with open drain output stage from Fairchild's Ultra High Speed Series of TinyLogic™ in the space saving SOT23 package. The device is fabricated with advanced CMOS technology to achieve ultra high speed with high output drive while maintaining low static power dissipation over a very broad V_{CC} operating range. The device is specified to operate over the 1.8V to 5.5V V_{CC} range. The input and output are high impedance when V_{CC} is 0V. Inputs tolerate voltages up to 6V independent of V_{CC} operating voltage. The open drain output stage tolerates voltages up to 6V independent of V_{CC} when in the high impedance state.

Features

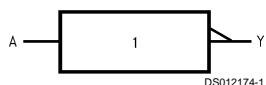
- Space saving 5-lead surface mount SOT23 package
- Open drain output for OR tied applications
- Ultra High Speed; T_{PD} 1.9 ns Typ into 50 pF at 5V V_{CC}
- High Output I_{OL} Drive; +24 mA at 3V V_{CC}
- Broad V_{CC} Operating Range; 1.8V to 5.5V
- Matches the performance of LCX when operated at 3.3V V_{CC}
- Power down high impedance inputs/output
- Overvoltage Tolerant inputs facilitate 5V to 3V translation
- Patented noise/EMI reduction circuitry implemented

Ordering Code:

Product Code	Package	Package Drawing	Package Top Mark	Supplied As
NC7SZ05M5	5-Pin SOT23-5	MA05B	7Z05	250 Units on Tape and Reel
NC7SZ05M5X	5-Pin SOT23-5	MA05B	7Z05	3k Units on Tape and Reel

Logic Symbol

IEEE/IEC

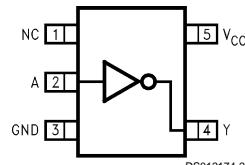


Pin Descriptions

Pin Names	Description
A	Input
Y	Output
NC	No Connect

Connection Diagram

Pin Assignment for SOT23-5 Package



Top View

Function Table

$$Y = \bar{A}$$

Input	Output
A	Y
L	*H
H	L

H = HIGH Logic Level

L = LOW Logic Level

*H = HIGH Impedance output state (Open Drain)

Absolute Maximum Ratings (Note 1)			Negative Source Current (NIT)	-500 mA	
			Positive Source Voltage (PVT)	+8V	
Supply Voltage (V_{CC})		-0.5V to +6V			
DC Input Voltage (V_{IN})		-0.5V to +6V			
DC Output Voltage (V_{OUT})		-0.5V to +6V			
DC Input Diode Current (I_{IK})					
@ $V_{IN} < -0.5V$		-50 mA	Supply Voltage Operating (V_{CC})	1.8V to 5.5V	
@ $V_{IN} > 6V$		+20 mA	Supply Voltage Data Retention (V_{CC})	1.5V to 5.5V	
DC Output Diode Current (I_{OK})			Input Voltage (V_{IN})	0V to 5.5V	
@ $V_{OUT} < -0.5V$		-50 mA	Output Voltage (V_{OUT})	0V to V_{CC}	
@ $V_{OUT} > 6V, V_{CC} = GND$		+20 mA	Operating Temperature (T_{OPR})	-40°C to +85°C	
DC Output Current (I_{OUT})		+50 mA	Input Rise and Fall Time (t_r, t_f)		
DC V_{CC}/GND Current (I_{CC}/I_{GND})		±50 mA	$V_{CC} = 1.8V, 2.5V \pm 0.2V$	0 ns/V to 20 ns/V	
Storage Temperature (T_{STG})		-65°C to +150°C	$V_{CC} = 3.3V \pm 0.3V$	0 ns/V to 10 ns/V	
Junction Temperature under Bias (T_J)		150°C	$V_{CC} = 5.0V \pm 0.5V$	0 ns/V to 5 ns/V	
Junction Lead Temperature (T_1); (Soldering, 10 sec)		260°C	Thermal Resistance (θ_{JA} in Free Air)	300°C/W	
Package Power Dissipation @ +70°C		200 mW	Note 1: Absolute maximum ratings are DC values beyond which the device may be damaged or have its useful life impaired. The datasheet specification should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. Fairchild does not recommend operation outside datasheet specifications.		
ESD Tolerance (Human Body Model)					
MIL-STD-883D Method 3015.7		1000V			
DC Latchup Tolerance (JEDEC Method 17)					

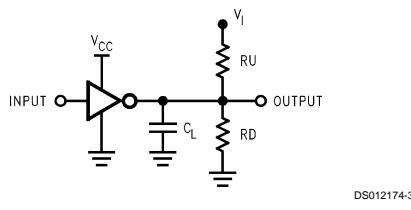
DC Electrical Characteristics

Symbol	Parameter	V_{CC} (V)	NC7SZ05			Unit	Conditions		
			$T_A = +25^\circ C$						
			Min	Typ	Max				
V_{IH}	High Level Input Voltage	1.8 2.3-5.5	0.75 V_{CC} 0.7 V_{CC}		0.75 V_{CC} 0.7 V_{CC}	V			
V_{IL}	Low Level Input Voltage	1.8 2.3-5.5		0.25 V_{CC} 0.3 V_{CC}		V			
I_{LKG}	High Level Output Leakage	1.8-5.5		±5		µA	$V_{IN} = V_{IL}$ $V_{OUT} = V_{CC}$ or GND		
V_{OL}	Low Level Output Voltage	1.8	0.0	0.1	0.1	V	$V_{IN} = V_{IH}$		
		2.3	0.0	0.1	0.1				
		3.0	0.0	0.1	0.1				
		4.5	0.0	0.1	0.1				
		2.3	0.10	0.3	0.3	V	$I_{OL} = 100 \mu A$		
		3.0	0.15	0.4	0.4				
		3.0	0.22	0.55	0.55				
		4.5	0.22	0.55	0.55				
I_{IN}	Input Leakage Current	0-5.5		±1	±10	µA	$0 \leq V_{IN} \leq 5.5V$		
I_{OFF}	Power Off Leakage Current	0.0		1	10	µA	V_{IN} or $V_{OUT} = 5.5V$		
I_{CC}	Quiescent Supply Current	1.8-5.5		2.0	20	µA	$V_{IN} = 5.5V, GND$		

AC Electrical Characteristics

Symbol	Parameter	V _{CC} (V)	NC7SZ05			Units	Conditions	Fig. No.			
			T _A = +25°C								
			Min	Typ	Max						
t _{PZL}	Propagation Delay	1.8	1.5	4.6	10.5	1.5	11.0	ns C _L = 50 pF RU = 500Ω RD = 500Ω VI = 2 x V _{CC}			
		2.5 ±0.2	0.8	3.0	7.0	0.8	7.5				
		3.3 ±0.3	0.8	2.4	5.0	0.8	5.2				
		5.0 ±0.5	0.5	1.9	4.3	0.5	4.5				
t _{PLZ}	Propagation Delay	1.8	1.5	4.1	10.5	1.5	11.0	ns C _L = 50 pF RU = 500Ω RD = 500Ω VI = 2 x V _{CC}			
		2.5 ±0.2	0.8	2.5	7.0	0.8	7.5				
		3.3 ±0.3	0.8	2.1	5.0	0.8	5.2				
		5.0 ±0.5	0.5	1.2	4.3	0.5	4.5				
C _{IN}	Input Capacitance	0	4			pF					
C _{OUT}	Output Capacitance	0	6			pF					
C _{PD}	Power Dissipation	3.3	3.6			pF	(Note 2)	Figure 2			
	Capacitance	5.0	6.5								

Note 2: C_{PD} is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (I_{CCD}) at no output loading and operating at 50% duty cycle. (See Figure 2) C_{PD} is related to I_{CCD} dynamic operating current by the expression: I_{CCD} = (C_{PD}) (V_{CC}) (f_{IN}) + (I_{CC} static)



C_L includes load and stray capacitance
Input PRR = 1.0 MHz; TW = 500 ns

FIGURE 1. AC Test Circuit

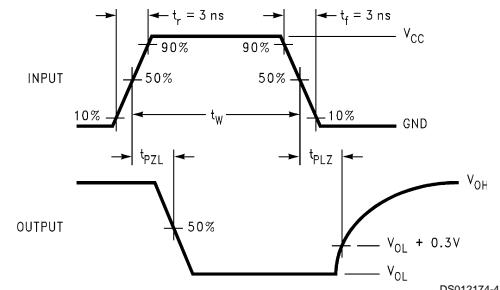
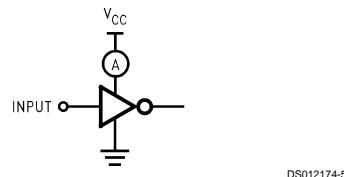


FIGURE 3. AC Waveforms

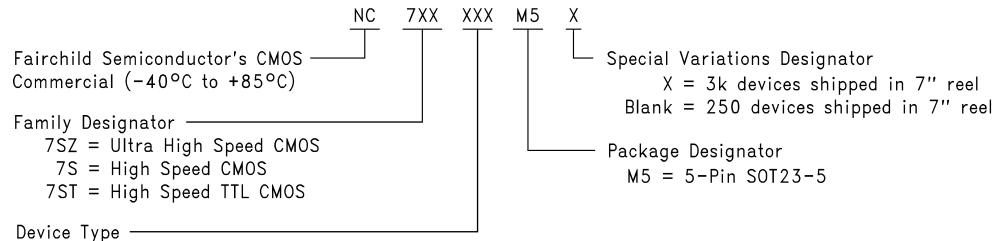


Input = AC Waveform; t_r = t_f = 1.8 ns
PRR = 10 MHz; Duty Cycle = 50%

FIGURE 2. AC Test Circuit

Ordering Information

The device number is used to form part of a simplified purchasing code where the package type and temperature range are defined as follows:



DS012174-1

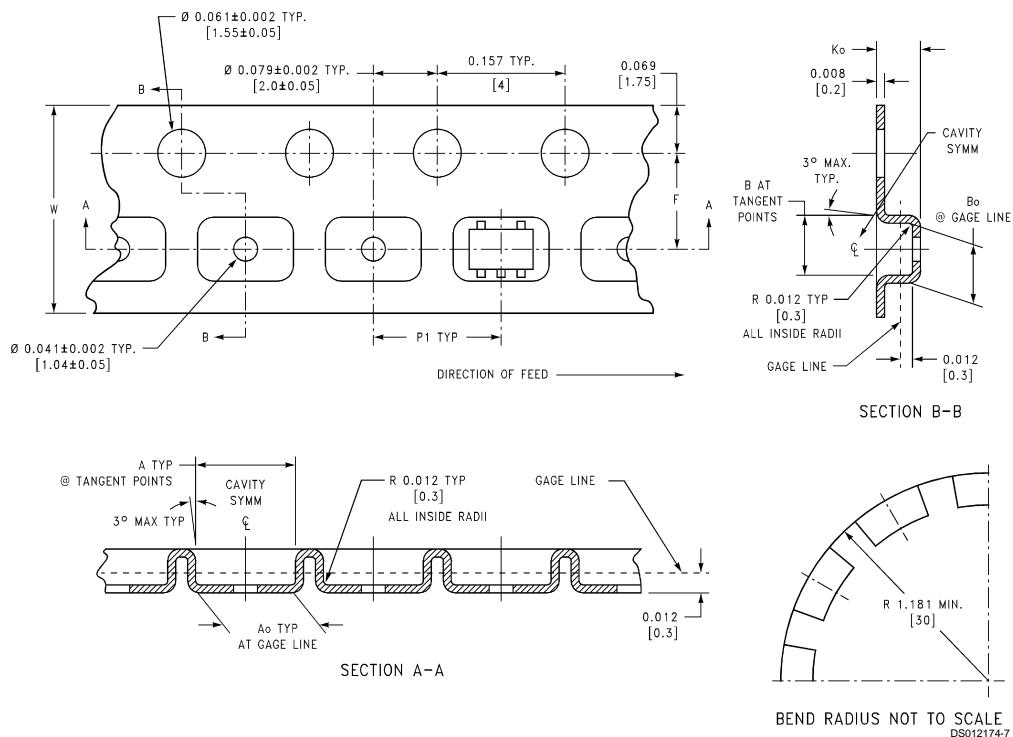
SOT23-5 Tape and Reel Specification

Tape Format

Tape Section	# Cavities	Cavity Status	Cover Tape Status
Leader (Start End)	0 (min)	Empty	Unsealed
	75 (min)	Empty	Sealed
Carrier	3000	Filled	Sealed
	250	Filled	Sealed
Trailer (Hub End)	125 (min)	Empty	Sealed
	0 (min)	Empty	Unsealed

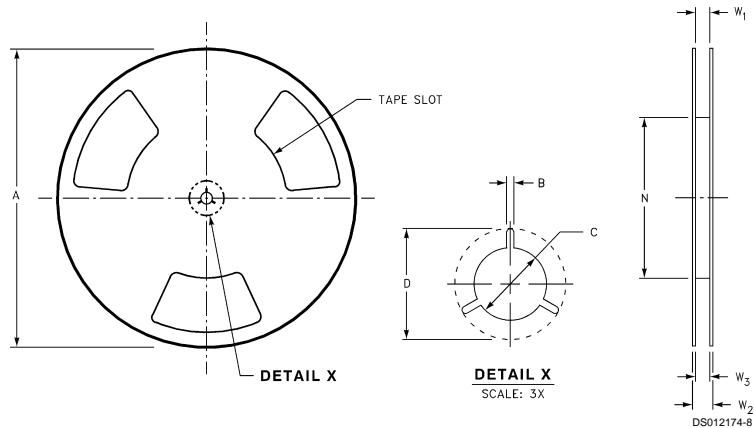
Tape Dimensions

inches (millimeters)

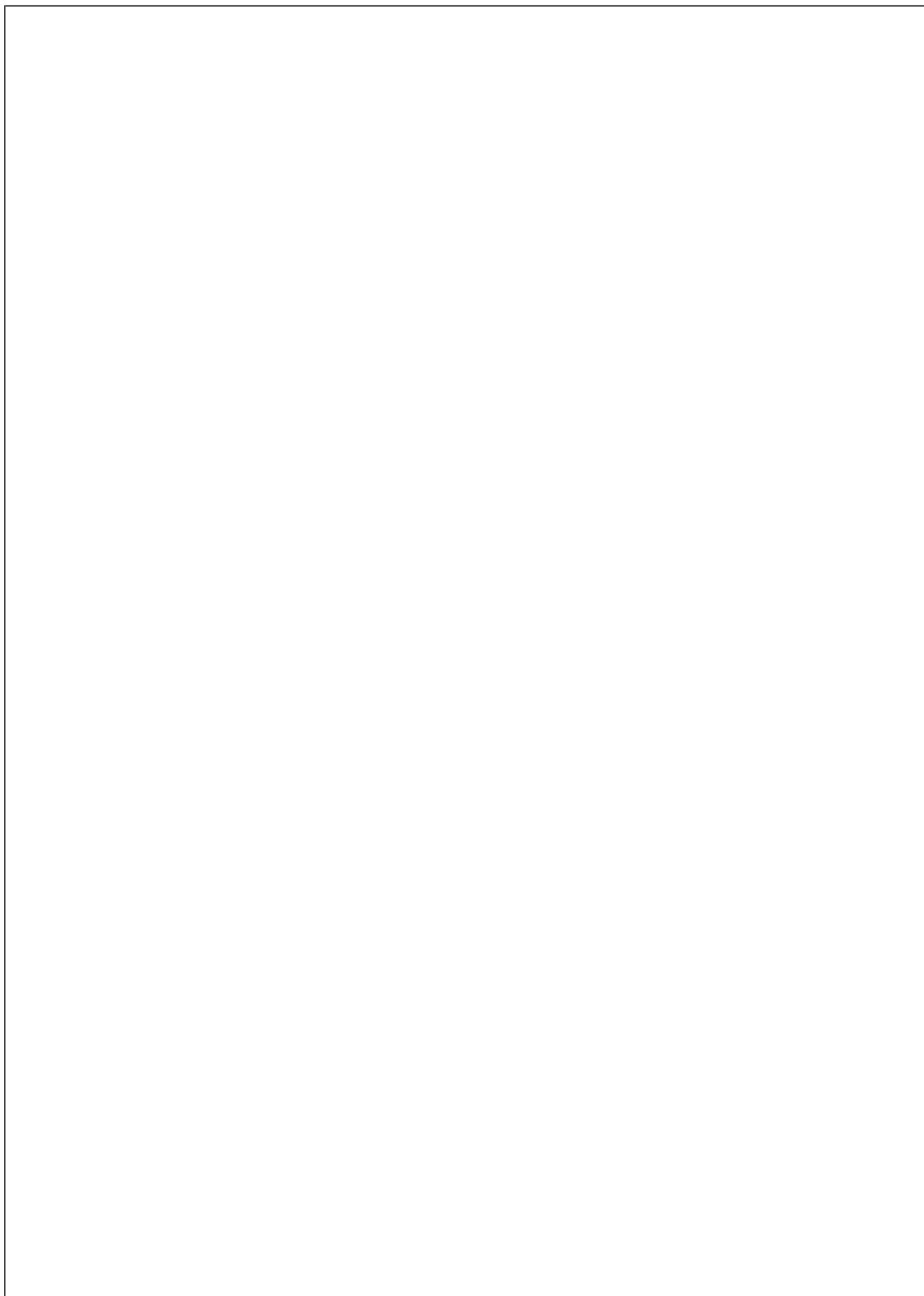


8mm	0.130 (3.3)	0.124 (3.15)	0.130 (3.3)	0.126 (3.2)	0.138 ± 0.002 (3.5 ± 0.05)	0.055 ± 0.004 (1.4 ± 0.11)	0.157 (4)	0.315 ± 0.012 (8 ± 0.3)
Tape Size	DIM A	DIM Ao	DIM B	DIM Bo	DIM F	DIM Ko	DIM Pi	DIM W

Reel Dimensions

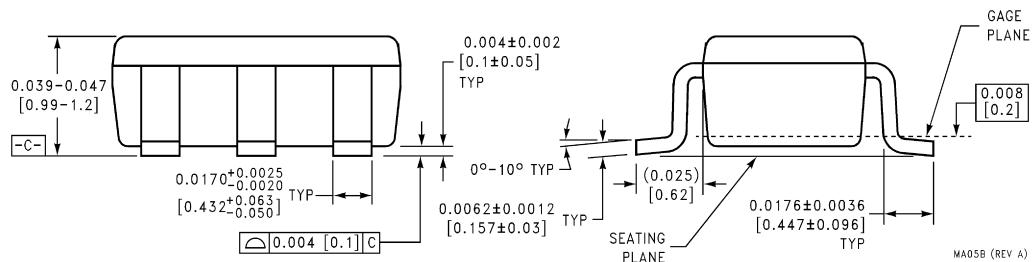
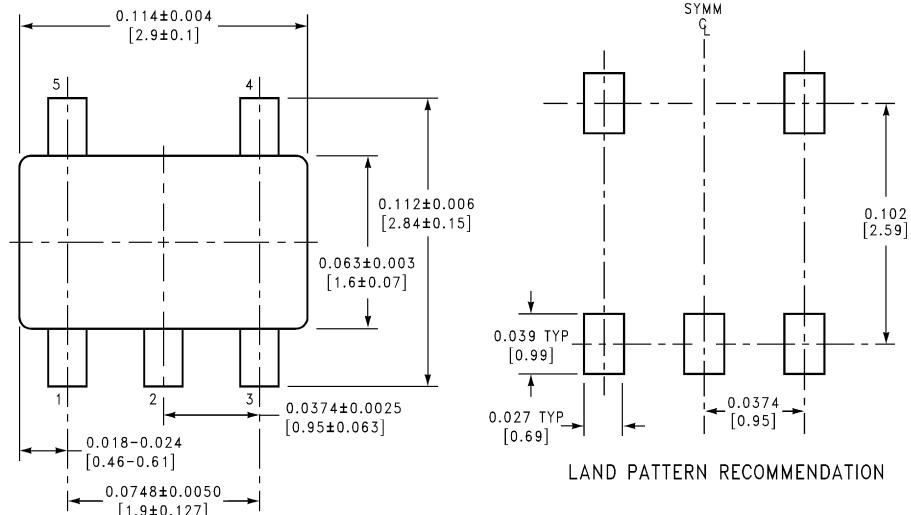


8mm	7.00 (177.8)	0.059 (1.50)	0.512 (13.00)	0.795 (20.20)	2.165 (55.00)	0.331 +0.059/-0.000 (8.40 1.50/-0.00)	0.567 (14.40)	W1 +0.078/-0.039 (W1 2.00/-1.00)
Tape Size	A	B	C	D	N	W1	W2	W3



NC7SZ05 TinyLogic UHS Inverter (Open Drain Output)

Physical Dimensions inches (millimeters) unless otherwise noted



5-Lead Molded SOT23 Enhanced Thermal
Package Number MA05B

MA05B (REV A)

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Fairchild
Corporation
Americas

Tel: 1-800-272-9959
Fax: 1-800-737-7018
Email: support@nsc.com

www.fairchildsemi.com

Fairchild
Europe

Fax: +49 (0) 1 80-530 85 86
Email: europe.support@nsc.com
Deutsch Tel: +49 (0) 1 80-530 85 85
English Tel: +49 (0) 1 80-532 78 32
Français Tel: +49 (0) 1 80-532 93 58
Italiano Tel: +49 (0) 1 80-534 16 80

Fairchild
Hong Kong Ltd.

13th Floor, Straight Block,
Ocean Centre, 5 Canton Rd.
Tsimshatsui, Kowloon
Hong Kong
Tel: (852) 2737-1600
Fax: (852) 2736-9960

National Semiconductor
Japan Ltd.

Tel: 81-3-5620-6175
Fax: 81-3-5620-6179