

# SLE 88CFX4002P

32-Bit Security Controller  
Optimized for multi-application cards with high level of security

400 kByte Flexible EEPROM  
160 kByte ROM  
16 kByte RAM

## Short Product Overview

March 2011

|                                         |  |                                        |
|-----------------------------------------|--|----------------------------------------|
| SLE 88CFX4002P Short Product Overview   |  | Ref.: Chip_Card_Product_Overview_12/10 |
| Revision History: Current Version 03.11 |  |                                        |
| Previous Releases:                      |  |                                        |
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|                           |                                                                                                                                         |
|---------------------------|-----------------------------------------------------------------------------------------------------------------------------------------|
| Product name              | SLE 88CFX4002P<br>Secure $\mu$ Slim Flash / EEPROM<br> |
| Product description       | Security cryptocontroller                                                                                                               |
| Interfaces                | ISO 7816                                                                                                                                |
| User-ROM                  | 160kByte                                                                                                                                |
| Flash                     | 400kByte                                                                                                                                |
| EEPROM                    | -                                                                                                                                       |
| RAM                       | 16kByte + 880Byte crypto                                                                                                                |
| CPU                       | 32-bit RISC                                                                                                                             |
| Symmetrical Cryptography  | 3DES                                                                                                                                    |
| Asymmetrical Cryptography | RSA up to 2048-bit,<br>ECC up to 521-bit                                                                                                |
| Ambient temperature       | -25 to +85°C                                                                                                                            |
| Delivery forms            | Module M5.1<br>MFC5.x<br>DSO-20<br>VQFN-10<br>TSSOP-20<br>die                                                                           |
| Typical applications      | GSM<br>UICC<br>Digital Signature                                                                                                        |
| Certifications            | CC EAL5+ high                                                                                                                           |

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