BUK9515-100A



N-channel TrenchMOS logic level FET Rev. 3 — 19 April 2011

Product data sheet

Product profile 1.

1.1 General description

Logic level N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using TrenchMOS technology. This product has been designed and qualified to the appropriate AEC standard for use in automotive critical applications.

1.2 Features and benefits

■ AEC Q101 compliant

Low conduction losses due to low on-state resistance

1.3 Applications

Automotive and general purpose power switching

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	-	100	V
I _D	drain current	T _{mb} = 25 °C	-	-	75	Α
P _{tot}	total power dissipation		-	-	230	W
T _j	junction temperature		-55	-	175	°C
Static cha	racteristics					
R _{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A};$ $T_j = 25 \text{ °C}$	-	11.5	14.4	mΩ
		$V_{GS} = 5 \text{ V}; I_D = 25 \text{ A};$ $T_j = 25 \text{ °C}$	-	12	15	mΩ
Avalanche	Avalanche ruggedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	$I_D = 35$ A; $V_{sup} \le 25$ V; $R_{GS} = 50$ Ω ; $V_{GS} = 5$ V; $T_{j(init)} = 25$ °C; unclamped	-	-	120	mJ



2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		_
2	D	drain	mb	D
3	S	source		
mb				mbb076 S
			SOT78A (TO-220AB)

3. Ordering information

Table 3. Ordering information

Type number			
	Name	Description	Version
BUK9515-100A	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78A

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	100	V
V_{DGR}	drain-gate voltage	$R_{GS} = 20 \text{ k}\Omega$	-	100	V
V_{GS}	gate-source voltage		-10	10	V
I_D	drain current	T _{mb} = 25 °C	-	75	Α
		T _{mb} = 100 °C	-	53	Α
I _{DM}	peak drain current	T _{mb} = 25 °C; pulsed	-	313	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C	-	230	W
T _{stg}	storage temperature		-55	175	°C
Tj	junction temperature		-55	175	°C
V_{GSM}	peak gate-source voltage	pulsed; t _p ≤ 50 μs	-15	15	V
Source-drai	n diode				
Is	source current	T _{mb} = 25 °C	-	75	Α
I _{SM}	peak source current	pulsed; T _{mb} = 25 °C	-	313	Α
Avalanche r	uggedness				
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	I_D = 35 A; V_{sup} ≤ 25 V; R_{GS} = 50 Ω ; V_{GS} = 5 V; $T_{j(init)}$ = 25 °C; unclamped	-	120	mJ
BUK9515-100A	All information provided in this document is subject to legal disclaimers. © NXP B.V. 2011. All rights rese				All rights reserve

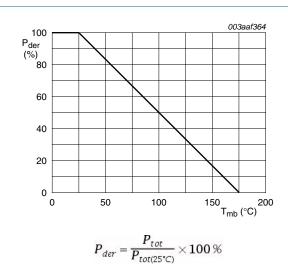


Fig 1. Normalized total power dissipation as a function of mounting base temperature

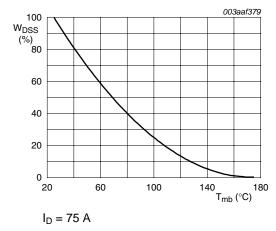
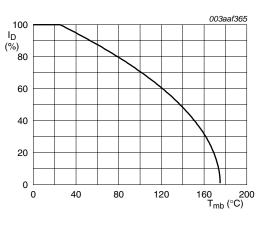


Fig 3. Normalised drain-source non-repetitive avalanche energy as a function of mounting-base temperature



$$I_{\textit{der}} = \frac{I_{\textit{D}}}{I_{\textit{D(25°C)}}} \times 100\,\%$$

V_{GS} ≥ 5 V

Fig 2. Normalized continuous drain current as a function of mounting base temperature

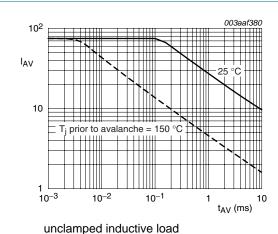
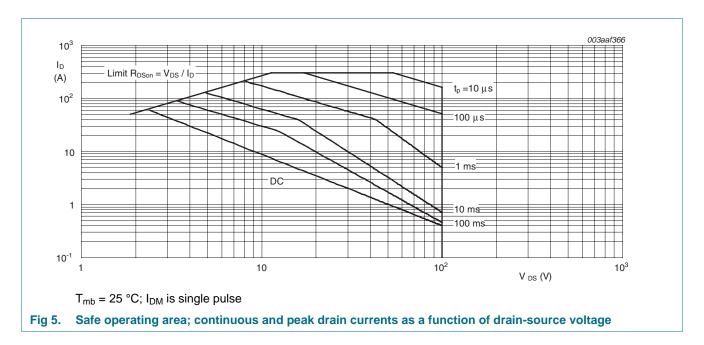


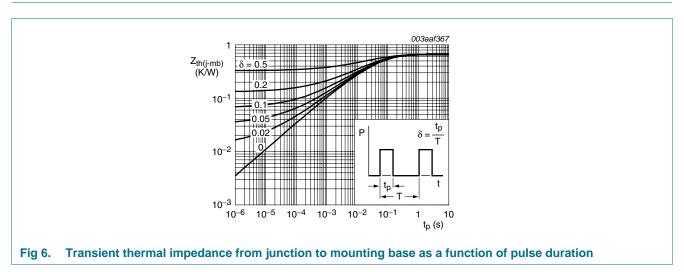
Fig 4. Single-shot avalanche rating; avalanche current as a function of avalanche period



5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{\text{th(j-mb)}}$	thermal resistance from junction to mounting base		-	-	0.65	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	-	60	-	K/W



6. Characteristics

Table 6. Characteristics

Table 6.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	racteristics					
$V_{(BR)DSS}$	drain-source	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	100	-	-	V
	breakdown voltage	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = -55 \text{ °C}$	89	-	-	V
()	gate-source threshold	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$	1	1.5	2	V
	voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ °C}$	-	-	2.3	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C}$	0.5	-	-	V
I _{DSS}	drain leakage current	$V_{DS} = 100 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 ^{\circ}\text{C}$	-	-	500	μΑ
		$V_{DS} = 100 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.05	10	μΑ
I_{GSS}	gate leakage current	$V_{GS} = 10 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	2	100	nA
		$V_{GS} = -10 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	2	100	nΑ
R _{DSon}	drain-source on-state	$V_{GS} = 4.5 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C}$	-	-	16	$m\Omega$
resis	resistance	$V_{GS} = 5 \text{ V}; I_D = 25 \text{ A}; T_j = 175 ^{\circ}\text{C}$	-	-	40.5	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C}$	-	11.5	14.4	mΩ
		$V_{GS} = 5 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C}$	-	12	15	mΩ
Dynamic	characteristics					
C _{iss}	input capacitance	$V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}; f = 1 \text{ MHz};$	-	6500	8600	pF
C _{oss}	output capacitance	T _j = 25 °C	-	550	660	pF
C _{rss}	reverse transfer capacitance		-	325	400	pF
t _{d(on)}	turn-on delay time	V_{DS} = 30 V; R_L = 1.2 Ω ; V_{GS} = 5 V;	-	45	65	ns
t _r	rise time	$R_{G(ext)} = 10 \Omega; T_j = 25 ^{\circ}C$	-	130	195	ns
t _{d(off)}	turn-off delay time		-	400	560	ns
t _f	fall time		-	130	190	ns
L _D	internal drain inductance	measured from contact screw on mounting base to centre of die; $T_j = 25 ^{\circ}\text{C}$	-	3.5	-	nΗ
		measured from drain lead 6 mm from package to centre of die; $T_j = 25$ °C	-	4.5	-	nΗ
L _S	internal source inductance	measured from source lead to source bond pad; $T_j = 25$ °C	-	7.5	-	nΗ
Source-di	rain diode					
V_{SD}	source-drain voltage	$I_S = 75 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	1.1	-	V
		$I_S = 25 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.85	1.2	V
t _{rr}	reverse recovery time	$I_S = 75 \text{ A}; dI_S/dt = -100 \text{ A/µs};$	-	60	-	ns
Q _r	recovered charge	$V_{GS} = -10 \text{ V}; V_{DS} = 30 \text{ V}; T_j = 25 \text{ °C}$	-	0.24	-	μC

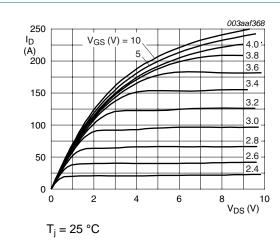


Fig 7. Output characteristics: drain current as a function of drain-source voltage; typical values

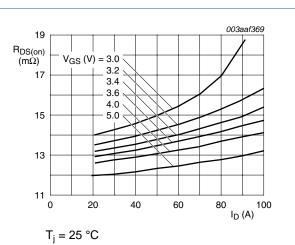


Fig 8. Drain-source on-state resistance as a function of drain current; typical values

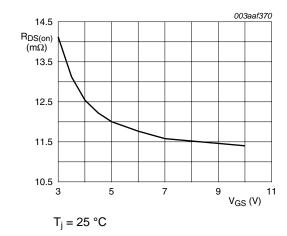


Fig 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

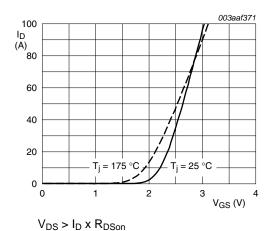
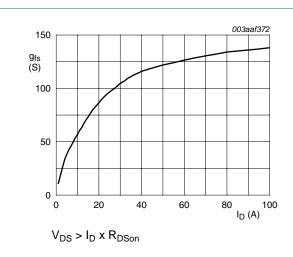
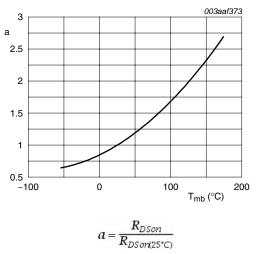


Fig 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values





 $I_D = 25 \text{ A}; V_{GS} = 5 \text{ V}$

Fig 11. Forward transconductance as a function of drain current; typical values

Fig 12. Not factorized transconductance as a function of drain current; typical values



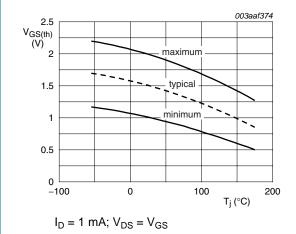


Fig 13. Gate-source threshold voltage as a function of junction temperature

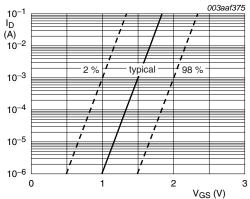


Fig 14. Sub-threshold drain current as a function of gate-source voltage

 $T_j = 25 \,^{\circ}C; V_{DS} = V_{GS}$

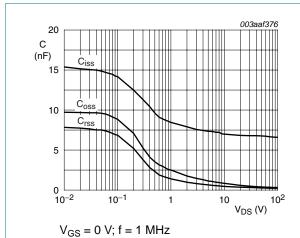
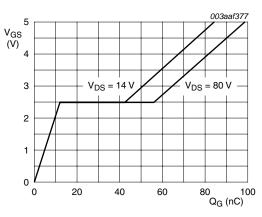
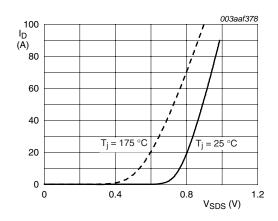


Fig 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



 $T_i = 25 \, ^{\circ}C; I_D = 25 \, A$

Fig 16. Gate-source voltage as a function of gate charge; typical values



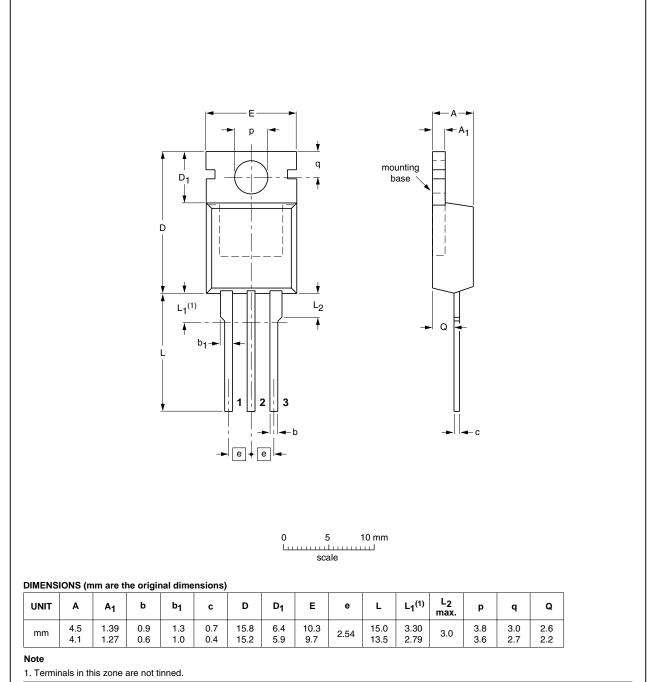
 $V_{GS} = 0 V$

Fig 17. Source (diode forward) current as a function of source-drain (diode forward) voltage; typical values

7. Package outline

Plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB

SOT78A



OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA	PROJECTION	155UE DATE	
SOT78A		3-lead TO-220AB	SC-46		03-01-22 05-03-14	

Fig 18. Package outline SOT78A (TO-220AB)

BUK9515-100A

8. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BUK9515-100A v.3	20110419	Product data sheet	-	BUK9515_9615-100A_2
Modifications:		t of this data sheet has b of NXP Semiconductors	•	comply with the new identity
	 Legal texts 	s have been adapted to t	he new company i	name where appropriate.
	Type num	ber BUK9515-100A sepa	rated from data sh	neet BUK9515_9615-100A_2.
BUK9515_9615-100A_2	19991101	Product specification	-	-

9. Legal information

9.1 Data sheet status

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

9.2 Definitions

Preview — The document is a preview version only. The document is still subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet

9.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use in automotive applications — This NXP Semiconductors product has been qualified for use in automotive applications. The product is not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the

BUK9515-100A

N-channel TrenchMOS logic level FET

Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

9.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Adelante, Bitport, Bitsound, CoolFlux, CoReUse, DESFire, EZ-HV, FabKey, GreenChip, HiPerSmart, HITAG, I²C-bus logo, ICODE, I-CODE, ITEC, Labelution, MIFARE, MIFARE Plus, MIFARE Ultralight, MoReUse, QLPAK, Silicon Tuner, SiliconMAX, SmartXA, STARplug, TOPFET, TrenchMOS, TriMedia and UCODE — are trademarks of NXP B.V.

HD Radio and **HD Radio** logo — are trademarks of iBiquity Digital Corporation.

10. Contact information

For more information, please visit: http://www.nxp.com

For sales office addresses, please send an email to: salesaddresses@nxp.com

11. Contents

1	Product profile
1.1	General description1
1.2	Features and benefits
1.3	Applications
1.4	Quick reference data1
2	Pinning information2
3	Ordering information2
4	Limiting values2
5	Thermal characteristics4
6	Characteristics5
7	Package outline
8	Revision history10
9	Legal information11
9.1	Data sheet status
9.2	Definitions11
9.3	Disclaimers
9.4	Trademarks12
10	Contact information12

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

NXP:

BUK9515-100A