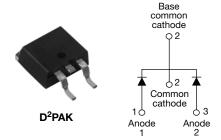


Vishay High Power Products

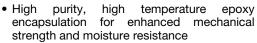
Schottky Rectifier, 2 x 15 A



PRODUCT SUMMARY						
I _{F(AV)}	2 x 15 A					
V_{R}	30 V					

FEATURES

- 150 °C T_{.I} operation
- Center tap configuration
- Very low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Halogen-free according to IEC 61249-2-21 definition
- Compliant to RoHS directive 2002/95/EC
- AEC-Q101 qualified

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	30	Α						
V_{RRM}		30	V						
I _{FSM}	t _p = 5 µs sine	1100	A						
V _F	15 Apk, T _J = 125 °C (per leg)	0.34	V						
T _J	Range	- 55 to 150	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-MBRB3030CTLPbF	UNITS					
Maximum DC reverse voltage	V_{R}	30	V					
Maximum working peak reverse voltage	V_{RWM}	30	V					

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	PARAMETER SYMBOL TEST CONDITIONS		VALUES	UNITS				
Maximum average per leg forward current per device			50 % duty cycle at T _C = 121 °C	rootangular wayafarm	15			
		I _{F(AV)}	30 % duty cycle at 1°C = 121°C	30	^			
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	1100	A		
			10 ms sine or 6 ms rect. pulse	360				
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 3 A, L = 2.9 mH		13	mJ		
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		3	Α		

VS-MBRB3030CTLPbF

Vishay High Power Products Schottky Rectifier, 2 x 15 A



ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
		15 A	T _{.1} = 25 °C	0.47	V			
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	30 A	1j=25 C	0.55				
See fig. 1	VFM (*)	15 A	T 105 °C	0.34				
		30 A	T _J = 125 °C	0.45				
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V Dated V	2	mA			
See fig. 2		T _J = 125 °C	V _R = Rated V _R	183				
Threshold voltage	V _{F(TO)}			0.22	V			
Forward slope resistance	r _t	$T_J = T_J$ maximum		6.76	mΩ			
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range	2840	pF				
Typical series inductance per leg	L _S	Measured lead to lead 5 r	8.0	nH				
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance,	per leg	В	DC operation	2.0	°C/W
junction to case	per package	R_{thJC}	DC operation	1.0	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight				2	g
Approximate weight				0.07	oz.
Maunting toyour	minimum			6 (5)	kgf · cm
Mounting torque	maximum			12 (10)	(lbf \cdot in)
Marking device	Case style D ² PAK MBRE		MBRB3	030CTL	



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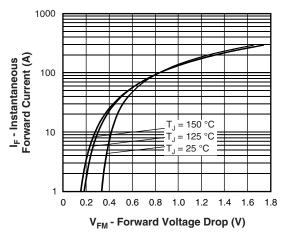


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

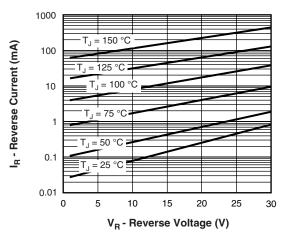


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

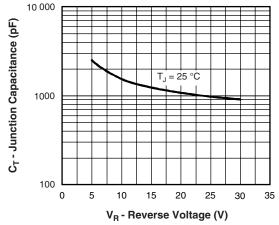


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

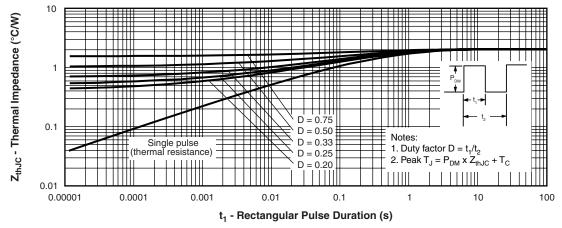


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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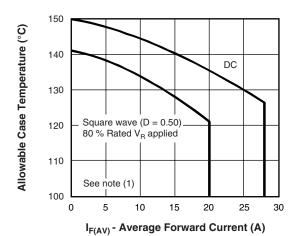


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

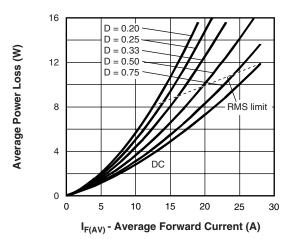


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

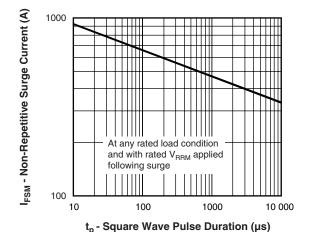


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

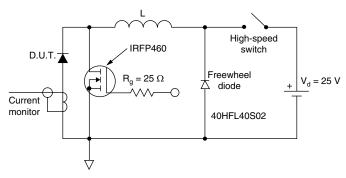


Fig. 8 - Unclamped Inductive Test Circuit

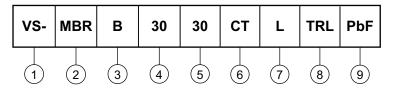
 $^{(1)}$ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$ $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$ at (I_{F(AV)}/D) (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R$ (1 - D); I_R at $V_{R1} = 10 \ V$



Schottky Rectifier, 2 x 15 A Vishay High Power Products

ORDERING INFORMATION TABLE

Device code



- 1 HPP product suffix
- 2 Schottky MBR series
- Current rating (30 = 30 A)
- 5 Voltage rating (30 = 30 V)
- 6 CT = Center tap (dual)
- 7 L = Low V_F
- 8 • None = Tube (50 pieces)
 - TRL = Tape and reel (left oriented for D²PAK only)
 - TRR = Tape and reel (right oriented for D²PAK only)
- 9 • PbF = Lead (Pb)-free (for D²PAK tube)
 - P = Lead (Pb)-free (for D²PAK TRR and TRL)

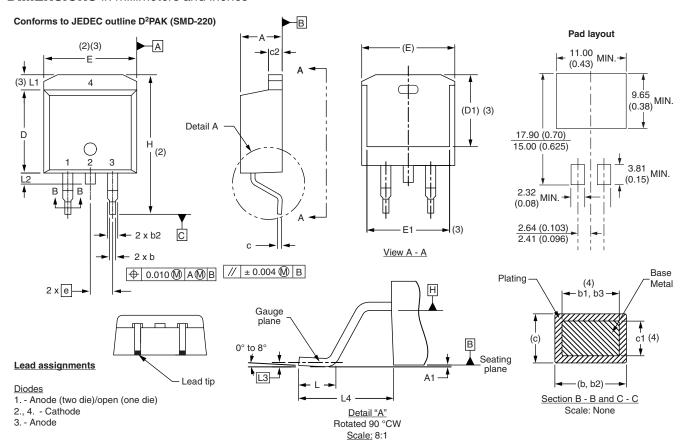
LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95046					
Part marking information	www.vishay.com/doc?95054					
Packaging information	www.vishay.com/doc?95032					



Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INC	INCHES		NOTES	NOTES	SYMBOL	MILLIM	ETERS	INC	HES
STWBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STWIDOL	MIN.	MAX.	MIN.	MAX.	
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

- $^{(1)}$ Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC outline TO-263AB

NOTES

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2, 3

3

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Legal Disclaimer Notice

Vishay

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