

SBOS201 – MAY 2001

## 12V, 7MHz, CMOS, Rail-to-Rail I/O OPERATIONAL AMPLIFIERS

### FEATURES

- **HIGH SPEED:** 7MHz, 10V/ $\mu$ s
- **RAIL-TO-RAIL INPUT AND OUTPUT**
- **WIDE SUPPLY RANGE:**  
Single Supply: 3.5V to 12V  
Dual Supplies:  $\pm 1.75$ V to  $\pm 6$ V
- **LOW QUIESCENT CURRENT:** 1.1mA
- **FULL-SCALE CMRR:** 84dB
- **MicroSIZE PACKAGES:**  
SOT23-5, MSOP-8, TSSOP-14
- **LOW INPUT BIAS CURRENT:** 1pA

### APPLICATIONS

- **LCD GAMMA CORRECTION**
- **AUTOMOTIVE APPLICATIONS:**  
Audio, Sensor Applications, Security Systems
- **PORTABLE EQUIPMENT**
- **ACTIVE FILTERS**
- **TRANSDUCER AMPLIFIER**
- **TEST EQUIPMENT**
- **DATA ACQUISITION**

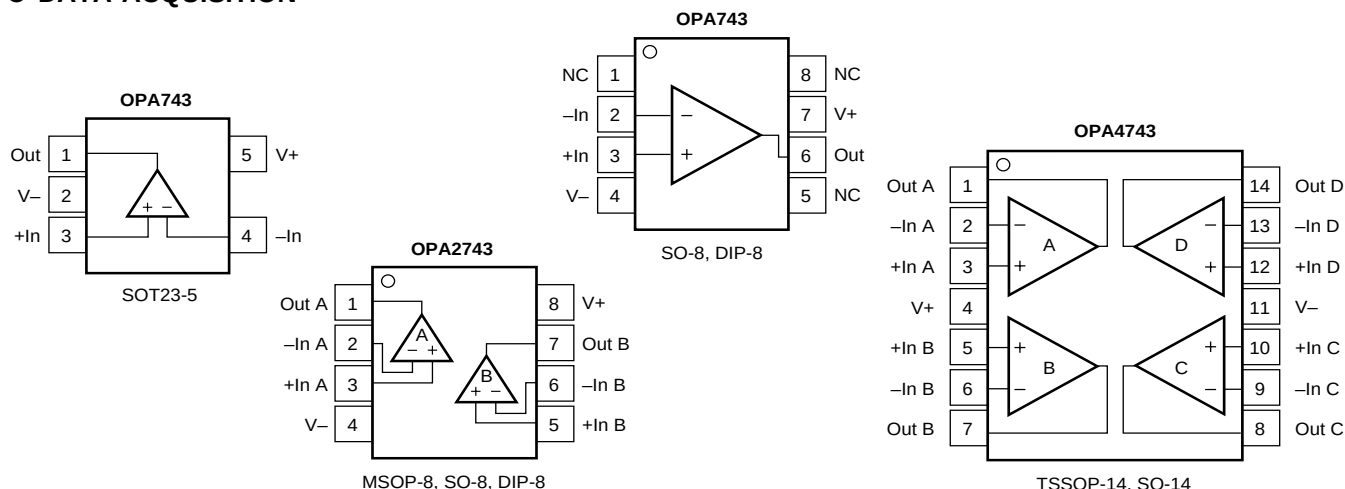
### DESCRIPTION

The OPA743 series utilizes a state-of-the-art 12V analog CMOS process and offers outstanding AC performance, such as 7MHz GBW, 10V/ $\mu$ s slew rate and 0.0008% THD+N. Optimized for single supply operation up to 12V, the input common-mode range extends beyond the power supply rails and the output swings to within 100mV of the rails. The low quiescent current of 1.1mA makes it well suited for use in battery operated equipment.

The OPA743 series' ability to drive high output currents together with 12V operation makes it particularly useful for use as gamma correction reference buffer in LCD panels.

For ease of use the OPA743 op-amp family is fully specified and tested over the supply range of  $\pm 1.75$ V to  $\pm 6$ V. Single, dual and quad versions are available.

The single versions (OPA743) are available in the *MicroSIZE* SOT23-5 and in the standard SO-8 surface-mount, as well as DIP-8 packages. Dual versions (OPA2743) are available versions in the MSOP-8, SO-8, and DIP-8 packages. The quad versions (OPA4743) are available in the TSSOP-14 and SO-14 packages. All are specified for operation from  $-40^{\circ}$ C to  $+85^{\circ}$ C.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

## ABSOLUTE MAXIMUM RATINGS<sup>(1)</sup>

|                                                      |                          |
|------------------------------------------------------|--------------------------|
| Supply Voltage, V+ to V-.....                        | 13.2V                    |
| Signal Input Terminals, Voltage <sup>(2)</sup> ..... | (V-) -0.3V to (V+) +0.3V |
| Current <sup>(2)</sup> .....                         | 10mA                     |
| Output Short-Circuit <sup>(3)</sup> .....            | Continuous               |
| Operating Temperature .....                          | -55°C to +125°C          |
| Storage Temperature .....                            | -65°C to +150°C          |
| Junction Temperature.....                            | +150°C                   |
| Lead Temperature (soldering, 10s).....               | +300°C                   |

NOTES: (1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. (2) Input terminals are diode-clamped to the power supply rails. Input signals that can swing more than 0.3V beyond the supply rails should be current-limited to 10mA or less. (3) Short-circuit to ground, one amplifier per package.



## ELECTROSTATIC DISCHARGE SENSITIVITY

This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

## PACKAGE/ORDERING INFORMATION

| PRODUCT       | PACKAGE  | PACKAGE DRAWING NUMBER | PACKAGE MARKING | ORDERING NUMBER <sup>(1)</sup> | TRANSPORT MEDIA |
|---------------|----------|------------------------|-----------------|--------------------------------|-----------------|
| <b>Single</b> |          |                        |                 |                                |                 |
| OPA743NA      | SOT23-5  | 331                    | D43             | OPA743NA/250                   | Tape and Reel   |
| "             | "        | "                      | "               | OPA743NA/3K                    | Tape and Reel   |
| OPA743UA      | SO-8     | 182                    | OPA743UA        | OPA743UA                       | Rails           |
| "             | "        | "                      | "               | OPA743UA/2K5                   | Tape and Reel   |
| OPA743PA      | DIP-8    | 006                    | OPA743PA        | OPA743PA                       | Rails           |
| <b>Dual</b>   |          |                        |                 |                                |                 |
| OPA2743EA     | MSOP-8   | 337                    | E43             | OPA2743EA/250                  | Tape and Reel   |
| "             | "        | "                      | "               | OPA2743EA/2K5                  | Tape and Reel   |
| OPA2743UA     | SO-8     | 182                    | OPA2743UA       | OPA2743UA                      | Rails           |
| "             | "        | "                      | "               | OPA2743UA/2K5                  | Tape and Reel   |
| OPA2743PA     | DIP-8    | 006                    | OPA2743PA       | OPA2743PA                      | Rails           |
| <b>Quad</b>   |          |                        |                 |                                |                 |
| OPA4743EA     | TSSOP-14 | 357                    | OPA4743EA       | OPA4743EA/250                  | Tape and Reel   |
| "             | "        | "                      | "               | OPA4743EA/2K5                  | Tape and Reel   |
| OPA4743UA     | SO-14    | 235                    | OPA4743UA       | OPA4743UA                      | Rails           |
| "             | "        | "                      | "               | OPA4743UA/2K5                  | Tape and Reel   |

NOTE: (1) Models with a slash (/) are available only in Tape and Reel in the quantities indicated (e.g., /3K indicates 3000 devices per reel). Ordering 3000 pieces of "OPA743NA/3K" will get a single 3000-piece Tape and Reel.

# ELECTRICAL CHARACTERISTICS: $V_S = 3.5V$ to $12V$

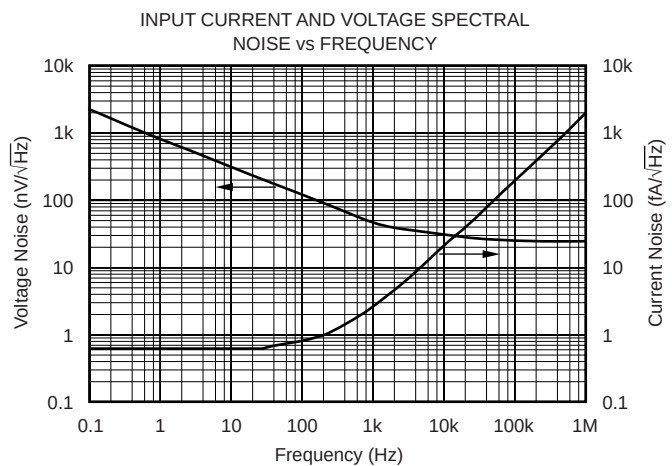
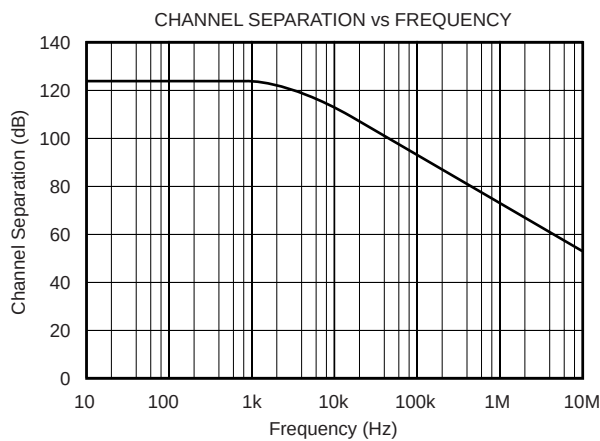
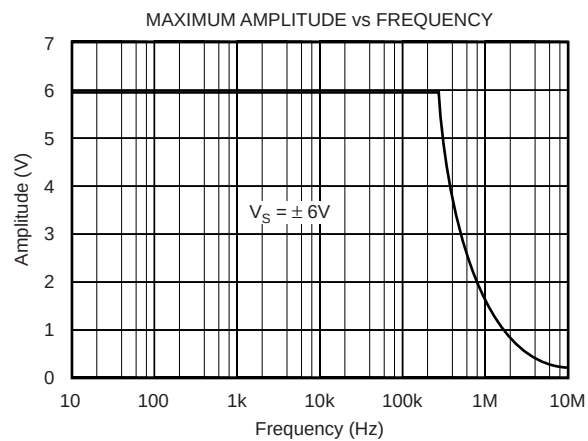
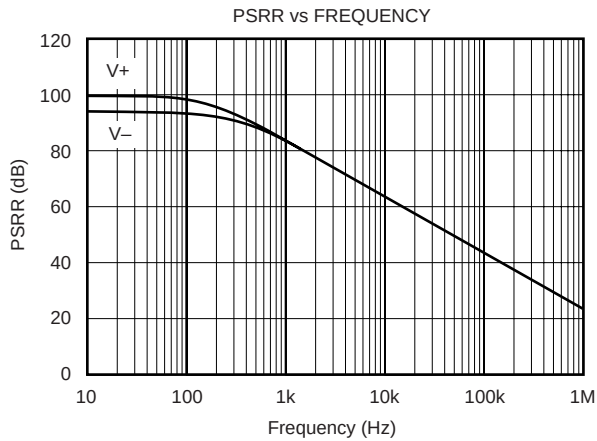
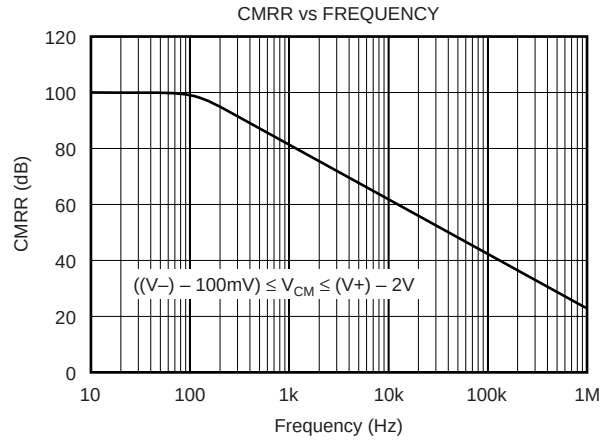
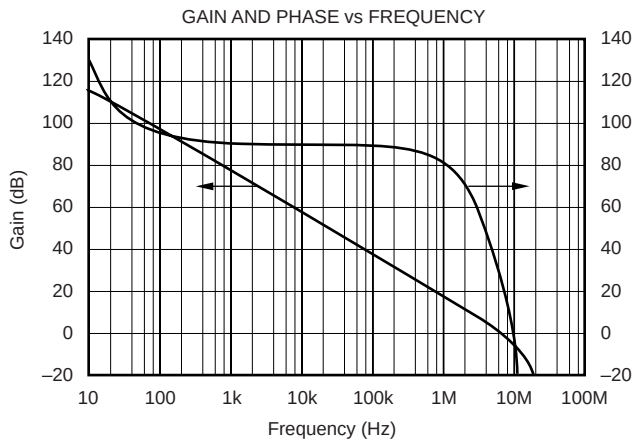
**Boldface** limits apply over the specified temperature range,  $T_A = -40^{\circ}C$  to  $+85^{\circ}C$

At  $T_A = +25^{\circ}C$ ,  $R_L = 10k\Omega$  connected to  $V_S/2$  and  $V_{OUT} = V_S/2$ , unless otherwise noted.

| PARAMETER                                                                                                                                                                                                                              | CONDITION                                                                                                                                                                                                                                                                | OPA743NA, UA, PA<br>OPA2743EA, UA, PA<br>OPA4743EA, UA   |                                                               |                                        | UNITS                                                                                                                                           |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------|---------------------------------------------------------------|----------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------|
|                                                                                                                                                                                                                                        |                                                                                                                                                                                                                                                                          | MIN                                                      | TYP                                                           | MAX                                    |                                                                                                                                                 |
| <b>OFFSET VOLTAGE</b><br>Input Offset Voltage<br><b>Drift</b><br>vs Power Supply<br><b>Over Temperature</b><br>Channel Separation, dc<br>$f = 10kHz$                                                                                   | $V_{OS}$<br>$dV_{OS}/dT$<br>PSRR<br>$V_S = \pm 5V, V_{CM} = 0V$<br>$T_A = -40^{\circ}C$ to $+85^{\circ}C$<br>$V_S = \pm 1.75V$ to $\pm 6V, V_{CM} = -0.25$<br>$V_S = \pm 1.75V$ to $\pm 6V, V_{CM} = -0.25$                                                              |                                                          | $\pm 1.5$<br>$\pm 8$<br>10<br>1<br>110                        | $\pm 7$<br><br>100<br><b>200</b>       | mV<br>$\mu V/^{\circ}C$<br>$\mu V/V$<br>$\mu V/V$<br>dB                                                                                         |
| <b>INPUT VOLTAGE RANGE</b><br>Common-Mode Voltage Range<br>Common-Mode Rejection Ratio<br><b>over Temperature</b>                                                                                                                      | $V_{CM}$<br>CMRR<br>$V_S = \pm 5V, (V-) - 0.1V < V_{CM} < (V+) + 0.1V$<br>$V_S = \pm 5V, (V-) < V_{CM} < (V+)$<br>$V_S = \pm 5V, (V-) - 0.1V < V_{CM} < (V+) - 2V$<br>$V_S = \pm 5V, (V-) < V_{CM} < (V+) - 2V$<br>$V_S = \pm 1.75V, (V-) - 0.1V < V_{CM} < (V+) + 0.1V$ | $(V-) - 0.1$<br>66<br><b>60</b><br>70<br><b>70</b><br>60 | 84<br><br>90                                                  | $(V+) + 0.1$                           | V<br>dB<br><b>dB</b><br>dB<br><b>dB</b><br>dB                                                                                                   |
| <b>INPUT BIAS CURRENT</b><br>Input Bias Current<br>Input Offset Current                                                                                                                                                                | $I_B$<br>$I_{OS}$<br>$V_S = \pm 6V, V_{CM} = 0V$<br>$V_S = \pm 6V, V_{CM} = 0V$                                                                                                                                                                                          |                                                          | $\pm 1$<br>$\pm 0.5$                                          | $\pm 10$<br>$\pm 10$                   | pA<br>pA                                                                                                                                        |
| <b>INPUT IMPEDANCE</b><br>Differential<br>Common-Mode                                                                                                                                                                                  |                                                                                                                                                                                                                                                                          |                                                          | $4 \cdot 10^9 \parallel 4$<br>$5 \cdot 10^{12} \parallel 4$   |                                        | $\Omega \parallel pF$<br>$\Omega \parallel pF$                                                                                                  |
| <b>NOISE</b><br>Input Voltage Noise, $f = 0.1Hz$ to $10Hz$<br>Input Voltage Noise Density, $f = 10kHz$<br>Current Noise Density, $f = 1kHz$                                                                                            | $e_n$<br>$i_n$<br>$V_S = \pm 6V, V_{CM} = 0V$<br>$V_S = \pm 6V, V_{CM} = 0V$<br>$V_S = \pm 6V, V_{CM} = 0V$                                                                                                                                                              |                                                          | 11<br>30<br>2.5                                               |                                        | $\mu Vp-p$<br>$nV/\sqrt{Hz}$<br>$fA/\sqrt{Hz}$                                                                                                  |
| <b>OPEN-LOOP GAIN</b><br>Open-Loop Voltage Gain<br><b>over Temperature</b>                                                                                                                                                             | $A_{OL}$<br>$R_L = 100k\Omega, (V-) + 0.1V < V_O < (V+) - 0.1V$<br>$R_L = 100k\Omega, (V-) + 0.125V < V_O < (V+) - 0.125V$<br>$R_L = 1k, (V-) + 0.325V < V_O < (V+) - 0.325V$<br>$R_L = 1k, (V-) + 0.450V < V_O < (V+) - 0.450V$                                         | 106<br><b>100</b><br>86<br><b>96</b>                     | 120<br><br>100                                                |                                        | dB<br><b>dB</b><br>dB<br><b>dB</b>                                                                                                              |
| <b>OUTPUT</b><br>Voltage Output Swing from Rail<br><b>over Temperature</b><br><br><b>over Temperature</b><br>Output Current<br>Short-Circuit Current<br>Capacitive Load Drive                                                          | $I_{OUT}$<br>$I_{SC}$<br>$C_{LOAD}$<br>$R_L = 100k\Omega, A_{OL} > 106dB$<br>$R_L = 100k\Omega, A_{OL} > 100dB$<br>$R_L = 1k\Omega, A_{OL} > 86dB$<br>$R_L = 1k\Omega, A_{OL} > 96dB$<br>$ V_S - V_{OUT}  < 1V$                                                          |                                                          | 75<br><b>100</b><br>300<br><b>425</b><br>$\pm 20$<br>$\pm 30$ | 100<br><b>125</b><br>325<br><b>450</b> | mV<br><b>mV</b><br>mV<br><b>mV</b><br>mA<br>mA                                                                                                  |
| <b>FREQUENCY RESPONSE</b><br>Gain-Bandwidth Product<br>Slew Rate<br>Settling Time, 0.1%<br>0.01%<br>Overload Recovery Time<br>Total Harmonic Distortion + Noise                                                                        | GBW<br>SR<br>$t_S$<br>$t_{0.01\%}$<br>THD+N<br>$C_L = 15pF$<br>$G = +1$<br>$V_S = \pm 6V, G = +1$<br>$V_S = \pm 6V, 5V$ Step, $G = +1$<br>$V_S = \pm 6V, 5V$ Step, $G = +1$<br>$V_{IN} \cdot \text{Gain} = V_S$<br>$V_S = \pm 6V, V_O = 1V_{rms}, G = +1, f = 6kHz$      |                                                          | 7<br>10<br>9<br>15<br>200<br>0.0008                           |                                        | MHz<br>$V/\mu s$<br>$\mu s$<br>$\mu s$<br>ns<br>%                                                                                               |
| <b>POWER SUPPLY</b><br>Specified Voltage Range, Single Supply<br>Specified Voltage Range, Dual Supplies<br>Quiescent Current (per amplifier)<br><b>over Temperature</b>                                                                | $V_S$<br>$V_S$<br>$I_Q$<br>$I_O = 0$                                                                                                                                                                                                                                     | 3.5<br>$\pm 1.75$                                        | 1.1                                                           | 12<br>$\pm 6$<br>1.5<br><b>1.7</b>     | V<br>V<br>mA<br><b>mA</b>                                                                                                                       |
| <b>TEMPERATURE RANGE</b><br>Specified Range<br>Operating Range<br>Storage Range<br>Thermal Resistance<br>SOT23-5 Surface-Mount<br>MSOP-8 Surface-Mount<br>TSSOP-14 Surface-Mount<br>SO-8 Surface Mount<br>SO-14 Surface Mount<br>DIP-8 | $\theta_{JA}$                                                                                                                                                                                                                                                            | -40<br>-55<br>-65                                        |                                                               | 85<br>125<br>150                       | $^{\circ}C$<br>$^{\circ}C$<br>$^{\circ}C$<br>$^{\circ}C/W$<br>$^{\circ}C/W$<br>$^{\circ}C/W$<br>$^{\circ}C/W$<br>$^{\circ}C/W$<br>$^{\circ}C/W$ |

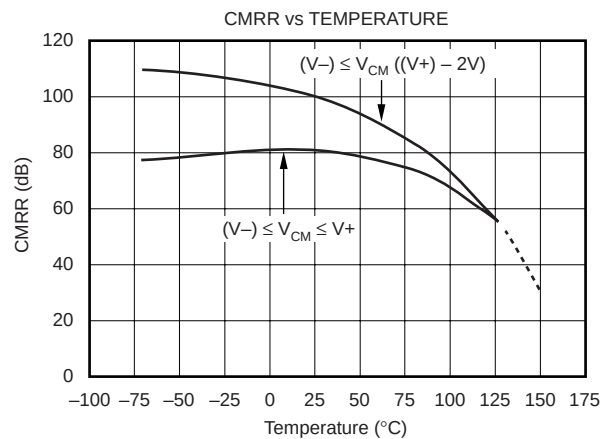
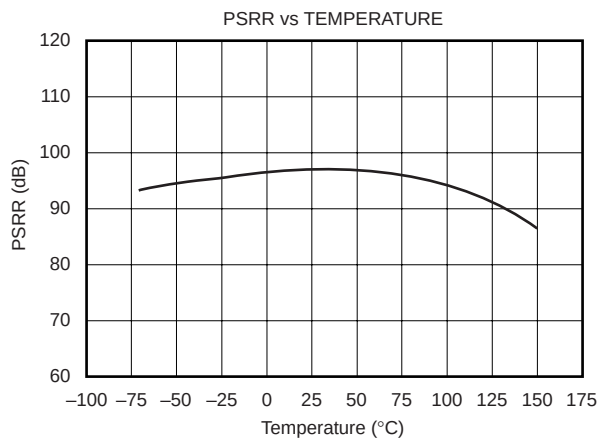
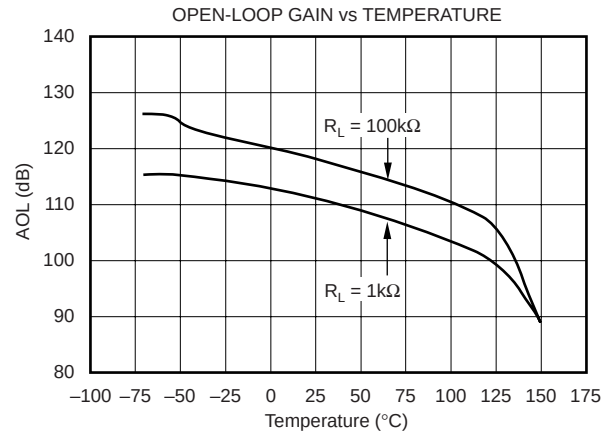
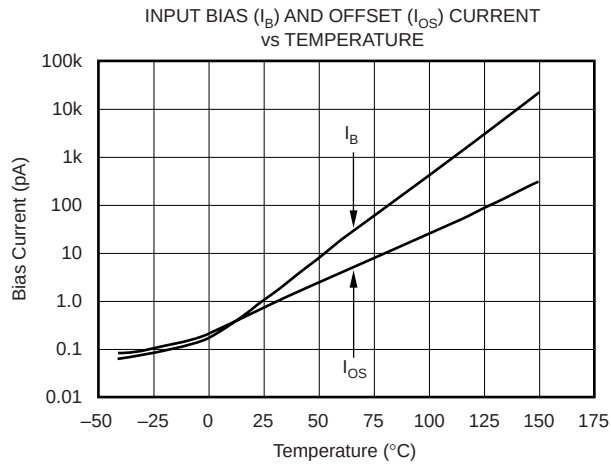
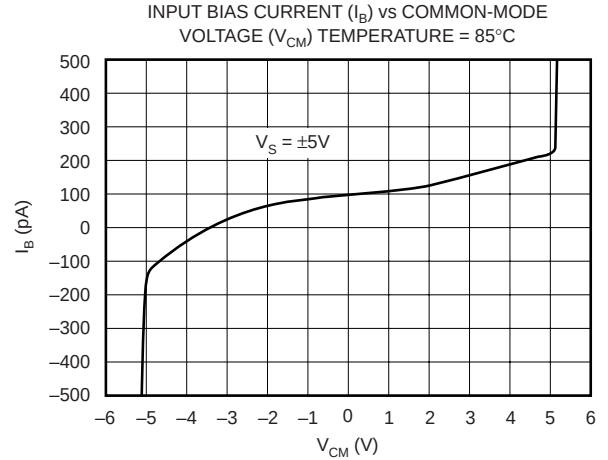
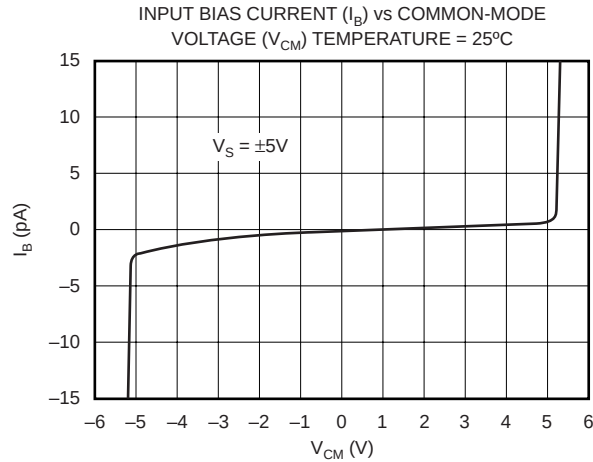
# TYPICAL CHARACTERISTICS

At  $T_A = +25^\circ\text{C}$ ,  $V_S = \pm 6\text{V}$ , and  $R_L = 10\text{k}\Omega$ , unless otherwise noted.



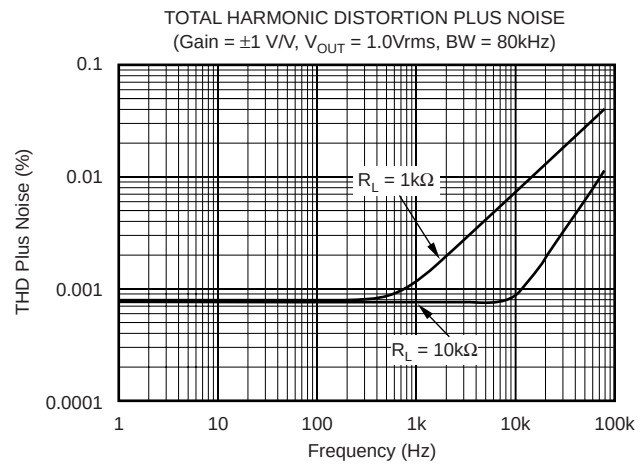
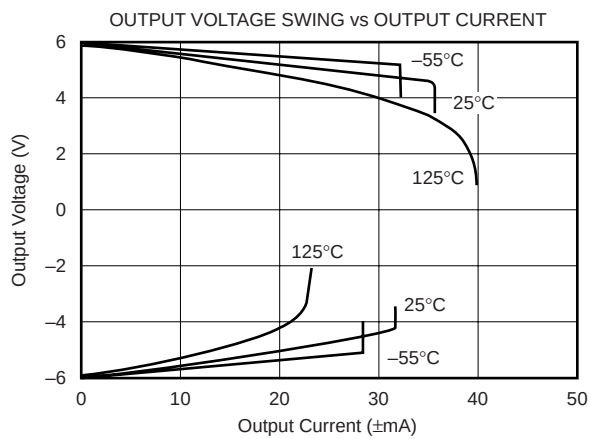
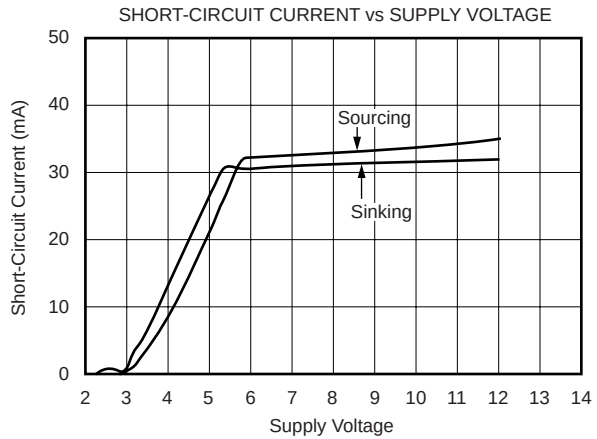
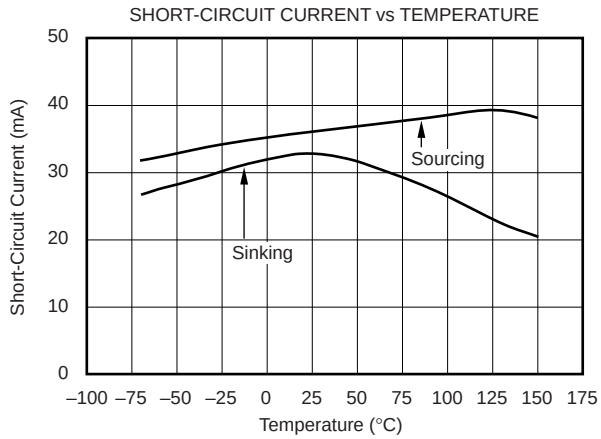
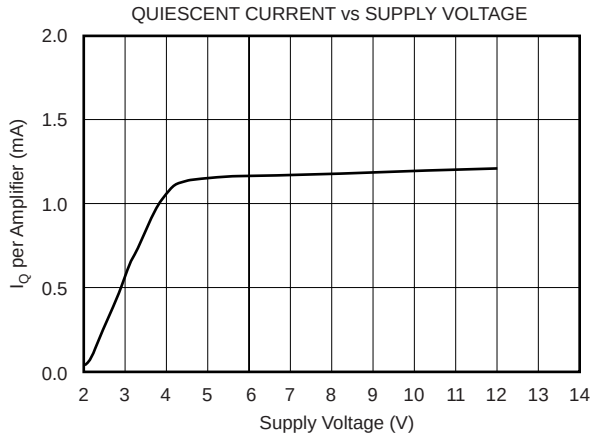
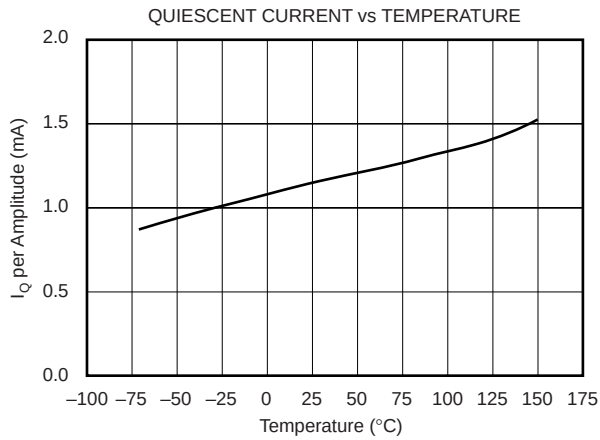
# TYPICAL CHARACTERISTICS (Cont.)

At  $T_A = +25^\circ\text{C}$ ,  $V_S = \pm 5\text{V}$ , and  $R_L = 10\text{k}\Omega$ , unless otherwise noted.



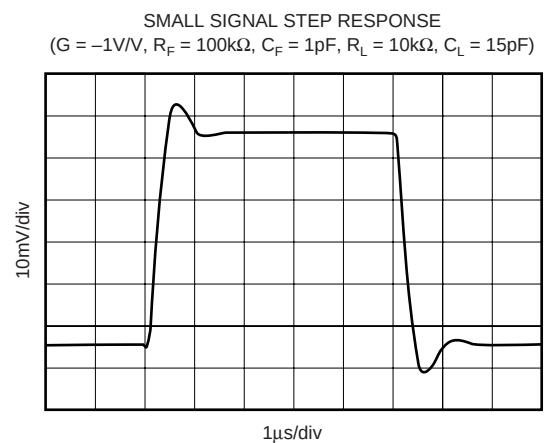
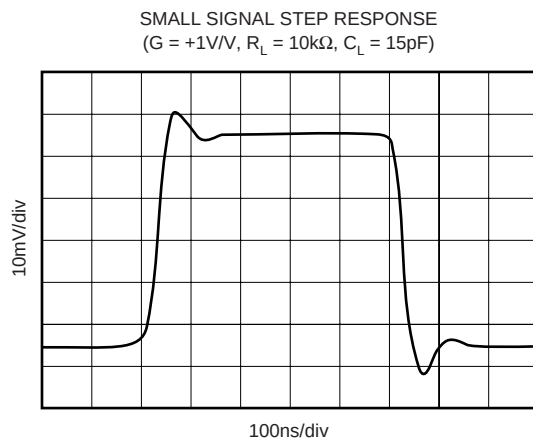
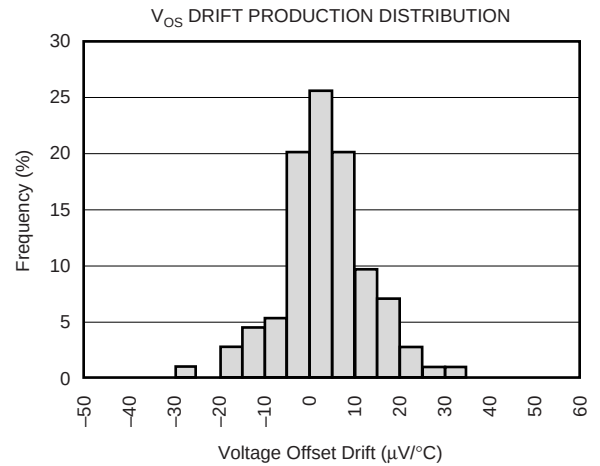
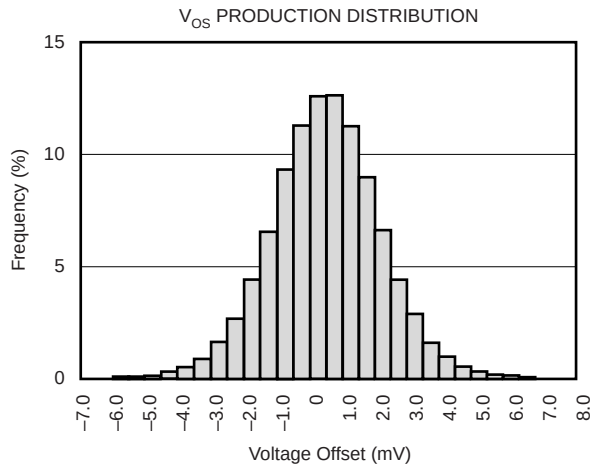
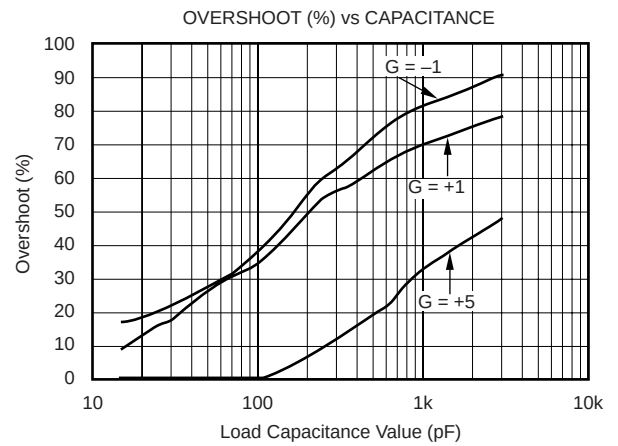
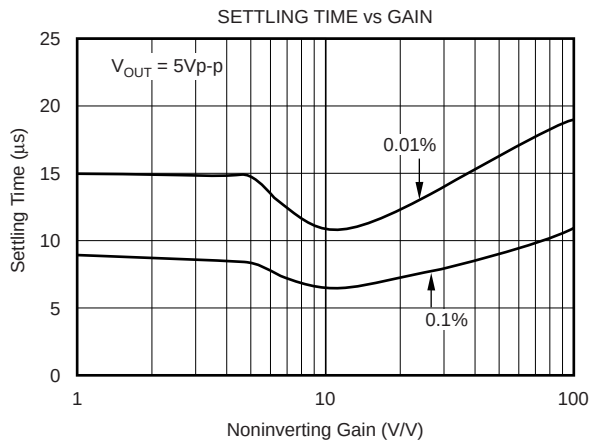
# TYPICAL CHARACTERISTICS (Cont.)

At  $T_A = +25^\circ\text{C}$ ,  $V_S = \pm 6\text{V}$ , and  $R_L = 10\text{k}\Omega$ , unless otherwise noted.



# TYPICAL CHARACTERISTICS (Cont.)

At  $T_A = +25^\circ\text{C}$ ,  $V_S = \pm 6\text{V}$ , and  $R_L = 10\text{k}\Omega$ , unless otherwise noted.

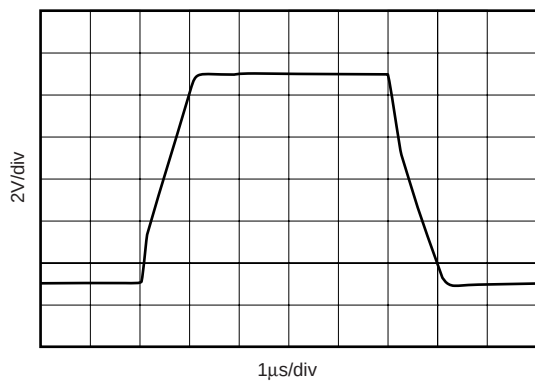


NOTE:  $C_F$  is used to optimize settling time.

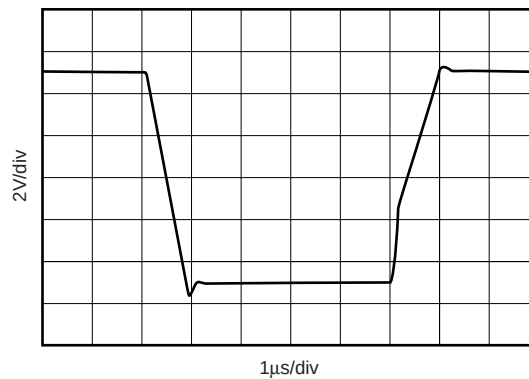
## TYPICAL CHARACTERISTICS (Cont.)

At  $T_A = +25^\circ\text{C}$ ,  $V_S = \pm 6\text{V}$ , and  $R_L = 10\text{k}\Omega$ , unless otherwise noted.

LARGE SIGNAL STEP RESPONSE  
( $G = +1\text{V/V}$ ,  $R_L = 10\text{k}\Omega$ ,  $C_L = 15\text{pF}$ )



LARGE SIGNAL STEP RESPONSE  
( $G = -1\text{V/V}$ ,  $R_L = 10\text{k}\Omega$ ,  $C_L = 15\text{pF}$ )





## APPLICATIONS INFORMATION

OPA743 series op amps can operate on 1.1mA quiescent current from a single (or split) supply in the range of 3.5V to 12V ( $\pm 1.75\text{V}$  to  $\pm 6\text{V}$ ), making them highly versatile and easy to use. The OPA743 is unity-gain stable and offers 7MHz bandwidth and 10V/ $\mu\text{s}$  slew rate.

Rail-to-rail input and output swing helps maintain dynamic range, especially in low supply applications. Figure 1 shows the input and output waveforms for the OPA743 in unity-gain configuration. On a  $\pm 6\text{V}$  supply with a 100k $\Omega$  load connected to  $V_S/2$ . The output is tested to swing within 100mV to the rail.

Power-supply pins should be bypassed with 1000pF ceramic capacitors in parallel with 1 $\mu\text{F}$  tantalum capacitors.

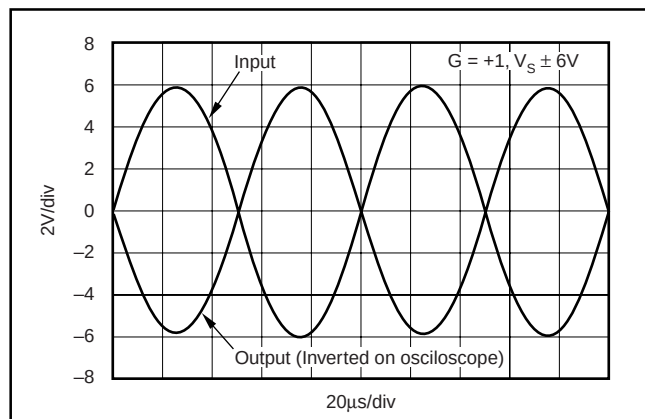


FIGURE 1. Rail-to-Rail Input and Output.

### OPERATING VOLTAGE

OPA743 series op amps are fully specified and guaranteed from 3.5V to 12V over a temperature range of  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ . Parameters that vary significantly with operating voltages or temperature are shown in the Typical Characteristics.

### RAIL-TO-RAIL INPUT

The input common-mode voltage range of the OPA743 series extends 100mV beyond the supply rails at room temperature. This is achieved with a complementary input stage—an N-channel input differential pair in parallel with a P-channel differential pair. The N-channel pair is active for input voltages close to the positive rail, typically  $(V+) - 2.0\text{V}$  to 100mV above the positive supply, while the P-channel pair is on for inputs from 100mV below the negative supply to approximately  $(V+) - 1.5\text{V}$ . There is a small transition region, typically  $(V+) - 2.0\text{V}$  to  $(V+) - 1.5\text{V}$ , in which both pairs are on. This 500mV transition region can vary  $\pm 100\text{mV}$  with process variation. Thus, the transition region (both stages on) can range from  $(V+) - 2.1\text{V}$  to  $(V+) - 1.4\text{V}$  on the low end, up to  $(V+) - 1.9\text{V}$  to  $(V+) - 1.6\text{V}$  on the high end. Most rail-to-rail op amps on the market use this two input stage approach, and exhibit a transition region where CMRR, offset voltage, and THD may vary compared to operation outside this region.

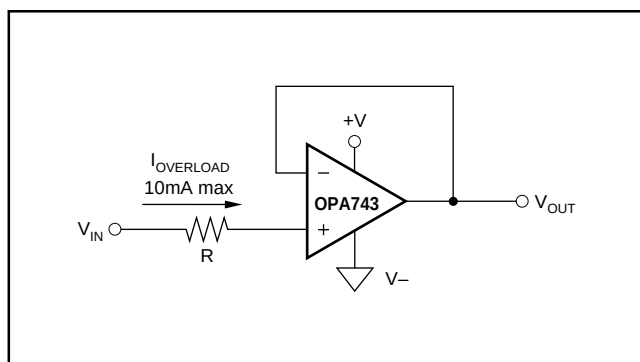


FIGURE 2. Input Current Protection for Voltages Exceeding the Supply Voltage.

### INPUT VOLTAGE

Device inputs are protected by ESD diodes that will conduct if the input voltages exceed the power supplies by more than approximately 300mV. Momentary voltages greater than 300mV beyond the power supply can be tolerated if the current is limited to 10mA. This is easily accomplished with an input resistor, in series with the op amp input as shown in Figure 2. Many input signals are inherently current-limited to less than 10mA; therefore, a limiting resistor is not always required. The OPA743 features no phase inversion when the inputs extend beyond supplies if the input current is limited, as seen in Figure 3.

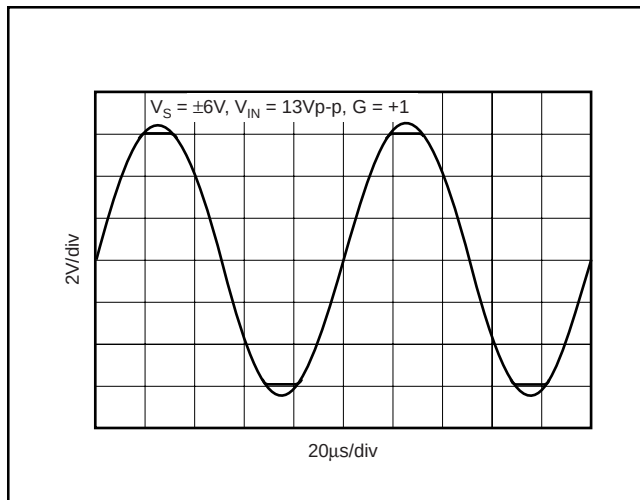


FIGURE 3. OPA743—No Phase Inversion with Inputs Greater than the Power-Supply Voltage.

### RAIL-TO-RAIL OUTPUT

A class AB output stage with common-source transistors is used to achieve rail-to-rail output. This output stage is capable of driving 1k $\Omega$  loads connected to any point between  $V+$  and  $V-$ . For light resistive loads ( $> 100\text{k}\Omega$ ), the output voltage can swing to 100mV from the supply rail. With 1k $\Omega$  resistive loads, the output can swing to within 325mV from the supply rails while maintaining high open-loop gain (see the typical performance curve “Output Voltage Swing vs Output Current”).

## CAPACITIVE LOAD AND STABILITY

The OPA743 series op amps can drive up to 1000pF pure capacitive load. Increasing the gain enhances the amplifier's ability to drive greater capacitive loads (see the typical performance curve "Small Signal Overshoot vs Capacitive Load").

One method of improving capacitive load drive in the unity-gain configuration is to insert a  $10\Omega$  to  $20\Omega$  resistor inside the feedback loop, as shown in Figure 4. This reduces ringing with large capacitive loads while maintaining DC accuracy.

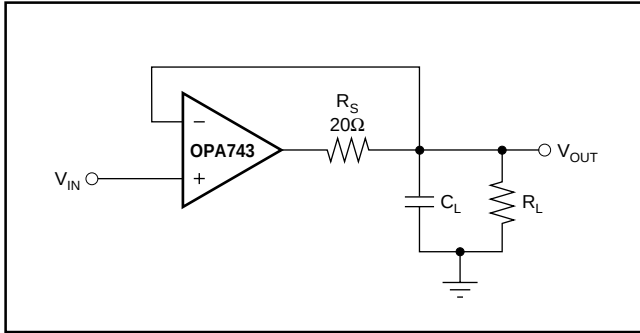


FIGURE 4. Series Resistor in Unity-Gain Buffer Configuration Improves Capacitive Load Drive.

## APPLICATION CIRCUITS

The OPA743 series op amps are optimized for driving medium-speed sampling data converters. The OPA743 op amps buffer the converter's input capacitance and resulting charge injection while providing signal gain.

Figure 5 shows the OPA743 in a dual supply buffered reference configuration for the DAC7644.

## REFERENCE BUFFER FOR LCD SOURCE DRIVERS

In modern high resolution TFT LCD displays, gamma correction must be performed to correct for nonlinearities in the glass transmission characteristics of the LCD panel. The typical LCD source driver for 64 Bits of Grayscale uses internal DAC to convert the 6-Bit data into analog voltages applied to the LCD. These DAC typically require external voltage references for proper operation. Normally these external reference voltages are generated using a simple resistive ladder, like the one shown in Figure 6.

Typical laptop or desktop LCD panels require 6 to 8 of the source driver circuits in parallel to drive all columns of the panel. Although the resistive load of one internal string DAC is only around  $10k\Omega$ , 6 to 8 in parallel represent a very substantial load. The power supply used for the LCD source drivers for laptops is typically in the order of 10V. To maximize the dynamic range of the DAC, rail-to-rail output performance is required for the upper and lower buffer. The OPA743's ability to operate on 12V supplies, to drive heavy resistive loads (as low as  $1k\Omega$ ), and to swing to within 325mV of the supply rails, makes it very well suited as a buffer for the reference voltage inputs of LCD source drivers.

During conversion, the DAC's internal switches create current glitches on the output of the reference buffer. The capacitor  $C_L$  (typically 100nF) functions as a charge reservoir that provides/absorbs most of the glitch energy. The series resistor  $R_S$  isolates the outputs of the OPA743 from the heavy capacitive load and helps to improve settling time.

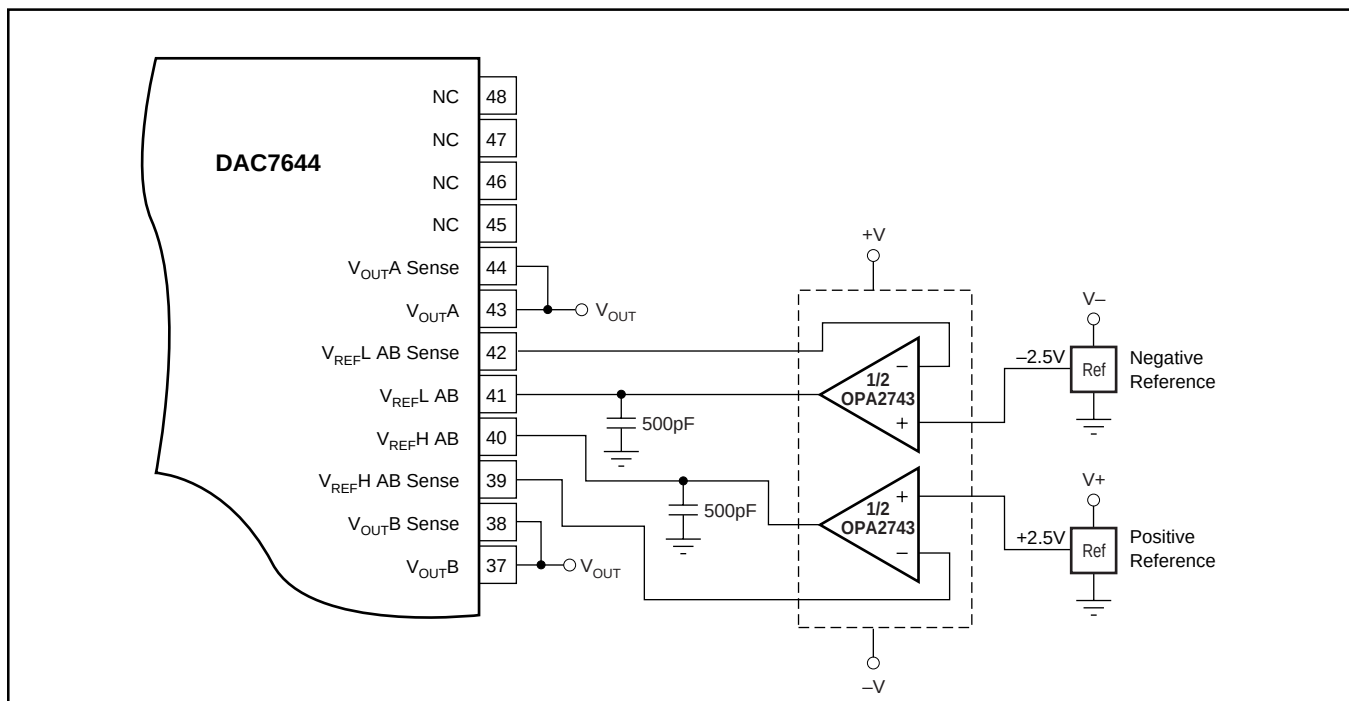


FIGURE 5. OPA743 as Dual Supply Configuration-Buffered References for the DAC7644.

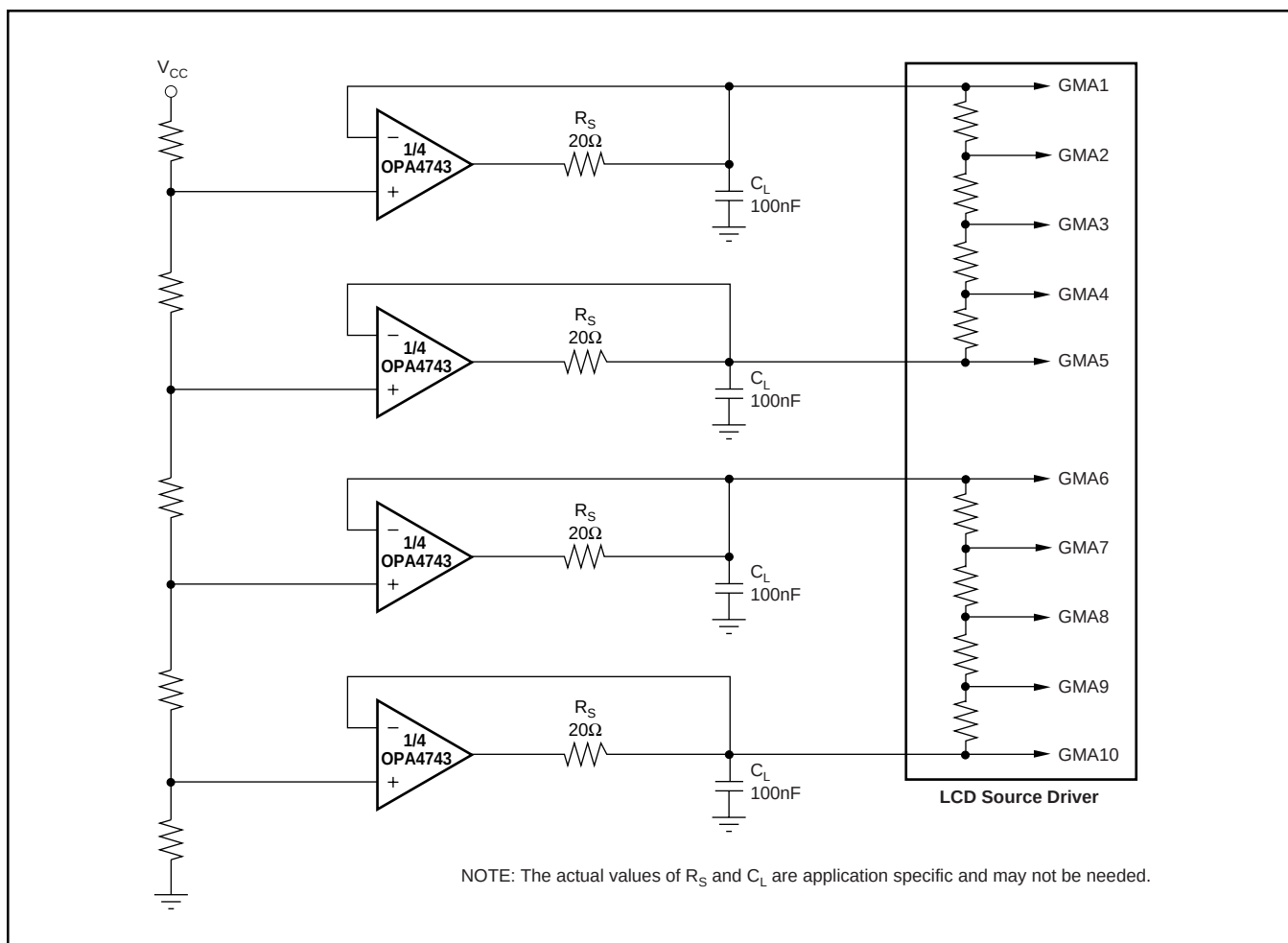


FIGURE 6. OPA743 Configured as a Reference Buffer for an LCD Display.

**PACKAGING INFORMATION**

| Orderable Device | Status<br>(1) | Package Type | Package<br>Drawing | Pins | Package<br>Qty | Eco Plan<br>(2)            | Lead/Ball Finish<br>(6) | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5) | Samples                 |
|------------------|---------------|--------------|--------------------|------|----------------|----------------------------|-------------------------|----------------------|--------------|-------------------------|-------------------------|
| OPA2743EA/250    | ACTIVE        | VSSOP        | DGK                | 8    | 250            | Green (RoHS<br>& no Sb/Br) | CU NIPDAUAG             | Level-2-260C-1 YEAR  | -40 to 85    | E43                     | <a href="#">Samples</a> |
| OPA2743EA/250G4  | ACTIVE        | VSSOP        | DGK                | 8    | 250            | Green (RoHS<br>& no Sb/Br) | CU NIPDAUAG             | Level-2-260C-1 YEAR  | -40 to 85    | E43                     | <a href="#">Samples</a> |
| OPA2743EA/2K5    | ACTIVE        | VSSOP        | DGK                | 8    | 2500           | Green (RoHS<br>& no Sb/Br) | CU NIPDAUAG             | Level-2-260C-1 YEAR  | -40 to 85    | E43                     | <a href="#">Samples</a> |
| OPA2743EA/2K5G4  | ACTIVE        | VSSOP        | DGK                | 8    | 2500           | Green (RoHS<br>& no Sb/Br) | CU NIPDAUAG             | Level-2-260C-1 YEAR  | -40 to 85    | E43                     | <a href="#">Samples</a> |
| OPA2743PA        | ACTIVE        | PDIP         | P                  | 8    | 50             | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | N / A for Pkg Type   | -40 to 85    | OPA2743PA               | <a href="#">Samples</a> |
| OPA2743PAG4      | ACTIVE        | PDIP         | P                  | 8    | 50             | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | N / A for Pkg Type   | -40 to 85    | OPA2743PA               | <a href="#">Samples</a> |
| OPA2743UA        | ACTIVE        | SOIC         | D                  | 8    | 75             | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-2-260C-1 YEAR  | -40 to 85    | OPA<br>2743UA           | <a href="#">Samples</a> |
| OPA2743UA/2K5    | ACTIVE        | SOIC         | D                  | 8    | 2500           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-2-260C-1 YEAR  | -40 to 85    | OPA<br>2743UA           | <a href="#">Samples</a> |
| OPA2743UA/2K5G4  | ACTIVE        | SOIC         | D                  | 8    | 2500           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-2-260C-1 YEAR  | -40 to 85    | OPA<br>2743UA           | <a href="#">Samples</a> |
| OPA2743UAG4      | ACTIVE        | SOIC         | D                  | 8    | 75             | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-2-260C-1 YEAR  | -40 to 85    | OPA<br>2743UA           | <a href="#">Samples</a> |
| OPA4743EA/250    | ACTIVE        | TSSOP        | PW                 | 14   | 250            | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-2-260C-1 YEAR  | -40 to 85    | OPA<br>4743EA           | <a href="#">Samples</a> |
| OPA4743EA/250G4  | ACTIVE        | TSSOP        | PW                 | 14   | 250            | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-2-260C-1 YEAR  | -40 to 85    | OPA<br>4743EA           | <a href="#">Samples</a> |
| OPA4743UA        | ACTIVE        | SOIC         | D                  | 14   | 50             | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | -40 to 85    | OPA4743UA               | <a href="#">Samples</a> |
| OPA4743UA/2K5    | ACTIVE        | SOIC         | D                  | 14   | 2500           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | -40 to 85    | OPA4743UA               | <a href="#">Samples</a> |
| OPA4743UA/2K5G4  | ACTIVE        | SOIC         | D                  | 14   | 2500           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | -40 to 85    | OPA4743UA               | <a href="#">Samples</a> |
| OPA4743UAG4      | ACTIVE        | SOIC         | D                  | 14   | 50             | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | -40 to 85    | OPA4743UA               | <a href="#">Samples</a> |
| OPA743NA/250     | ACTIVE        | SOT-23       | DBV                | 5    | 250            | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-2-260C-1 YEAR  | -40 to 85    | D43                     | <a href="#">Samples</a> |

| Orderable Device | Status<br>(1) | Package Type | Package Drawing | Pins | Package Qty | Eco Plan<br>(2)         | Lead/Ball Finish<br>(6) | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5) | Samples                 |
|------------------|---------------|--------------|-----------------|------|-------------|-------------------------|-------------------------|----------------------|--------------|-------------------------|-------------------------|
| OPA743NA/250G4   | ACTIVE        | SOT-23       | DBV             | 5    | 250         | Green (RoHS & no Sb/Br) | CU NIPDAU               | Level-2-260C-1 YEAR  | -40 to 85    | D43                     | <a href="#">Samples</a> |
| OPA743NA/3K      | ACTIVE        | SOT-23       | DBV             | 5    | 3000        | Green (RoHS & no Sb/Br) | CU NIPDAU               | Level-2-260C-1 YEAR  | -40 to 85    | D43                     | <a href="#">Samples</a> |
| OPA743NA/3KG4    | ACTIVE        | SOT-23       | DBV             | 5    | 3000        | Green (RoHS & no Sb/Br) | CU NIPDAU               | Level-2-260C-1 YEAR  | -40 to 85    | D43                     | <a href="#">Samples</a> |
| OPA743PA         | ACTIVE        | PDIP         | P               | 8    | 50          | Green (RoHS & no Sb/Br) | CU NIPDAU               | N / A for Pkg Type   | -40 to 85    | OPA743PA                | <a href="#">Samples</a> |
| OPA743UA         | ACTIVE        | SOIC         | D               | 8    | 75          | Green (RoHS & no Sb/Br) | CU NIPDAU               | Level-2-260C-1 YEAR  | -40 to 85    | OPA743UA                | <a href="#">Samples</a> |
| OPA743UAG4       | ACTIVE        | SOIC         | D               | 8    | 75          | Green (RoHS & no Sb/Br) | CU NIPDAU               | Level-2-260C-1 YEAR  | -40 to 85    | OPA743UA                | <a href="#">Samples</a> |

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**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

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**TBD:** The Pb-Free/Green conversion plan has not been defined.

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**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

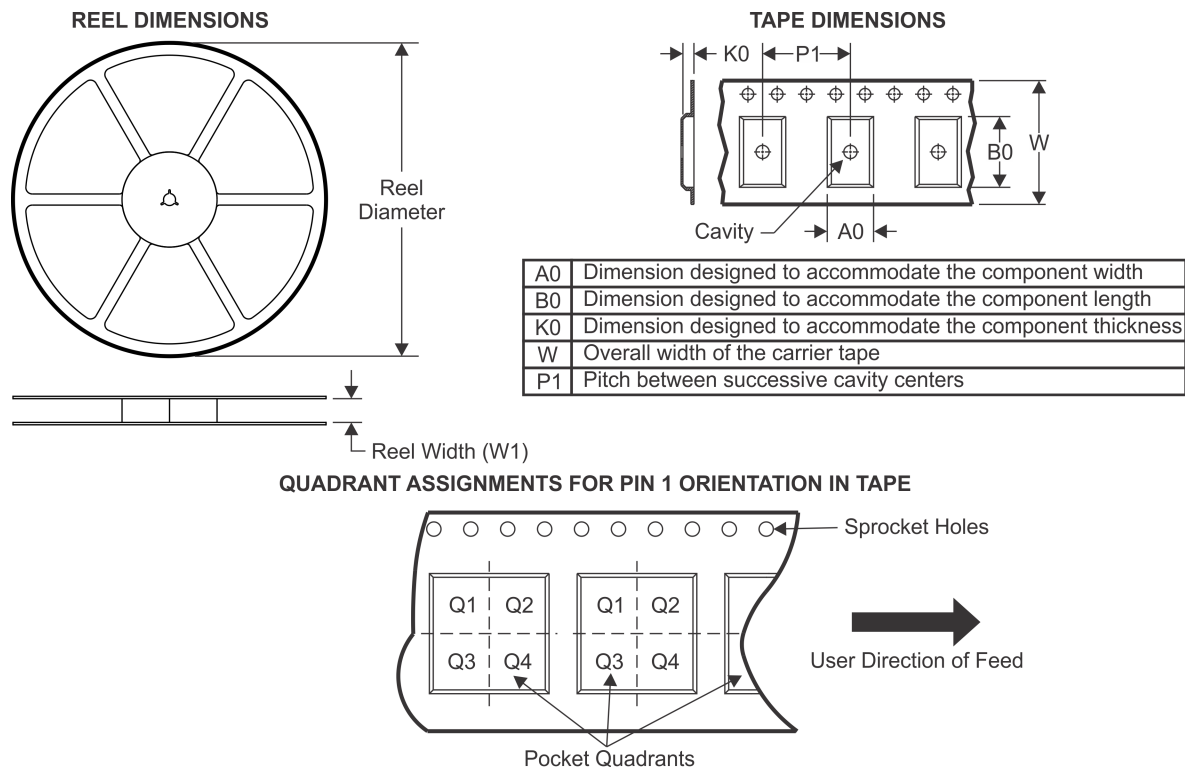
(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "-" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

<sup>(6)</sup> Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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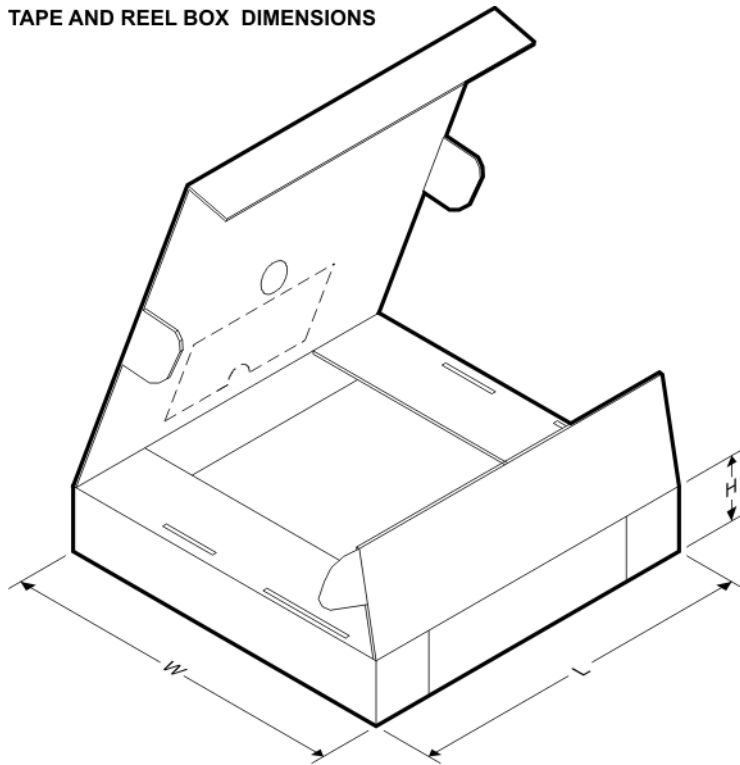
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\*All dimensions are nominal

| Device        | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|---------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| OPA2743EA/250 | VSSOP        | DGK             | 8    | 250  | 180.0              | 12.4               | 5.3     | 3.4     | 1.4     | 8.0     | 12.0   | Q1            |
| OPA2743EA/2K5 | VSSOP        | DGK             | 8    | 2500 | 330.0              | 12.4               | 5.3     | 3.4     | 1.4     | 8.0     | 12.0   | Q1            |
| OPA2743UA/2K5 | SOIC         | D               | 8    | 2500 | 330.0              | 12.4               | 6.4     | 5.2     | 2.1     | 8.0     | 12.0   | Q1            |
| OPA4743EA/250 | TSSOP        | PW              | 14   | 250  | 180.0              | 12.4               | 6.9     | 5.6     | 1.6     | 8.0     | 12.0   | Q1            |
| OPA4743UA/2K5 | SOIC         | D               | 14   | 2500 | 330.0              | 16.4               | 6.5     | 9.0     | 2.1     | 8.0     | 16.0   | Q1            |
| OPA743NA/250  | SOT-23       | DBV             | 5    | 250  | 178.0              | 9.0                | 3.3     | 3.2     | 1.4     | 4.0     | 8.0    | Q3            |
| OPA743NA/3K   | SOT-23       | DBV             | 5    | 3000 | 178.0              | 9.0                | 3.3     | 3.2     | 1.4     | 4.0     | 8.0    | Q3            |

## TAPE AND REEL BOX DIMENSIONS



\*All dimensions are nominal

| Device        | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|---------------|--------------|-----------------|------|------|-------------|------------|-------------|
| OPA2743EA/250 | VSSOP        | DGK             | 8    | 250  | 210.0       | 185.0      | 35.0        |
| OPA2743EA/2K5 | VSSOP        | DGK             | 8    | 2500 | 367.0       | 367.0      | 35.0        |
| OPA2743UA/2K5 | SOIC         | D               | 8    | 2500 | 367.0       | 367.0      | 35.0        |
| OPA4743EA/250 | TSSOP        | PW              | 14   | 250  | 210.0       | 185.0      | 35.0        |
| OPA4743UA/2K5 | SOIC         | D               | 14   | 2500 | 367.0       | 367.0      | 38.0        |
| OPA743NA/250  | SOT-23       | DBV             | 5    | 250  | 180.0       | 180.0      | 18.0        |
| OPA743NA/3K   | SOT-23       | DBV             | 5    | 3000 | 180.0       | 180.0      | 18.0        |



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