

M5M82C59AFP,-2

T-52-33-13

DESCRIPTION

The M5M82C59AFP,-2 is a programmable LSI for interrupt control. It is fabricated using silicon-gate CMOS technology and is designed to be used easily in connection with an 8085A, 8086 or 8088.

M5M82C59AFP,-2's specifications are compatible with M5M82C59AP,-2. Only package outline is different. M5M82C59AFP,-2 is mounted in the SOP (Small Outline Package).

Refer to M5M82C59AP,-2 for detail information

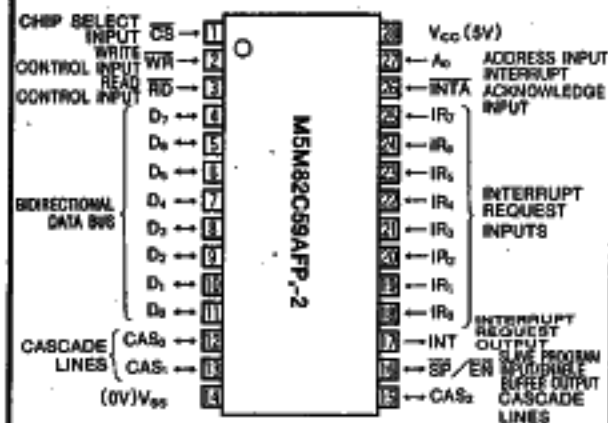
FEATURES

- Single 5V supply voltage
- TTL compatible
- Having internal anti-noise circuit on $IR_0 \sim IR_7$ pins
- CALL instruction to the CPU is generated automatically
- Priority, interrupt mask and vectored address for each interrupt request input are programmable
- Up to 64 levels of interrupt requests can be controlled by cascading with M5M82C59AFP,-2
- Polling functions
- Packaged in flat small outline package

APPLICATION

The M5M82C59AFP,-2 can be used as an interrupt controller for CPUs 8085A, 8086 and 8088

PIN CONFIGURATION (TOP VIEW)



Outline 28P2W

BLOCK DIAGRAM

