

N-CHANNEL MOS FIELD EFFECT TRANSISTOR  
FOR SWITCHING

## DESCRIPTION

The 2SK3105 is a switching device which can be driven directly by a 4 V power source.

The 2SK3105 features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

## FEATURES

- Can be driven by a 4 V power source
- Low on-state resistance

$R_{DS(on)1} = 95 \text{ m}\Omega \text{ MAX. } (V_{GS} = 10 \text{ V}, I_D = 1.5 \text{ A})$   
 $R_{DS(on)2} = 135 \text{ m}\Omega \text{ MAX. } (V_{GS} = 4.5 \text{ V}, I_D = 1.5 \text{ A})$   
 $R_{DS(on)3} = 150 \text{ m}\Omega \text{ MAX. } (V_{GS} = 4.0 \text{ V}, I_D = 1.5 \text{ A})$

## ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK3105	3-pin Mini Mold (Thin Type)

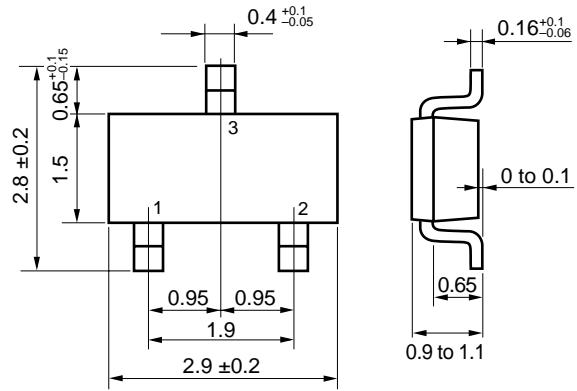
ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ )

Drain to Source Voltage	$V_{DSS}$	30	V
Gate to Source Voltage	$V_{GSS}$	$\pm 20$	V
Drain Current (DC)	$I_D(\text{DC})$	$\pm 2.5$	A
Drain Current (pulse) <sup>Note1</sup>	$I_D(\text{pulse})$	$\pm 10$	A
Total Power Dissipation	$P_{T1}$	0.2	W
Total Power Dissipation <sup>Note2</sup>	$P_{T2}$	1.25	W
Channel Temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

**Notes 1.**  $PW \leq 10 \mu\text{s}$ , Duty Cycle  $\leq 1\%$   
**2.** Mounted on FR4 Board,  $t \leq 5 \text{ sec.}$

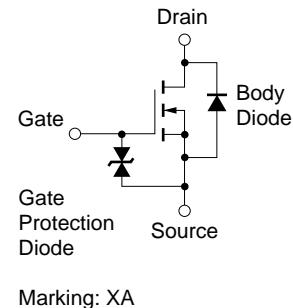
**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

## PACKAGE DRAWING (Unit : mm)



1 : Gate  
2 : Source  
3 : Drain

## EQUIVALENT CIRCUIT

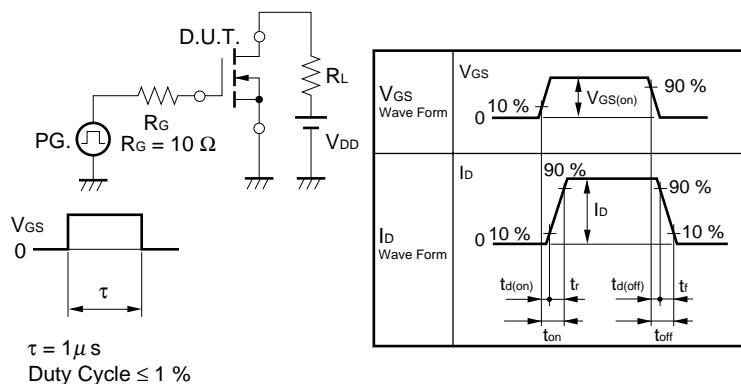


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 Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

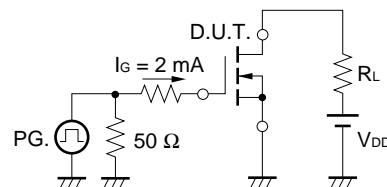
## ELECTRICAL CHARACTERISTICS (TA = 25 °C)

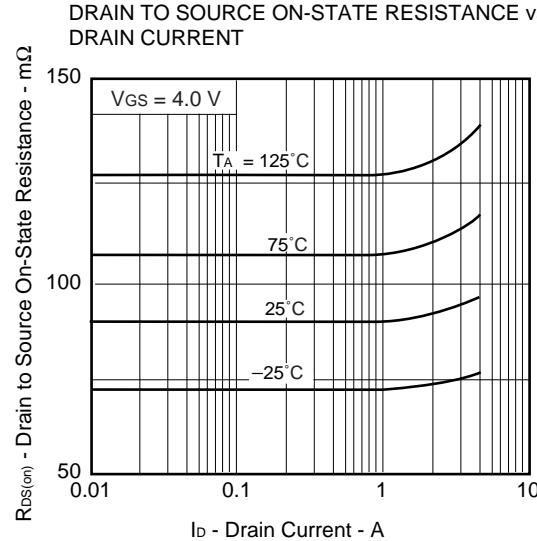
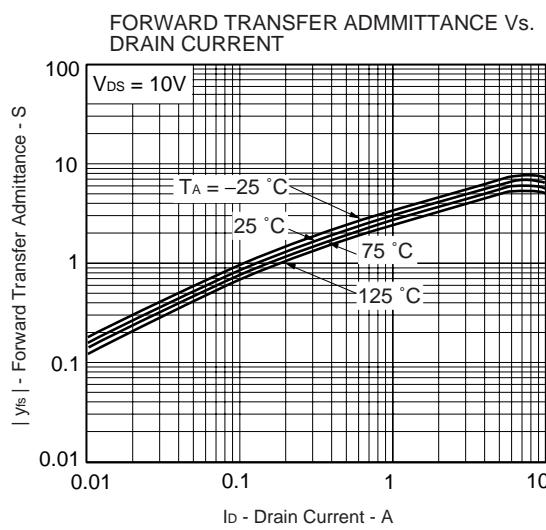
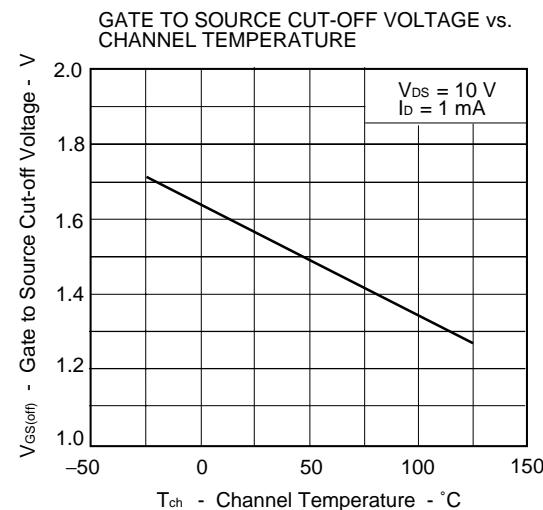
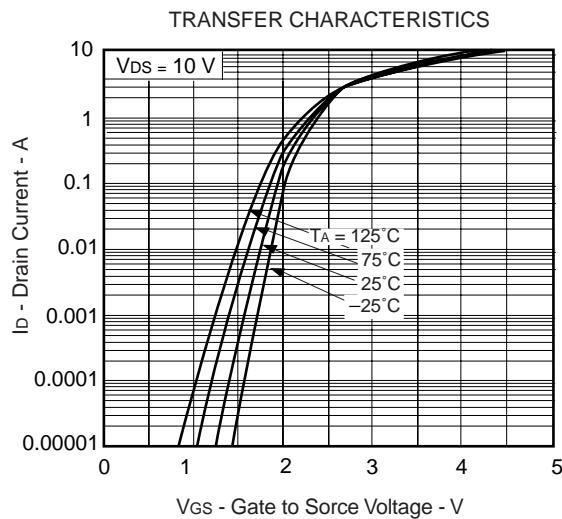
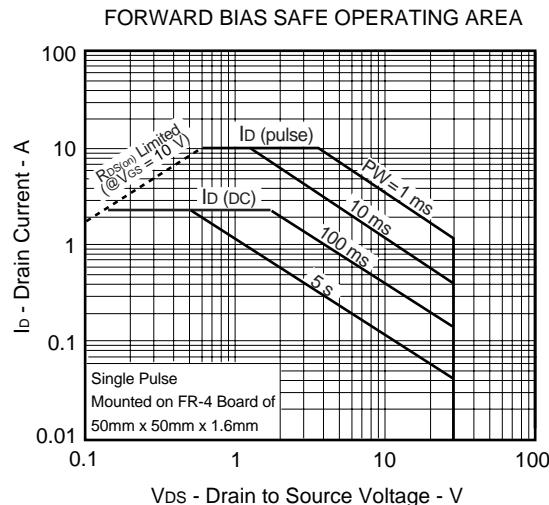
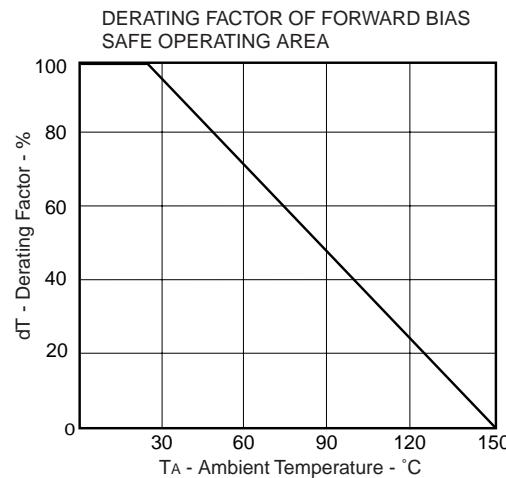
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain Cut-off Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			-10	μA
Gate Leakage Current	I <sub>GS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V			±10	μA
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.0	1.6	2.5	V
Forward Transfer Admittance	y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1.5 A	1	3.5		S
Drain to Source On-state Resistance	R <sub>DSON1</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1.5 A		56	95	mΩ
	R <sub>DSON2</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1.5 A		82	135	mΩ
	R <sub>DSON3</sub>	V <sub>GS</sub> = 4.0 V, I <sub>D</sub> = 1.5 A		91	150	mΩ
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 10 V V <sub>GS</sub> = 0 V f = 1 MHz		211		pF
Output Capacitance	C <sub>oss</sub>			95		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			42		pF
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 10 V I <sub>D</sub> = 1.0 A V <sub>GS(on)</sub> = 10 V R <sub>G</sub> = 10 Ω		12		ns
Rise Time	t <sub>r</sub>			44		ns
Turn-off Delay Time	t <sub>d(off)</sub>			28		ns
Fall Time	t <sub>f</sub>			15		ns
Total Gate Charge	Q <sub>G</sub>	V <sub>DS</sub> = 10 V I <sub>D</sub> = 2.5 A V <sub>GS</sub> = 4.0 V		2.1		nC
Gate to Source Charge	Q <sub>GS</sub>			0.61		nC
Gate to Drain Charge	Q <sub>GD</sub>			0.84		nC
Diode Forward Voltage	V <sub>F(S-D)</sub>	I <sub>F</sub> = 2.5 A, V <sub>GS</sub> = 0 V		0.81		V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 2.5 A, V <sub>GS</sub> = 0 V di/dt = 90 A/μs		15		ns
Reverse Recovery Charge	Q <sub>rr</sub>			3.7		nC

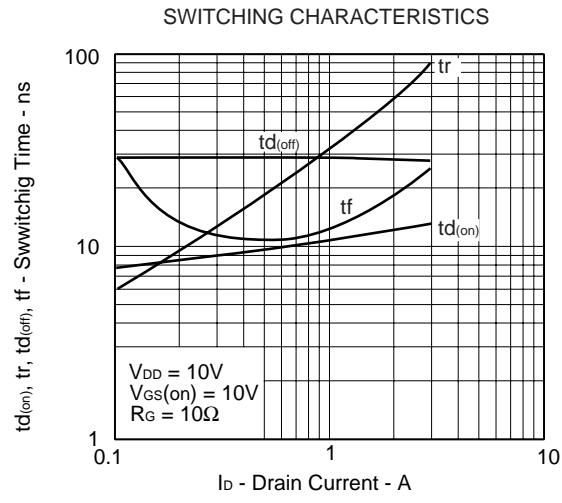
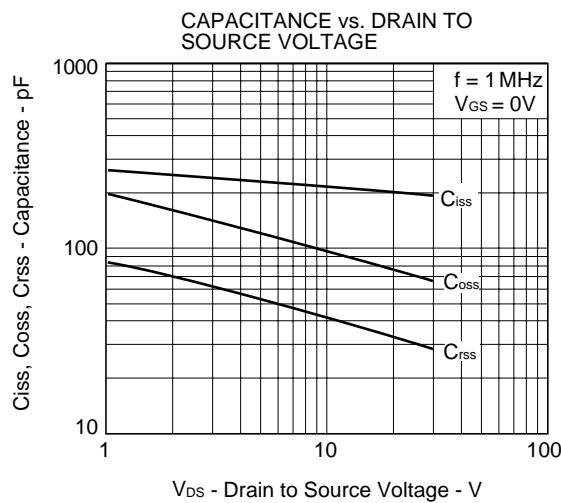
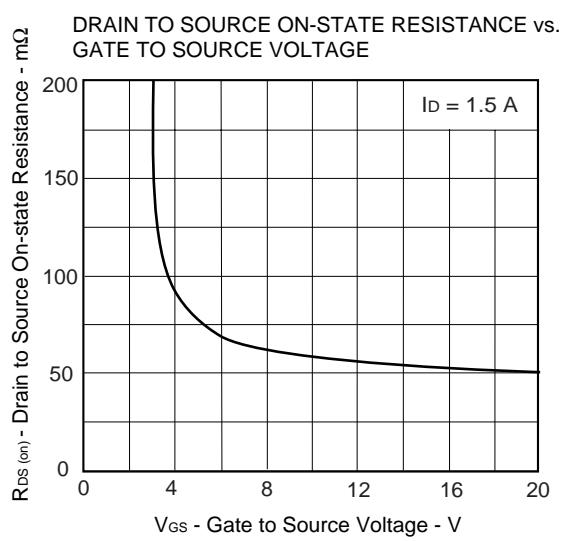
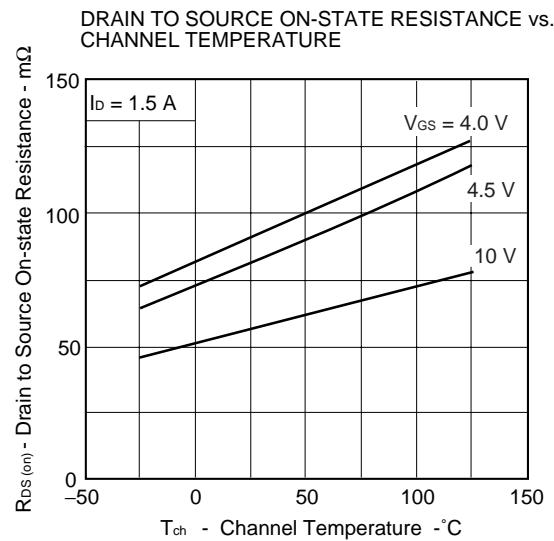
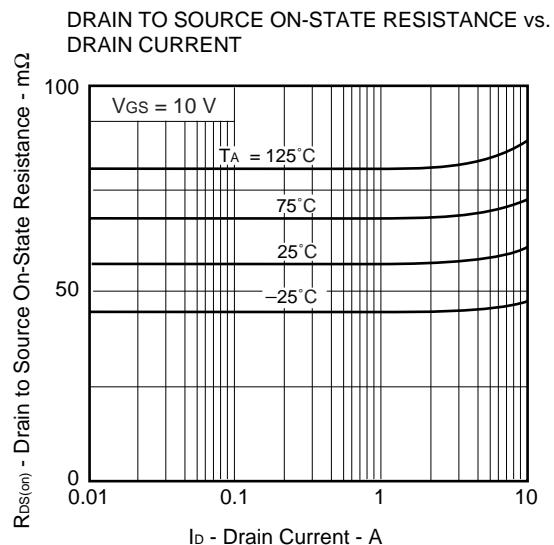
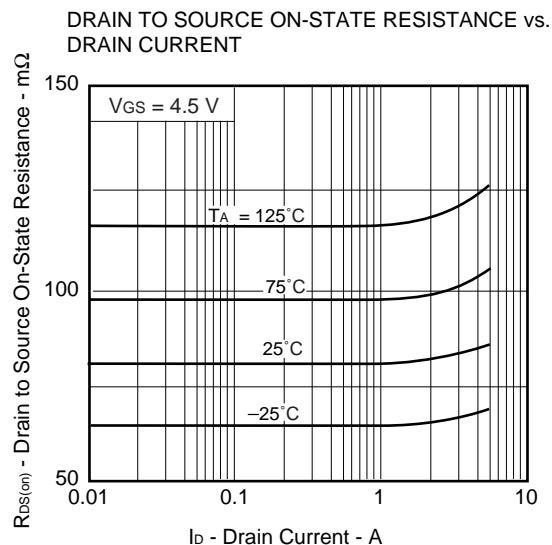
## TEST CIRCUIT 1 SWITCHING TIME



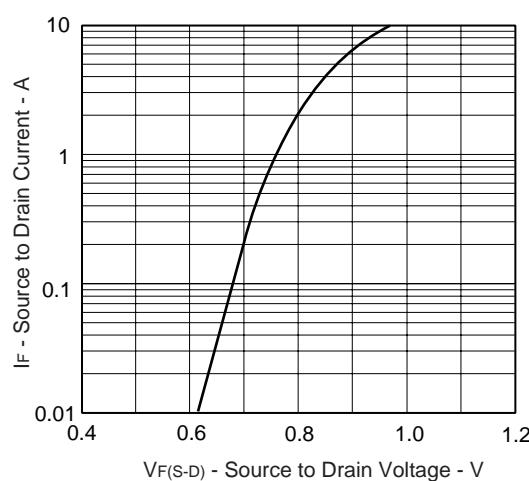
## TEST CIRCUIT 2 GATE CHARGE



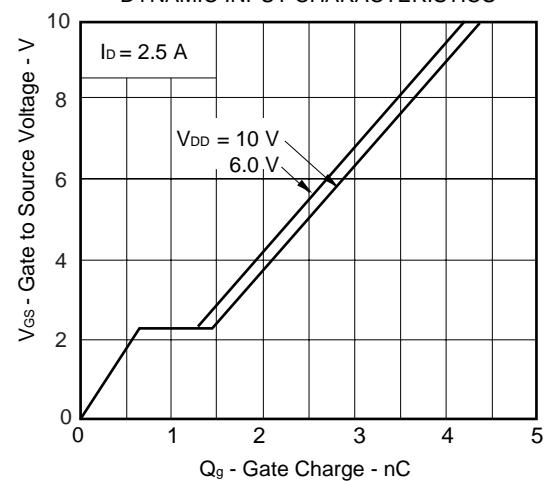
TYPICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ )



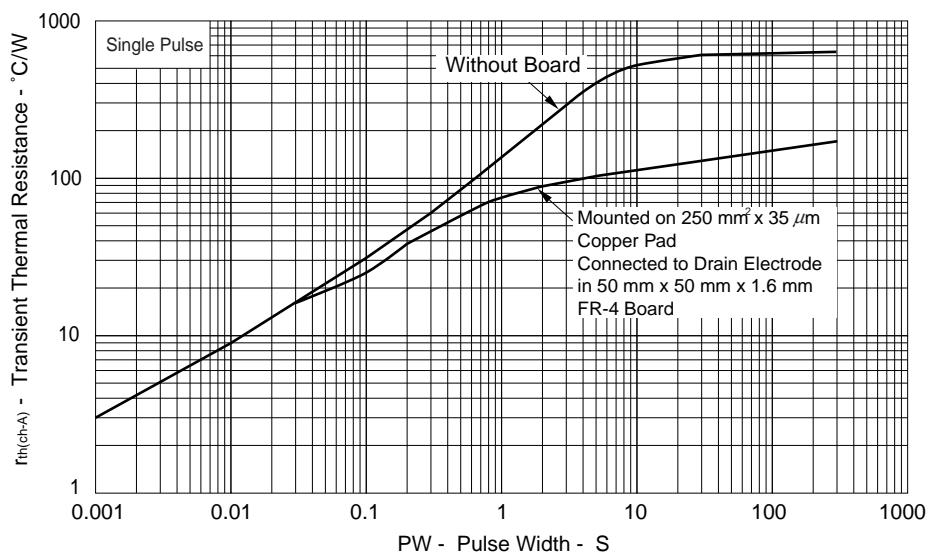
SOURCE TO DRAIN DIODE FORWARD VOLTAGE



DYNAMIC INPUT CHARACTERISTICS



TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



**[MEMO]**

**[MEMO]**

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