

# Switching (200V, 3A)

## 2SK2887

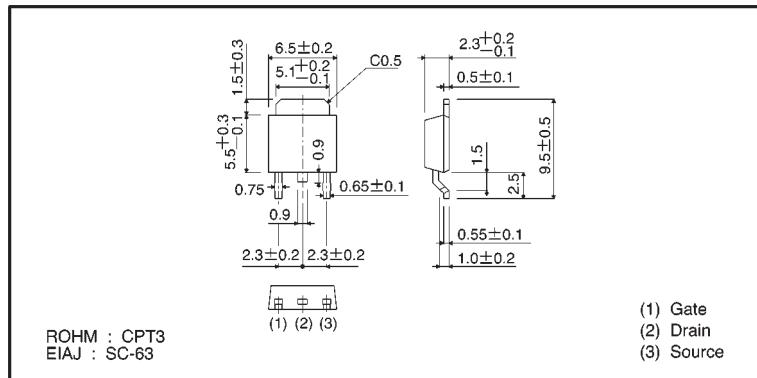
### ● Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Wide SOA (safe operating area).
- 4) Gate-source voltage ( $V_{GSS}$ ) guaranteed to be  $\pm 30V$ .
- 5) Easily designed drive circuits.
- 6) Easy to parallel.

### ● Structure

Silicon N-channel  
MOSFET

### ● External dimensions (Units: mm)



### ● Absolute maximum ratings ( $T_a = 25^\circ C$ )

Parameter	Symbol	Limits	Unit
Drain-source voltage	$V_{DSS}$	200	V
Gate-source voltage	$V_{GSS}$	$\pm 30$	V
Drain current	Continuous	$I_D$	A
	Pulsed	$I_{DP}^*$	A
Reverse drain current	Continuous	$I_{DR}$	A
	Pulsed	$I_{DRP}^*$	A
Total power dissipation ( $T_c=25^\circ C$ )	$P_D$	20	W
Channel temperature	$T_{ch}$	150	$^\circ C$
Storage temperature	$T_{stg}$	$-55 \sim +150$	$^\circ C$

\*  $P_w \leq 10 \mu s$ , Duty cycle  $\leq 1\%$

### ● Packaging specifications

Type	Package	Taping
	Code	TL
	Basic ordering unit (pieces)	2500
2SK2887		○

● Electrical characteristics ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Gate-source leakage	$I_{GSS}$	—	—	$\pm 100$	nA	$V_{GS} = \pm 30\text{V}$ , $V_{DS} = 0\text{V}$
Drain-source breakdown voltage	$V_{(BR)DSS}$	200	—	—	V	$I_D = 1\text{mA}$ , $V_{GS} = 0\text{V}$
Zero gate voltage drain current	$I_{DSS}$	—	—	100	$\mu\text{A}$	$V_{DS} = 200\text{V}$ , $V_{GS} = 0\text{V}$
Gate threshold voltage	$V_{GS(\text{th})}$	2.0	—	4.0	V	$V_{DS} = 10\text{V}$ , $I_D = 1\text{mA}$
Static drain-source on-state resistance	$R_{DS(on)}$	—	0.7	0.9	$\Omega$	$I_D = 1.5\text{A}$ , $V_{GS} = 10\text{V}$
Forward transfer admittance	$ Y_{fs} $	0.6	1.5	—	S	$I_D = 1.5\text{A}$ , $V_{DS} = 10\text{V}$
Input capacitance	$C_{iss}$	—	230	—	pF	$V_{DS} = 10\text{V}$
Output capacitance	$C_{oss}$	—	100	—	pF	$V_{GS} = 0\text{V}$
Reverse transfer capacitance	$C_{rss}$	—	35	—	pF	$f = 1\text{MHz}$
Turn-on delay time	$t_{d(on)}$	—	10	—	ns	$I_D = 1.5\text{A}$ , $V_{DD} = 100\text{V}$
Rise time	$t_r$	—	12	—	ns	$V_{GS} = 10\text{V}$
Turn-off delay time	$t_{d(off)}$	—	26	—	ns	$R_L = 68\Omega$
Fall time	$t_f$	—	34	—	ns	$R_G = 10\Omega$
Reverse recovery time	$t_{rr}$	—	96	—	ns	$I_{DR} = 3\text{A}$ , $V_{GS} = 0\text{V}$
Reverse recovery charge	$Q_{rr}$	—	0.59	—	$\mu\text{C}$	$di/dt = 100\text{A}/\mu\text{s}$

## ● Electrical characteristic curves

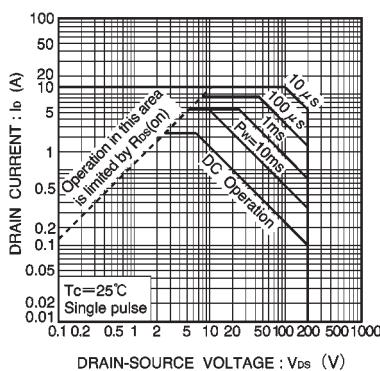


Fig.1 Maximum safe operating area

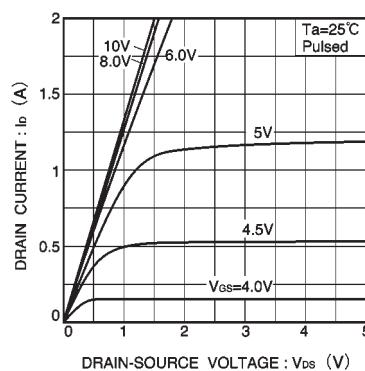


Fig.2 Typical output characteristics

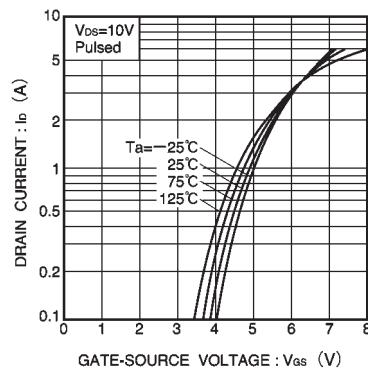


Fig.3 Typical transfer characteristics

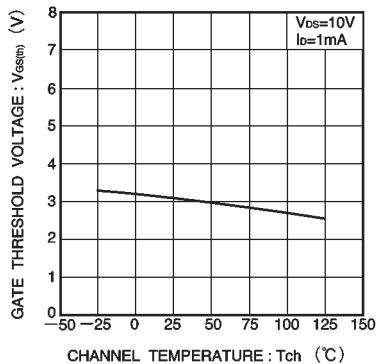


Fig.4 Gate threshold voltage vs. channel temperature

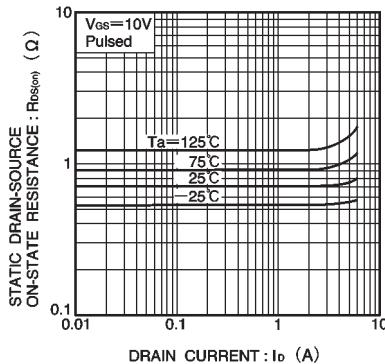


Fig.5 Static drain-source on-state resistance vs. drain current

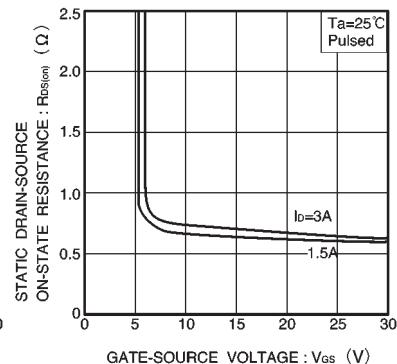


Fig.6 Static drain-source on-state resistance vs. gate-source voltage

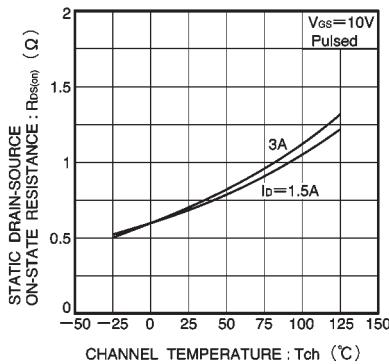


Fig.7 Static drain-source on-state resistance vs. channel temperature

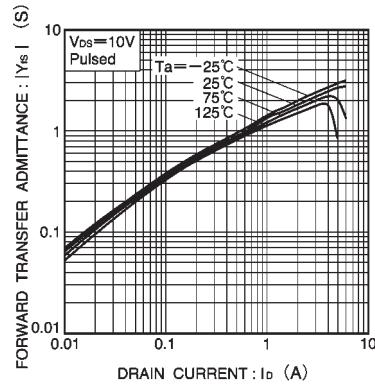


Fig.8 Forward transfer admittance vs. drain current

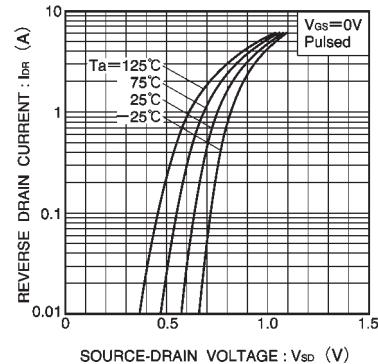


Fig.9 Reverse drain current vs. source-drain voltage (I)

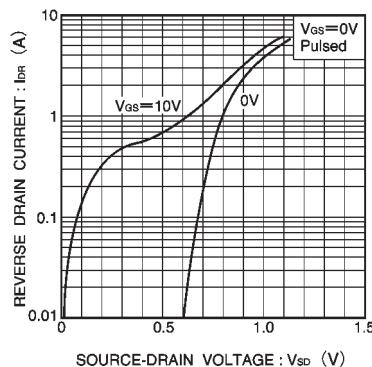


Fig.10 Reverse drain current vs. source-drain voltage (II)

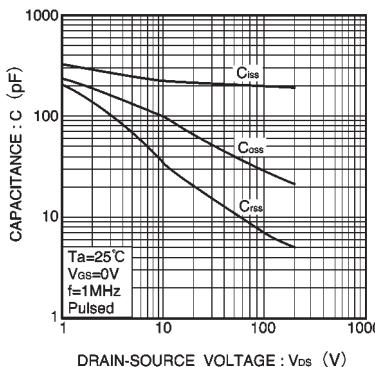


Fig.11 Typical capacitance vs. drain-source voltage

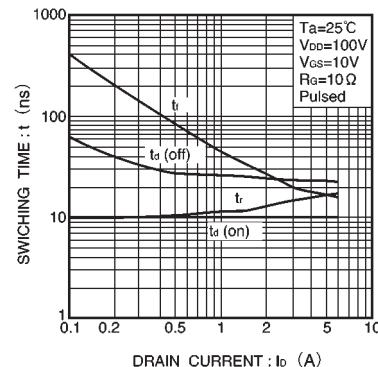


Fig.12 Switching characteristics (See Figures 16 and 17 for the measurement circuit and resultant waveforms)

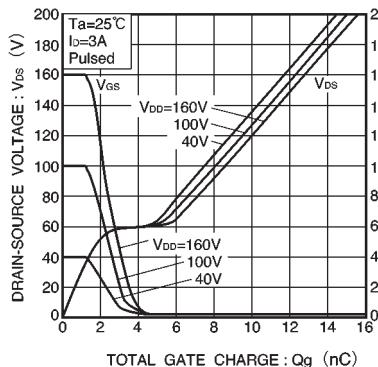


Fig.13 Dynamic input characteristics  
(See Figure 18 for  
measurement circuit)

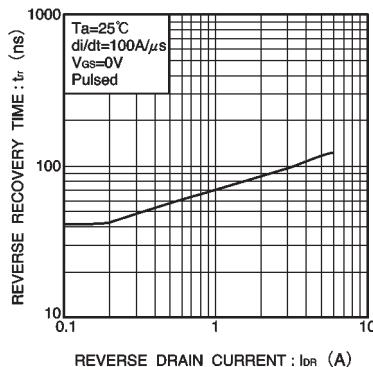


Fig.14 Reverse recovery time  
vs. reverse drain current

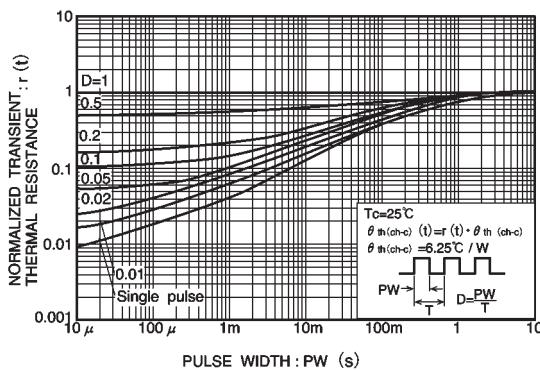


Fig.15 Normalized transient thermal  
resistance vs. pulse width

● Switching characteristics  
measurement circuit

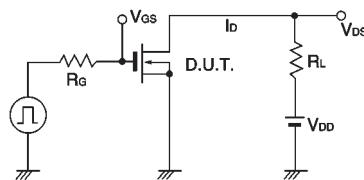


Fig.16 Switching time measurement circuit

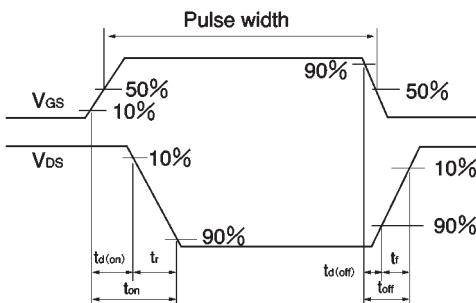


Fig.17 Switching time waveforms

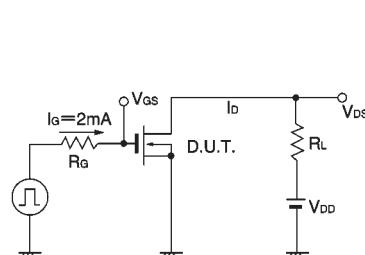


Fig.18 Gate charge time measurement circuit

## Appendix

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