

SGL160N60UFD

Ultrafast IGBT

General Description

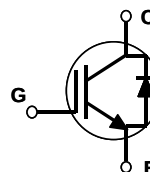
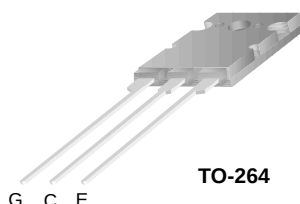
Fairchild's UFD series of Insulated Gate Bipolar Transistors (IGBTs) provides low conduction and switching losses. The UFD series is designed for applications such as motor control and general inverters where high speed switching is a required feature.

Features

- High speed switching
- Low saturation voltage : $V_{CE(sat)} = 2.1 \text{ V @ } I_C = 80 \text{ A}$
- High input impedance
- CO-PAK, IGBT with FRD: $t_{rr} = 75 \text{ ns (typ.)}$

Applications

AC & DC motor controls, general purpose inverters, robotics, servo controls, and power supplies.



Absolute Maximum Ratings

$T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Description	SGL160N60UFD	Units
V_{CES}	Collector-Emitter Voltage	600	V
V_{GES}	Gate-Emitter Voltage	± 20	V
I_C	Collector Current @ $T_C = 25^\circ\text{C}$	160	A
	Collector Current @ $T_C = 100^\circ\text{C}$	80	A
$I_{CM(1)}$	Pulsed Collector Current	300	A
I_F	Diode Continuous Forward Current @ $T_C = 100^\circ\text{C}$	25	A
I_{FM}	Diode Maximum Forward Current	280	A
P_D	Maximum Power Dissipation @ $T_C = 25^\circ\text{C}$	250	W
	Maximum Power Dissipation @ $T_C = 100^\circ\text{C}$	100	W
T_J	Operating Junction Temperature	-55 to +150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55 to +150	$^\circ\text{C}$
T_L	Maximum Lead Temp. for Soldering Purposes, 1/8" from Case for 5 Seconds	300	$^\circ\text{C}$

Notes :

(1) Repetitive rating : Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JC}(\text{IGBT})$	Thermal Resistance, Junction-to-Case	--	0.5	$^\circ\text{C/W}$
$R_{\theta JC}(\text{DIODE})$	Thermal Resistance, Junction-to-Case	--	0.83	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	--	25	$^\circ\text{C/W}$

Electrical Characteristics of the IGBT $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
Off Characteristics						
BV_{CES}	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_C = 250\mu A$	600	--	--	V
$\Delta BV_{CES} / \Delta T_J$	Temperature Coefficient of Breakdown Voltage	$V_{GE} = 0V, I_C = 1mA$	--	0.6	--	V/ $^\circ\text{C}$
I_{CES}	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$	--	--	250	μA
I_{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$	--	--	± 100	nA

On Characteristics

$V_{GE(th)}$	G-E Threshold Voltage	$I_C = 80mA, V_{CE} = V_{GE}$	3.5	4.5	6.5	V
$V_{CE(sat)}$	Collector to Emitter Saturation Voltage	$I_C = 80A, V_{GE} = 15V$	--	2.1	2.6	V
		$I_C = 160A, V_{GE} = 15V$	--	2.6	--	V

Dynamic Characteristics

C_{ies}	Input Capacitance	$V_{CE} = 30V, V_{GE} = 0V,$ $f = 1MHz$	--	5000	--	pF
C_{oes}	Output Capacitance		--	600	--	pF
C_{res}	Reverse Transfer Capacitance		--	200	--	pF

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 300V, I_C = 80A,$ $R_G = 3.9\Omega, V_{GE} = 15V$ Inductive Load, $T_C = 25^\circ\text{C}$	--	40	--	ns
t_r	Rise Time		--	101	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	90	130	ns
t_f	Fall Time		--	75	150	ns
E_{on}	Turn-On Switching Loss		--	2500	--	μJ
E_{off}	Turn-Off Switching Loss	$V_{CC} = 300V, I_C = 80A,$ $R_G = 3.9\Omega, V_{GE} = 15V$ Inductive Load, $T_C = 125^\circ\text{C}$	--	1760	--	μJ
E_{ts}	Total Switching Loss		--	4260	5000	μJ
$t_{d(on)}$	Turn-On Delay Time		--	45	--	ns
t_r	Rise Time		--	105	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	140	200	ns
t_f	Fall Time	$V_{CE} = 300V, I_C = 80A,$ $V_{GE} = 15V$	--	122	250	ns
E_{on}	Turn-On Switching Loss		--	2785	--	μJ
E_{off}	Turn-Off Switching Loss		--	3100	--	μJ
E_{ts}	Total Switching Loss		--	5885	--	μJ
Q_g	Total Gate Charge		--	345	520	nC
Q_{ge}	Gate-Emitter Charge	Measured 5mm from PKG	--	60	100	nC
Q_{gc}	Gate-Collector Charge		--	95	150	nC
L_e	Internal Emitter Inductance		--	18	--	nH

Electrical Characteristics of DIODE $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Units
V _{FM}	Diode Forward Voltage	I _F = 25A	T _C = 25°C	--	1.4	1.7	V
			T _C = 100°C	--	1.3	--	
t _{rr}	Diode Reverse Recovery Time	I _F = 25A, di/ dt = 200 A/us	T _C = 25°C	--	50	95	ns
			T _C = 100°C	--	105	--	
I _{rr}	Diode Peak Reverse Recovery Current		T _C = 25°C	--	4.5	10	A
			T _C = 100°C	--	8.5	--	
Q _{rr}	Diode Reverse Recovery Charge		T _C = 25°C	--	112	375	nC
			T _C = 100°C	--	420	--	

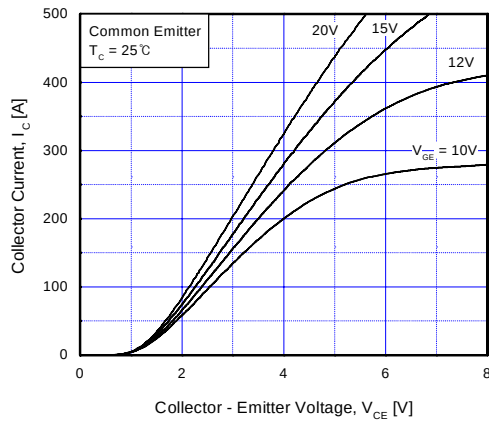


Fig 1. Typical Output Characteristics

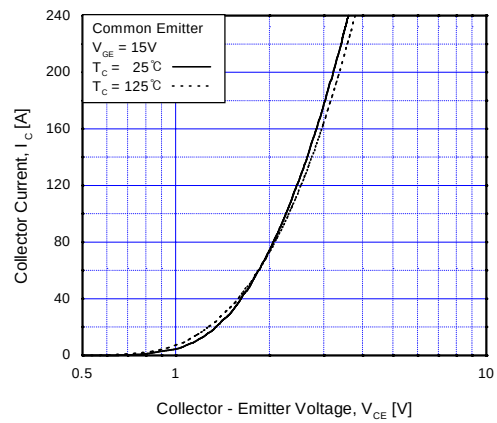


Fig 2. Typical Saturation Voltage Characteristics

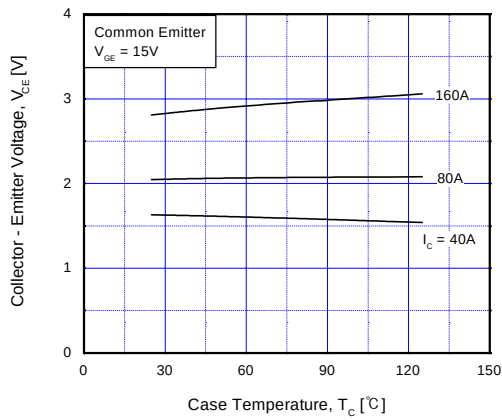


Fig 3. Saturation Voltage vs. Case Temperature at Variant Current Level

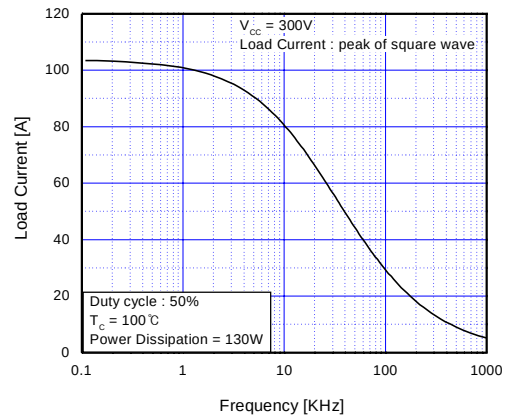


Fig 4. Load Current vs. Frequency

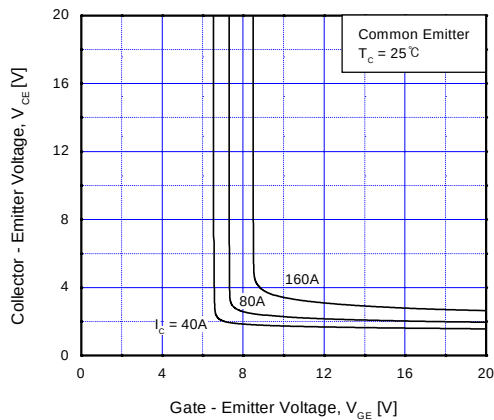


Fig 6. Saturation Voltage vs. V_{GE}

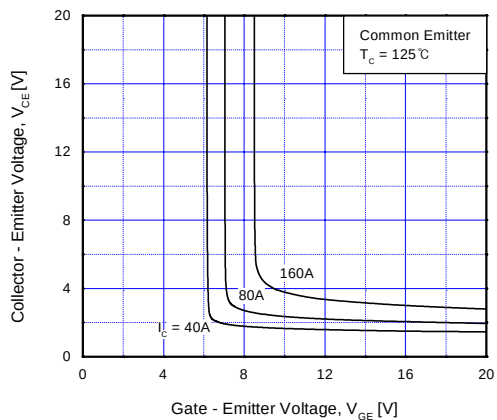


Fig 7. Saturation Voltage vs. V_{GE}

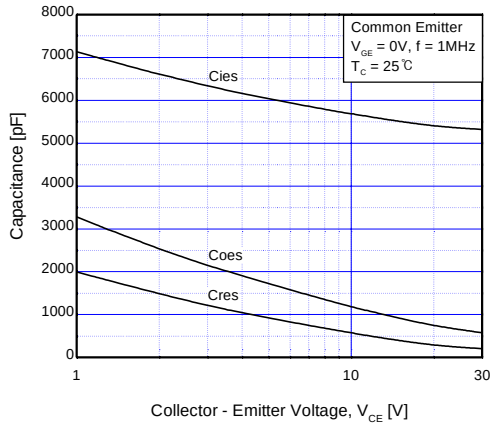


Fig 7. Capacitance Characteristics

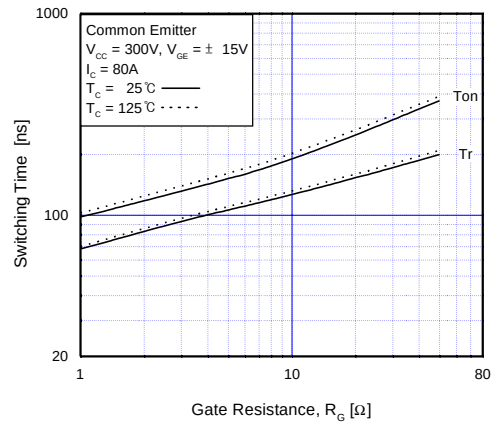


Fig 8. Turn-On Characteristics vs. Gate Resistance

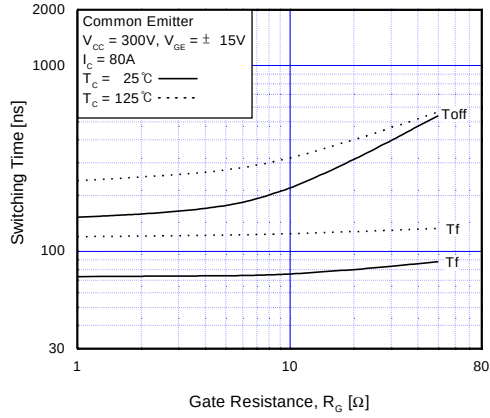


Fig 9. Turn-Off Characteristics vs. Gate Resistance

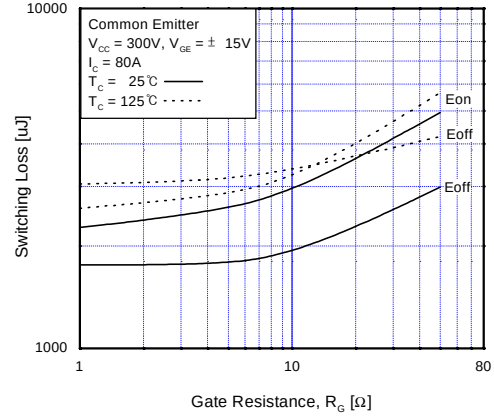


Fig 10. Switching Loss vs. Gate Resistance

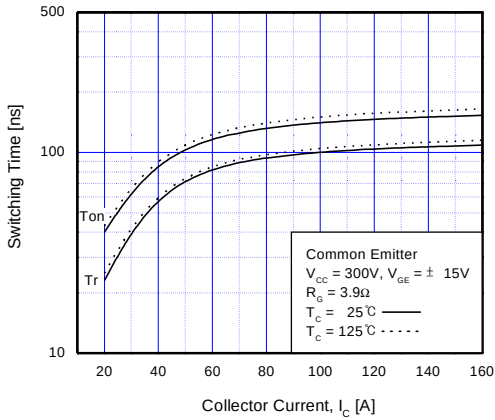


Fig 11. Turn-On Characteristics vs. Collector Current

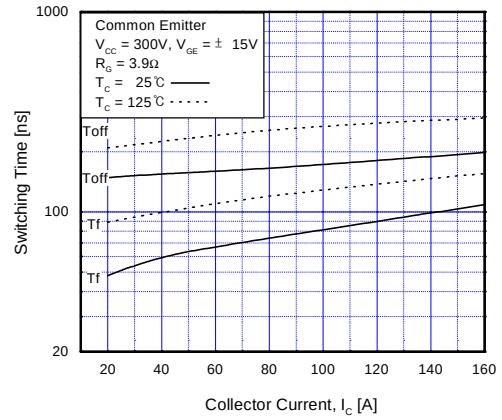


Fig 12. Turn-Off Characteristics vs. Collector Current

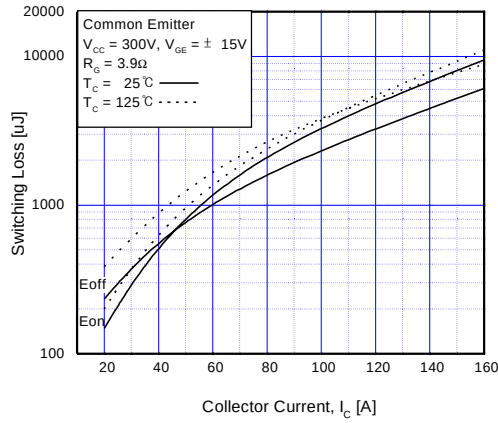


Fig 13. Switching Loss vs. Collector Current

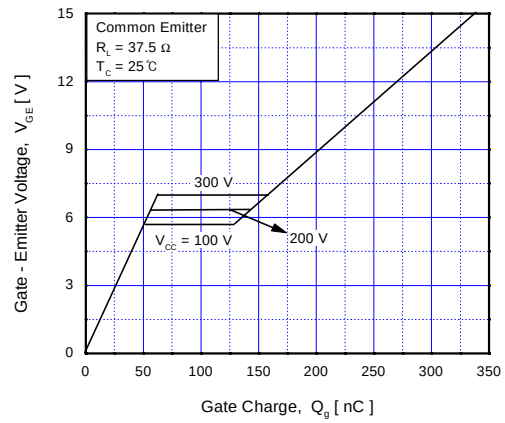


Fig 14. Gate Charge Characteristics

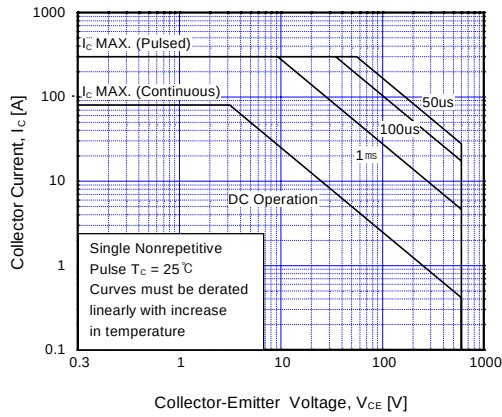


Fig 15. SOA Characteristic

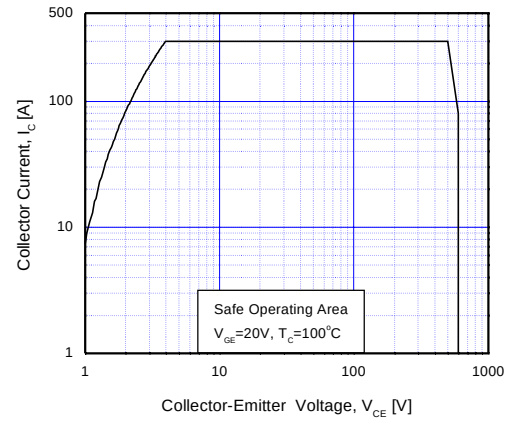


Fig 16. Turn-Off SOA Characteristics

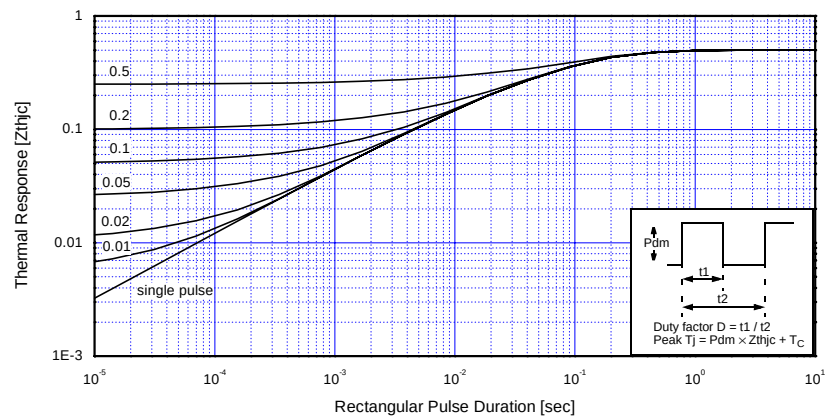


Fig 17. Transient Thermal Impedance of IGBT

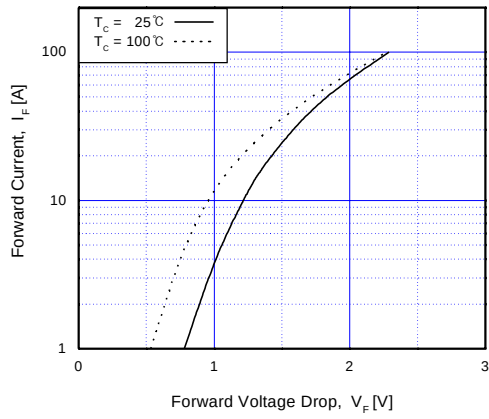


Fig 18. Forward Characteristics

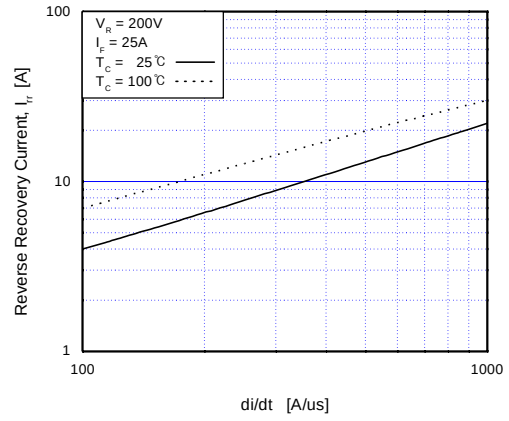


Fig 19. Reverse Recovery Current

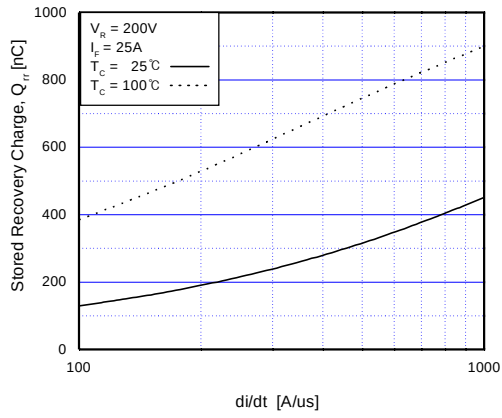


Fig 20. Stored Charge

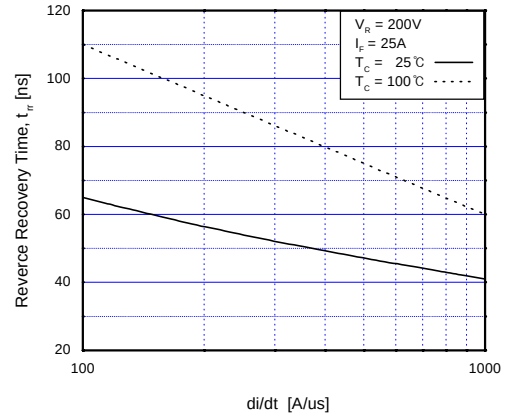


Fig 21. Reverse Recovery Time

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