

Features

- Low On-Resistance
- Low Gate Threshold Voltage $V_{GS(th)} < 1V$
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Complementary Pair MOSFET
- Ultra-Small Surface Mount Package
- **Lead Free/RoHS Compliant (Note 2)**
- **ESD Protected Gate to 2.5kV HBM**
- **"Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: SOT-563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 7
- Ordering Information: See Page 7
- Weight: 0.006 grams (approximate)



Maximum Ratings N-CHANNEL – Q_1 @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V_{DSS}	20	V
Gate-Source Voltage	V_{GSS}	± 6	V
Drain Current (Note 1)	I_D	870 630	mA

Maximum Ratings P-CHANNEL – Q_2 @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V_{DSS}	-20	V
Gate-Source Voltage	V_{GSS}	± 6	V
Drain Current (Note 1)	I_D	-640 -460	mA

Thermal Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 1)	P_D	530	mW
Thermal Resistance, Junction to Ambient (Note 1)	$R_{\theta JA}$	235	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Notes:

1. Device mounted on FR-4 PCB.
2. No purposefully added lead.
3. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

Electrical Characteristics N-CHANNEL – Q₁ @T_A = 25°C unless otherwise specified

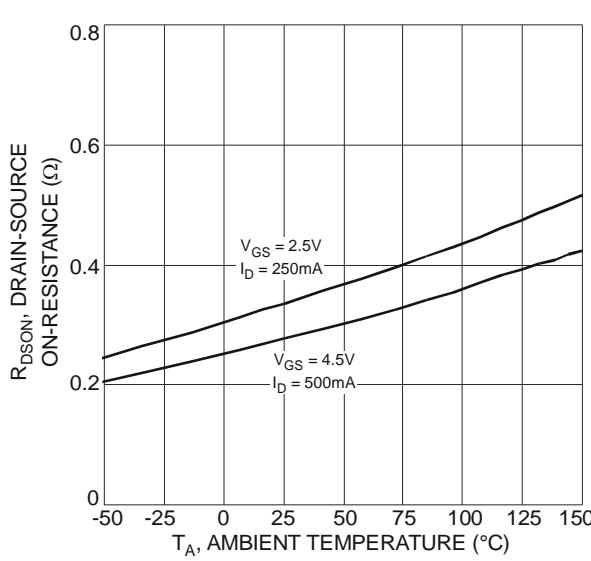
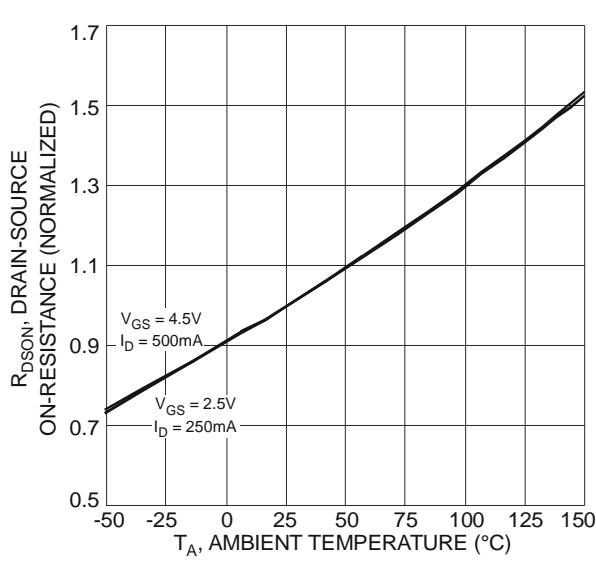
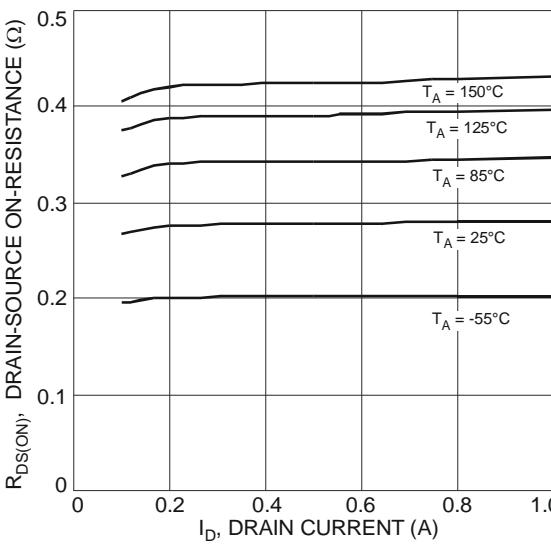
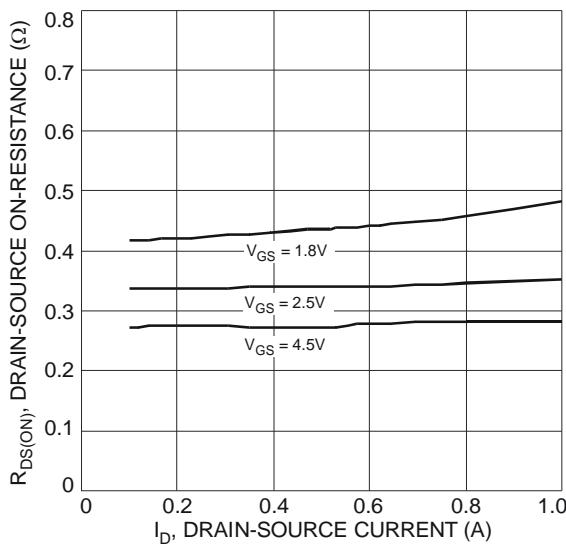
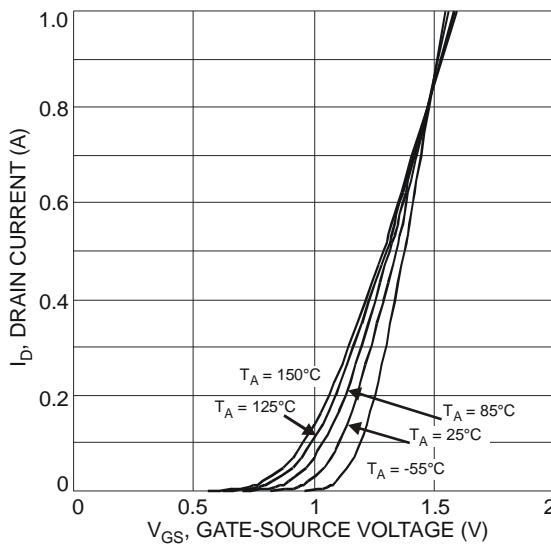
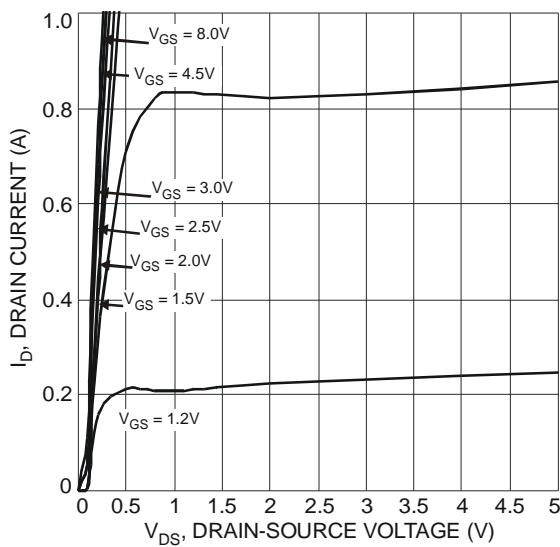
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	100	nA	V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	± 1.0	μA	V _{GS} = ±4.5V, V _{DS} = 0V
ON CHARACTERISTICS (Note 4)						
Gate Threshold Voltage	V _{GS(th)}	0.5	—	1.0	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS (ON)}	—	0.3	0.4	Ω	V _{GS} = 4.5V, I _D = 600mA
		—	0.4	0.5		V _{GS} = 2.5V, I _D = 500mA
		—	0.5	0.7		V _{GS} = 1.8V, I _D = 350mA
Forward Transfer Admittance	Y _{fs}	—	1.4	—	S	V _{DS} = 10V, I _D = 400mA
Diode Forward Voltage (Note 4)	V _{SD}	—	0.7	1.2	V	V _{GS} = 0V, I _S = 150mA
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}	—	60.67	—	pF	V _{DS} = 16V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	9.68	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	5.37	—	pF	
Total Gate Charge	Q _g	—	736.6	—	pC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 250mA
Gate-Source Charge	Q _{gs}	—	93.6	—		
Gate-Drain Charge	Q _{gd}	—	116.6	—		
Turn-On Delay Time	t _{d(on)}	—	5.1	—	ns	V _{DD} = 10V, V _{GS} = 4.5V, R _L = 47Ω, R _G = 10Ω, I _D = 200mA
Turn-On Rise Time	t _r	—	7.4	—		
Turn-Off Delay Time	t _{d(off)}	—	26.7	—		
Turn-Off Fall Time	t _f	—	12.3	—		

Electrical Characteristics P-CHANNEL – Q₂ @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-100	nA	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	± 2.0	μA	V _{GS} = ±4.5V, V _{DS} = 0V
ON CHARACTERISTICS (Note 4)						
Gate Threshold Voltage	V _{GS(th)}	-0.5	—	-1.0	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS (ON)}	—	0.5	0.7	Ω	V _{GS} = -4.5V, I _D = -430mA V _{GS} = -2.5V, I _D = -300mA V _{GS} = -1.8V, I _D = -150mA
Forward Transfer Admittance	Y _{fs}	—	-0.9	—	S	
Diode Forward Voltage (Note 4)	V _{SD}	—	-0.8	-1.2	V	
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}	—	59.76	—	pF	V _{DS} = -16V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	12.07	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	6.36	—	pF	
Total Gate Charge	Q _g	—	622.4	—	pC	V _{GS} = -4.5V, V _{DS} = -10V, I _D = -250mA
Gate-Source Charge	Q _{gs}	—	100.3	—		
Gate-Drain Charge	Q _{gd}	—	132.2	—		
Turn-On Delay Time	t _{d(on)}	—	5.1	—	ns	V _{DD} = -10V, V _{GS} = -4.5V, R _L = 47Ω, R _G = 10Ω, I _D = -200mA
Turn-On Rise Time	t _r	—	8.1	—		
Turn-Off Delay Time	t _{d(off)}	—	28.4	—		
Turn-Off Fall Time	t _f	—	20.7	—		

Notes: 4. Short duration pulse test used to minimize self-heating effect.

N-CHANNEL – Q₁



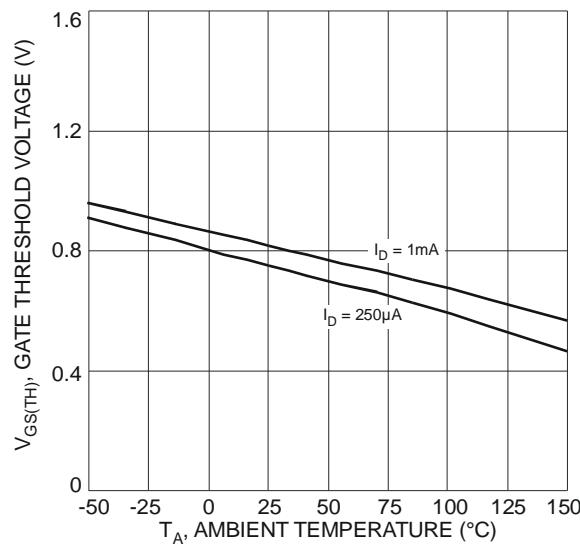
N-CHANNEL – Q₁ (continued)

Fig. 7 Gate Threshold Variation vs. Ambient Temperature

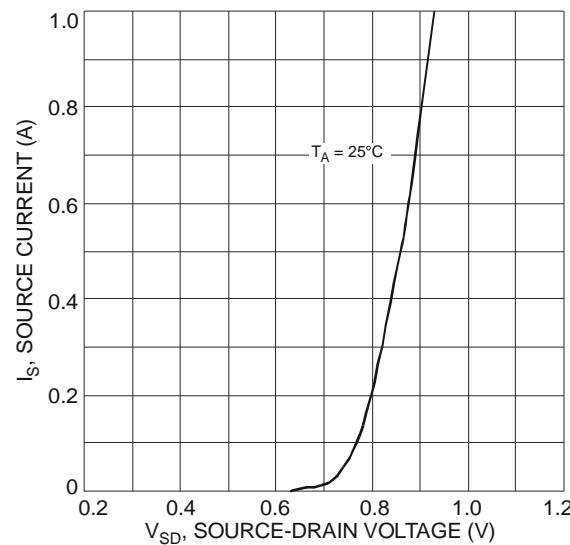


Fig. 8 Diode Forward Voltage vs. Current

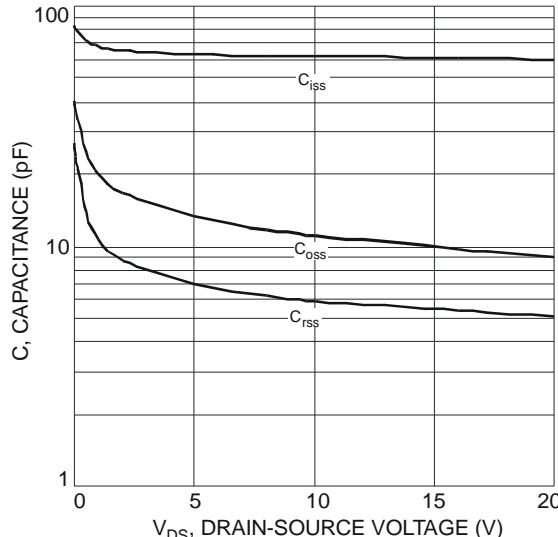


Fig. 9 Typical Total Capacitance

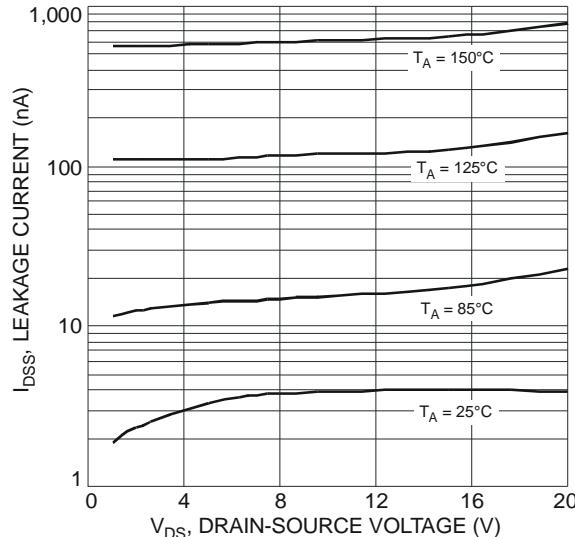


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

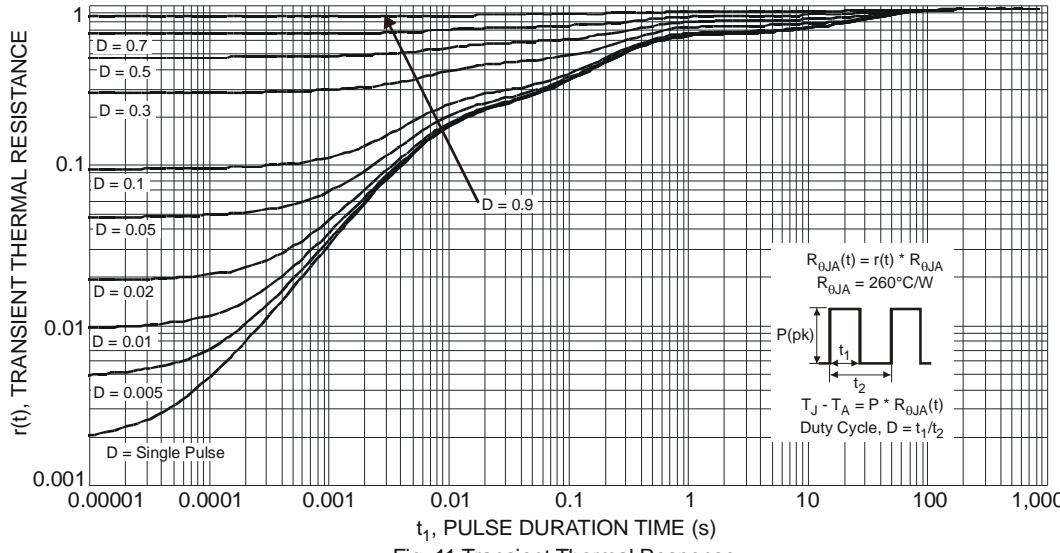


Fig. 11 Transient Thermal Response

P-CHANNEL – Q₂

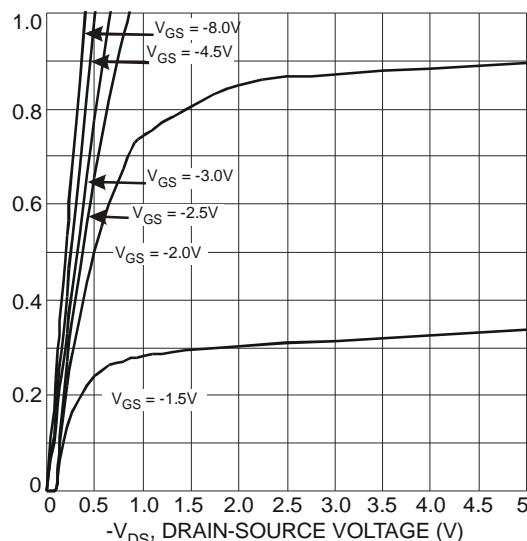


Fig. 12 Typical Output Characteristic

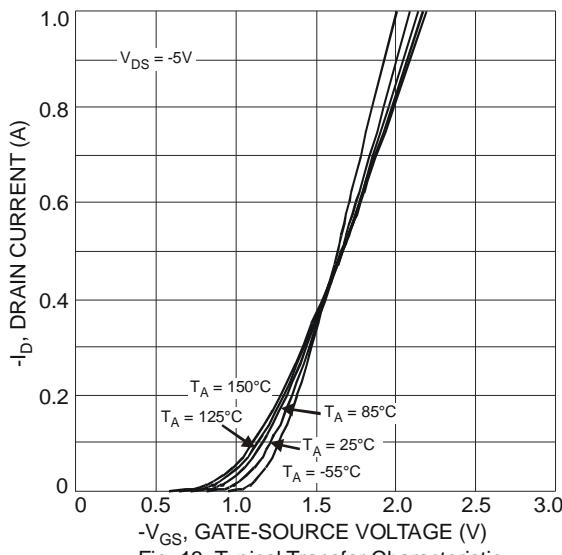


Fig. 13 Typical Transfer Characteristic

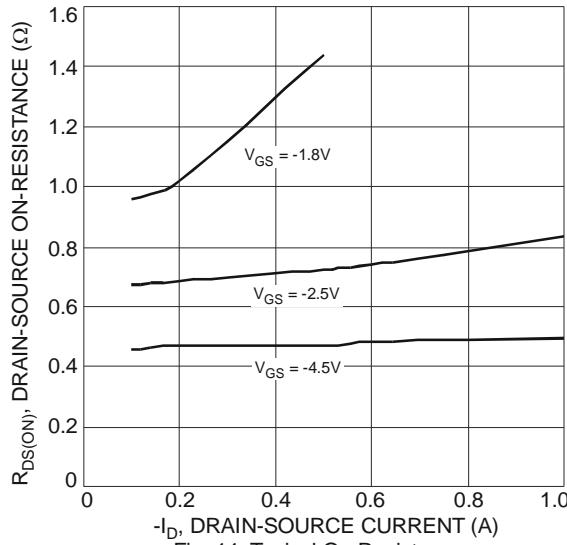


Fig. 14 Typical On-Resistance
vs. Drain Current and Gate Voltage

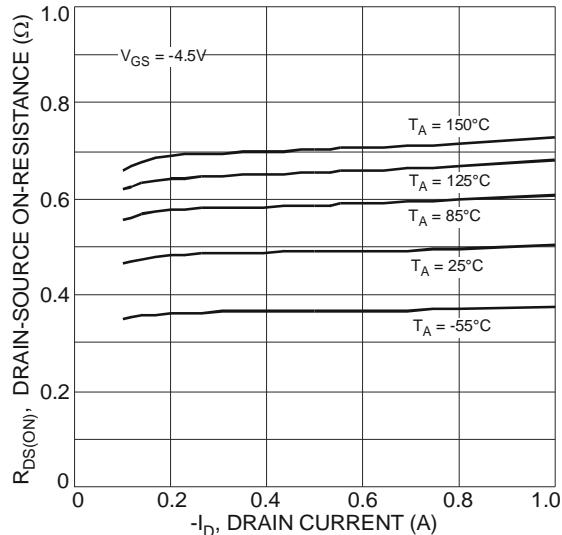


Fig. 15 Typical On-Resistance
vs. Drain Current and Temperature

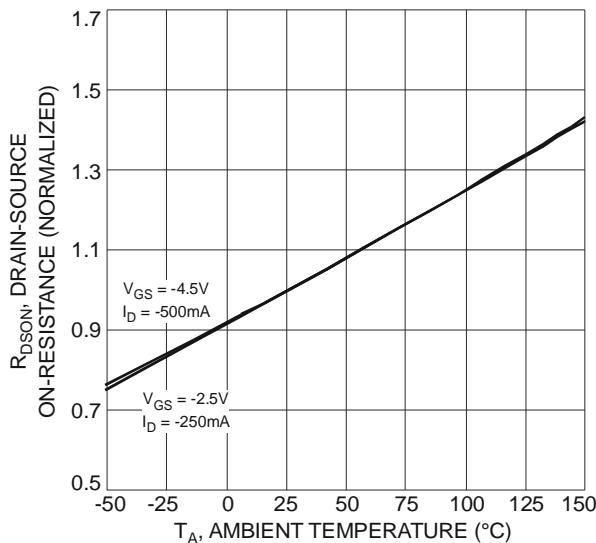


Fig. 16 On-Resistance Variation with Temperature

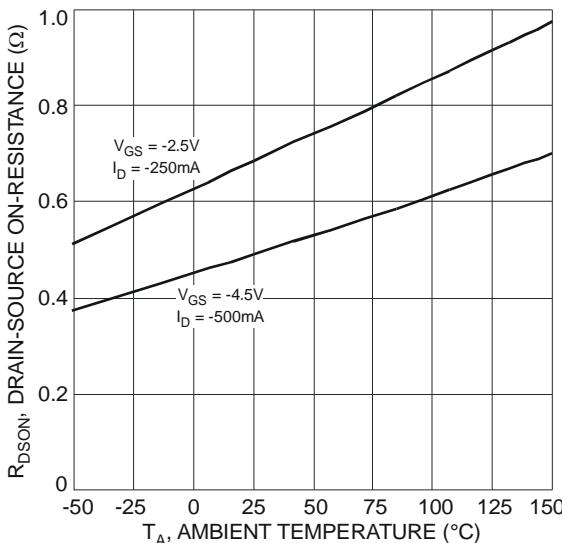


Fig. 17 On-Resistance Variation with Temperature

P-CHANNEL – Q₂ (continued)

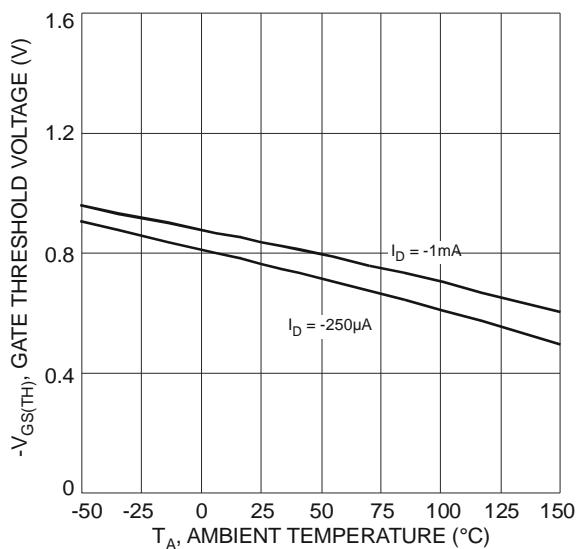


Fig. 18 Gate Threshold Variation vs. Ambient Temperature

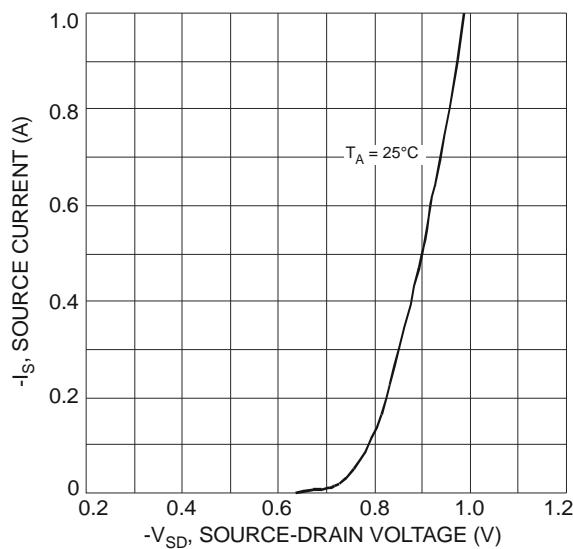


Fig. 19 Diode Forward Voltage vs. Current

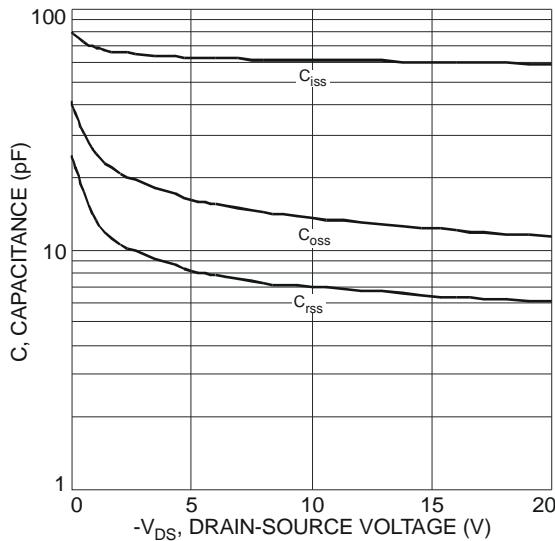


Fig. 20 Typical Total Capacitance

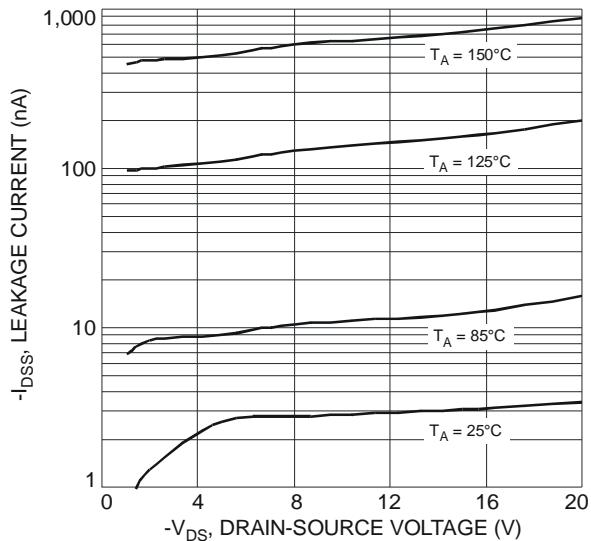


Fig. 21 Typical Leakage Current vs. Drain-Source Voltage

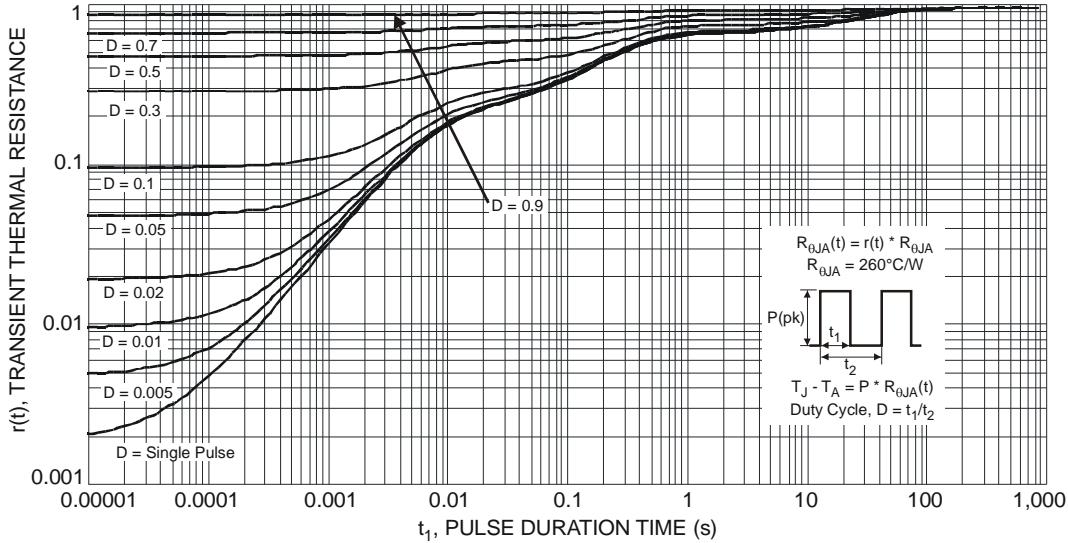


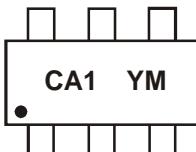
Fig. 22 Transient Thermal Response

Ordering Information (Note 5)

Part Number	Case	Packaging
DMG1016V-7	SOT-563	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information

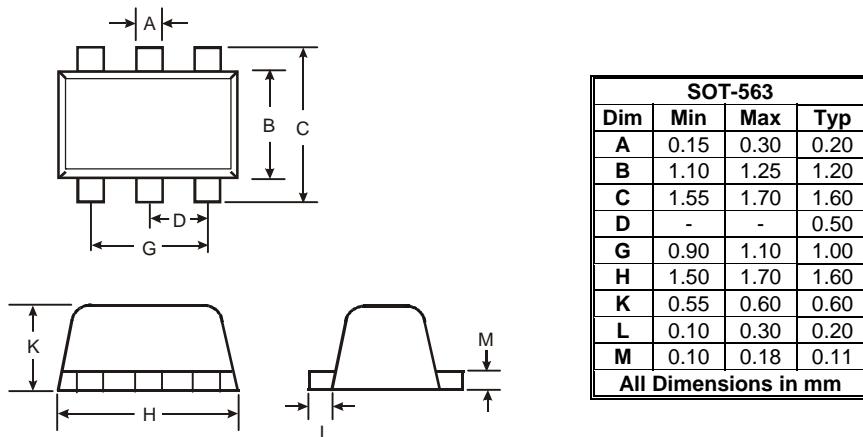


CA1 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: W = 2009)
 M = Month (ex: 9 = September)

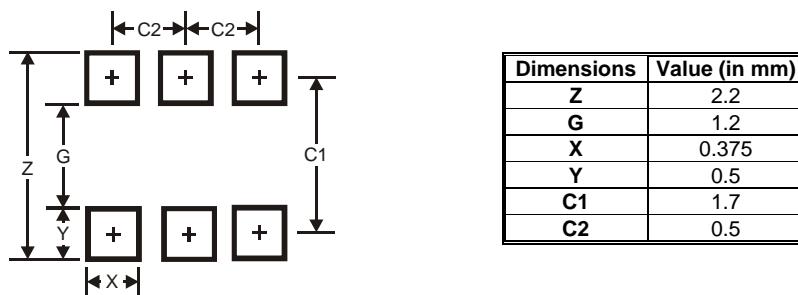
Date Code Key

Year	2009	2010	2011	2012	2013	2014	2015					
Code	W	X	Y	Z	A	B	C					
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Package Outline Dimensions



Suggested Pad Layout



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