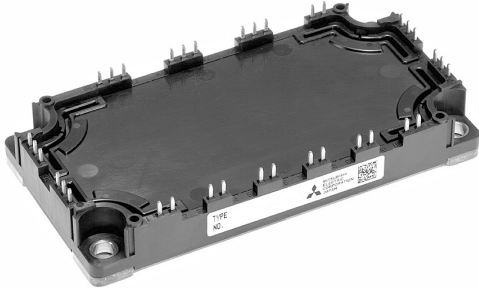


< IGBT MODULES >

CM100TX-24S1

HIGH POWER SWITCHING USE
INSULATED TYPE



sixpack (3φ Inverter)

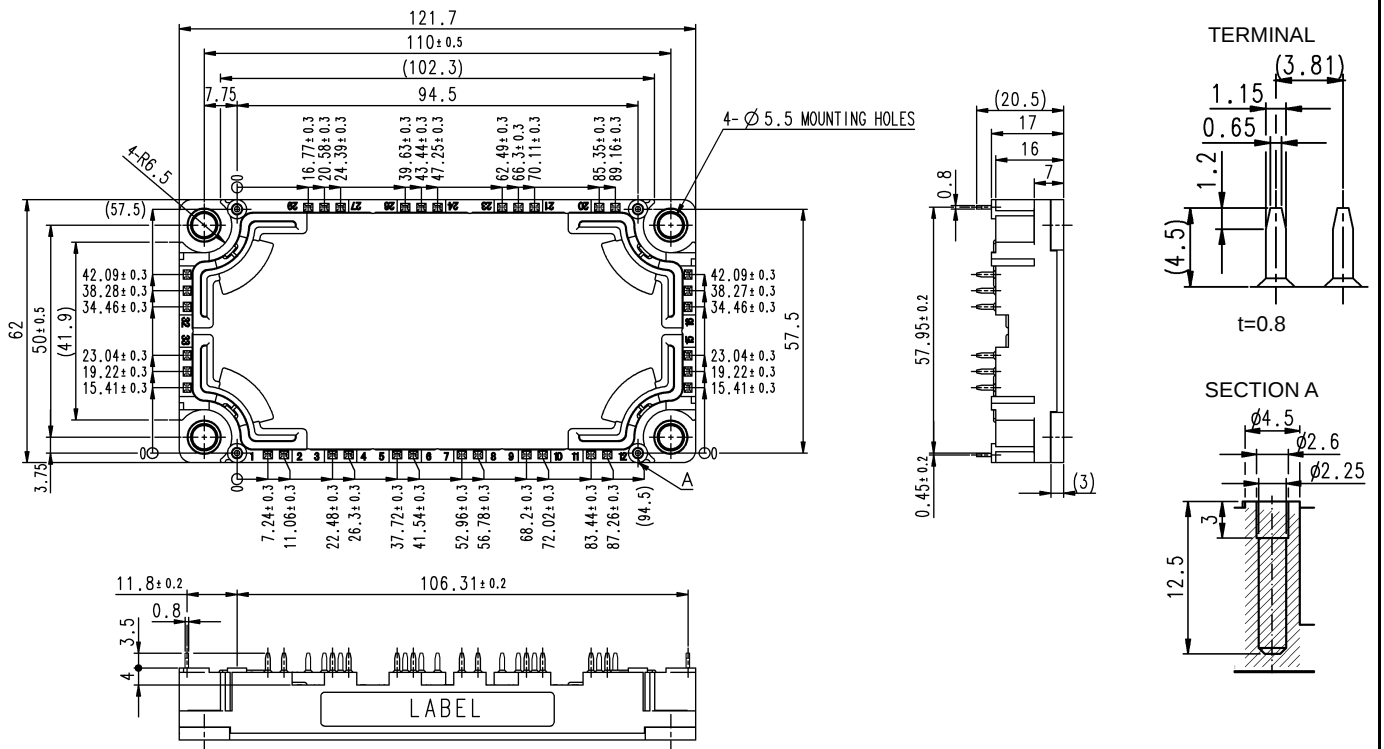
Collector current I_C **1 0 0 A**
 Collector-emitter voltage V_{CES} **1 2 0 0 V**
 Maximum junction temperature T_{jmax} **1 7 5 °C**

- Flat base Type
- Copper base plate (non-plating)
- Tin plating pin terminals
- RoHS Directive compliant
- Recognized under UL1557, File E323585

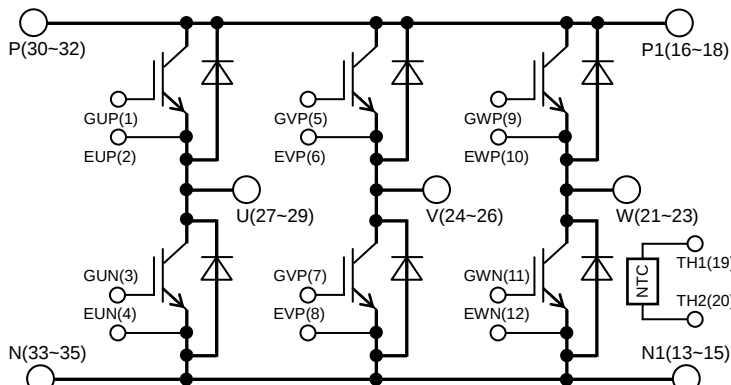
APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.

OUTLINE DRAWING & INTERNAL CONNECTION



INTERNAL CONNECTION



Tolerance otherwise specified

Division of Dimension	Tolerance
0.5 to 3	±0.2
over 3 to 6	±0.3
over 6 to 30	±0.5
over 30 to 120	±0.8
over 120 to 400	±1.2

< IGBT MODULES >

CM100TX-24S1

HIGH POWER SWITCHING USE
INSULATED TYPEMAXIMUM RATINGS ($T_J=25\text{ }^{\circ}\text{C}$, unless otherwise specified)

INVERTER PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit
V_{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
I_C	Collector current	DC, $T_C=107\text{ }^{\circ}\text{C}$ (Note2, 4)	100	A
I_{CRM}		Pulse, Repetitive (Note3)	200	
P_{tot}	Total power dissipation	$T_C=25\text{ }^{\circ}\text{C}$ (Note2, 4)	625	W
I_E (Note1)	Emitter current	DC (Note2)	100	A
I_{ERM} (Note1)		Pulse, Repetitive (Note3)	200	

MODULE

Symbol	Item	Conditions	Rating	Unit
V_{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T_{jmax}	Maximum junction temperature	Instantaneous event (overload)	175	$^{\circ}\text{C}$
T_{Cmax}	Maximum case temperature	(Note4)	125	
T_{jop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	$^{\circ}\text{C}$
T_{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS ($T_J=25\text{ }^{\circ}\text{C}$, unless otherwise specified)

INVERTER PART IGBT/DIODE

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μA
V _{GE(th)}	Gate-emitter threshold voltage	I _C =10 mA, V _{CE} =10 V		5.4	6.0	6.6	V
V _{CEsat} (Terminal)	Collector-emitter saturation voltage	I _C =100 A, V _{GE} =15 V, Refer to the figure of test circuit (Note5)	T _J =25 °C	-	1.80	2.25	V
			T _J =125 °C	-	2.00	-	
			T _J =150 °C	-	2.05	-	
V _{CEsat} (Chip)		I _C =100 A, V _{GE} =15 V, (Note5)	T _J =25 °C	-	1.70	2.15	V
			T _J =125 °C	-	1.90	-	
			T _J =150 °C	-	1.95	-	
C _{ies}	Input capacitance	V _{CE} =10 V, G-E short-circuited		-	-	10	nF
C _{oes}	Output capacitance			-	-	2.0	
C _{res}	Reverse transfer capacitance			-	-	0.17	
Q _G	Gate charge	V _{CC} =600 V, I _C =100 A, V _{GE} =15 V		-	210	-	nC
t _{d(on)}	Turn-on delay time	V _{CC} =600 V, I _C =100 A, V _{GE} =±15 V, R _G =6.2 Ω, Inductive load		-	-	300	ns
t _r	Rise time			-	-	200	
t _{d(off)}	Turn-off delay time			-	-	600	
t _f	Fall time			-	-	300	
V _{EC} (Note1) (Terminal)	Emitter-collector voltage	I _E =100 A, G-E short-circuited, Refer to the figure of test circuit (Note5)	T _J =25 °C	-	2.60	3.40	V
			T _J =125 °C	-	2.16	-	
			T _J =150 °C	-	2.10	-	
V _{EC} (Note1) (Chip)		I _E =100 A, G-E short-circuited, (Note5)	T _J =25 °C	-	2.50	3.30	V
			T _J =125 °C	-	2.06	-	
			T _J =150 °C	-	2.00	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =600 V, I _E =100 A, V _{GE} =±15 V, R _G =6.2 Ω, Inductive load		-	-	300	ns
Q _{rr} (Note1)	Reverse recovery charge	R _G =6.2 Ω, Inductive load		-	2.7	-	μC
E _{on}	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =I _E =100 A, V _{GE} =±15 V, R _G =6.2 Ω, T _J =150 °C, Inductive load		-	5.9	-	mJ
E _{off}	Turn-off switching energy per pulse			-	9.7	-	
E _{rr} (Note1)	Reverse recovery energy per pulse			-	9.7	-	mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =25 °C (Note4)		-	-	2.2	mΩ
r _g	Internal gate resistance	Per switch		-	0	-	Ω

ELECTRICAL CHARACTERISTICS (cont.; $T_j=25\text{ }^{\circ}\text{C}$, unless otherwise specified)

NTC THERMISTOR PART

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
R_{25}	Zero-power resistance	$T_C=25\text{ }^{\circ}\text{C}$ (Note4)	4.85	5.00	5.15	$\text{k}\Omega$
$\Delta R/R$	Deviation of resistance	$R_{100}=493\text{ }\Omega$, $T_C=100\text{ }^{\circ}\text{C}$ (Note4)	-7.3	-	+7.8	%
$B_{(25/50)}$	B-constant	Approximate by equation (Note6)	-	3375	-	K
P_{25}	Power dissipation	$T_C=25\text{ }^{\circ}\text{C}$ (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	0.24	K/W
$R_{th(j-c)D}$		Junction to case, per Inverter DIODE (Note4)	-	-	0.37	
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, per 1 module, Thermal grease applied (Note4, 7)	-	15	-	K/kW

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
M_s	Mounting torque	Mounting to heat sink M 5 screw	2.5	3.0	3.5	N·m
m	mass	-	-	330	-	g
d_s	Creepage distance	Terminal to terminal	16.3	-	-	mm
		Terminal to base plate	19.1	-	-	
d_a	Clearance	Terminal to terminal	10.3	-	-	mm
		Terminal to base plate	15.3	-	-	
e_c	Flatness of base plate	On the centerline X, Y (Note8)	± 0	-	+100	μm

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (DIODE).

- Junction temperature (T_j) should not increase beyond $T_{j\text{max}}$ rating.
- Pulse width and repetition rate should be such that the device junction temperature (T_j) dose not exceed $T_{j\text{max}}$ rating.
- Case temperature (T_C) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- Pulse width and repetition rate should be such as to cause negligible temperature rise.

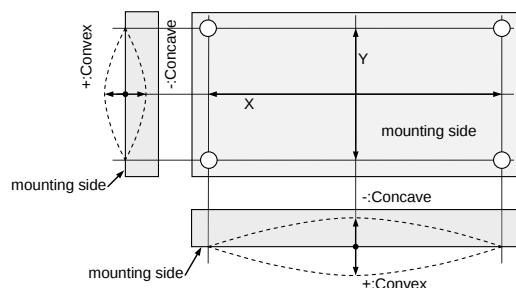
$$6. B_{(25/50)} = \ln\left(\frac{R_{25}}{R_{50}}\right) / \left(\frac{1}{T_{25}} - \frac{1}{T_{50}}\right),$$

R_{25} : resistance at absolute temperature T_{25} [K]; $T_{25}=25\text{ }^{\circ}\text{C}+273.15=298.15$ [K]

R_{50} : resistance at absolute temperature T_{50} [K]; $T_{50}=50\text{ }^{\circ}\text{C}+273.15=323.15$ [K]

- Typical value is measured by using thermally conductive grease of $\lambda=0.9\text{ W/(m}\cdot\text{K)}$.

- The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.

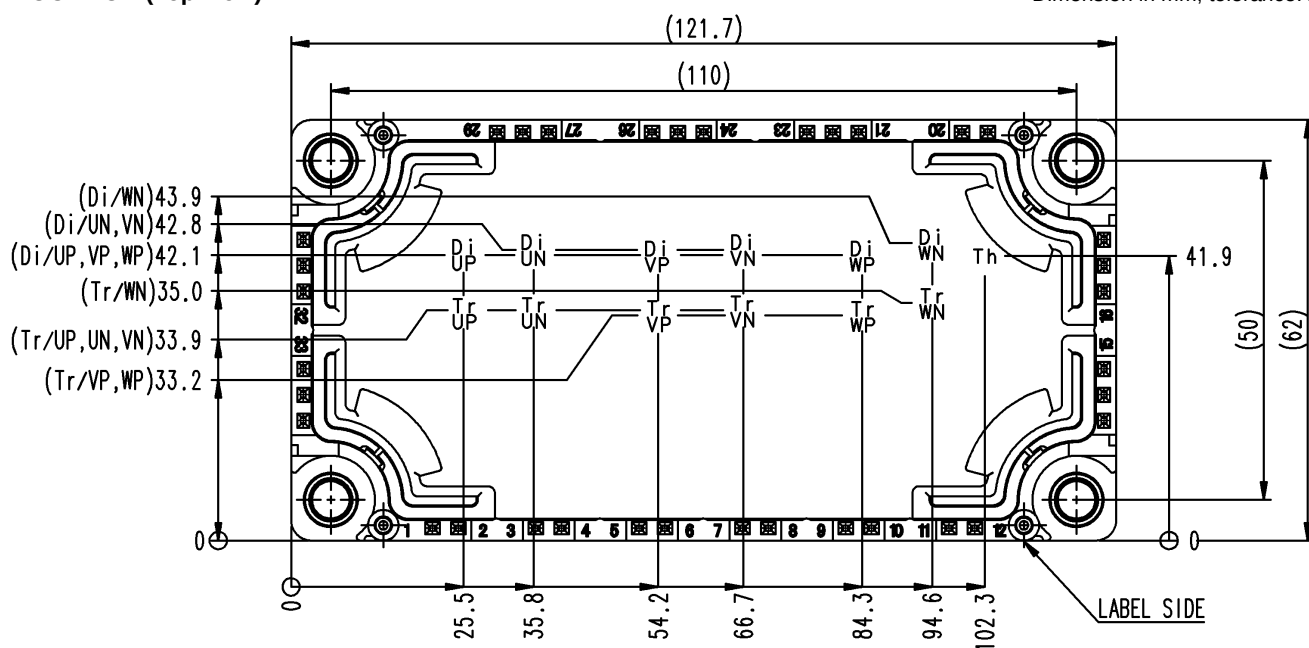


- Use the following screws when mounting the printed circuit board (PCB) on the stand offs.
"φ2.6×10 or φ2.6×12 B1 tapping screw"
The length of the screw depends on the thickness (t1.6~t2.0) of the PCB.

RECOMMENDED OPERATING CONDITIONS

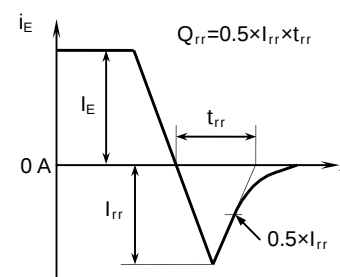
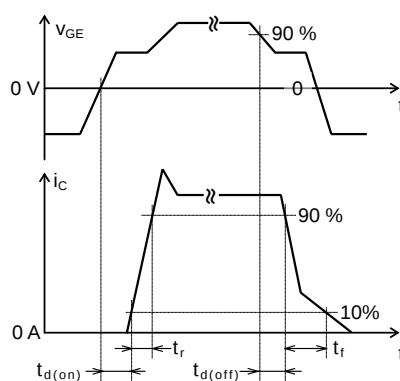
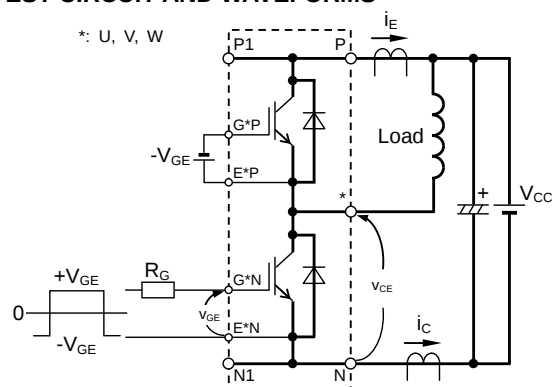
Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
V_{CC}	(DC) Supply voltage	Applied across P-N/P1-N1 terminals	-	600	850	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G*P-E*P/G*N-E*N(*=U, V, W) terminals	13.5	15.0	16.5	V
R_G	External gate resistance	Per switch	6.2	-	62	Ω

CHIP LOCATION (Top view)

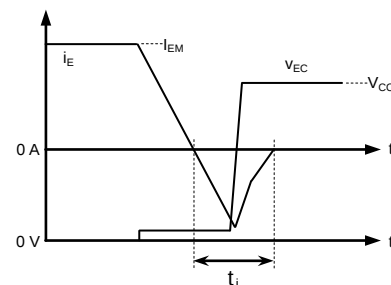
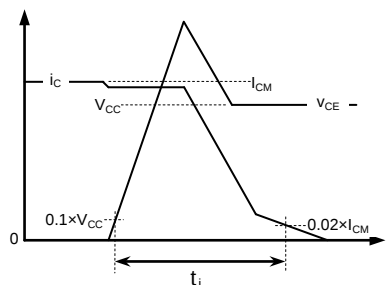
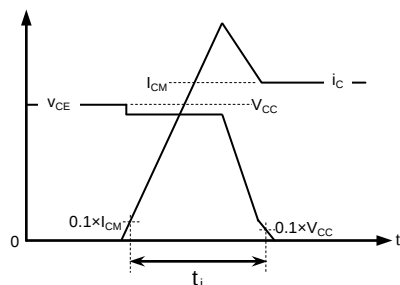
Dimension in mm, tolerance: ± 1 mm

Tr*P/Tr*N: IGBT, Di*P/Di*N: DIODE (*=U/V/W), Th: NTC thermistor

TEST CIRCUIT AND WAVEFORMS



Switching characteristics test circuit and waveforms

 t_{rr} , Q_{rr} characteristics test waveform

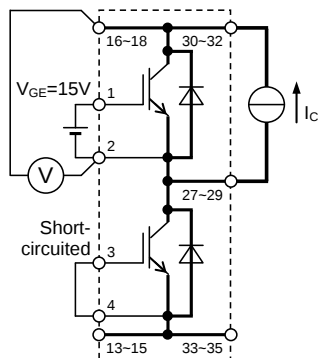
IGBT Turn-on switching energy

IGBT Turn-off switching energy

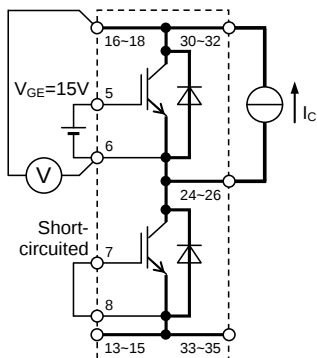
DIODE Reverse recovery energy

Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

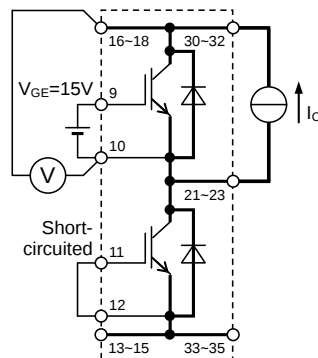
TEST CIRCUIT



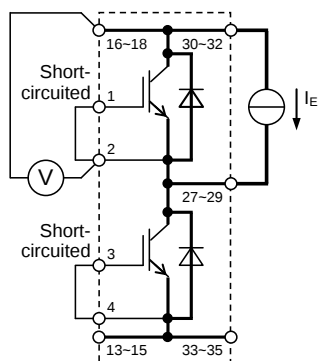
Gate-emitter GVP-EVP, GVN-EVN,
short-circuited GWP-EWP, GWN-EWN
UP / UN IGBT



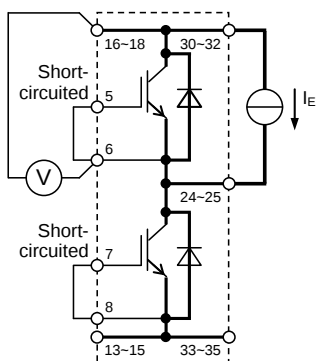
Gate-emitter GUP-EUP, GUN-EUN,
short-circuited GWP-EWP, GWN-EWN
VP / VN IGBT



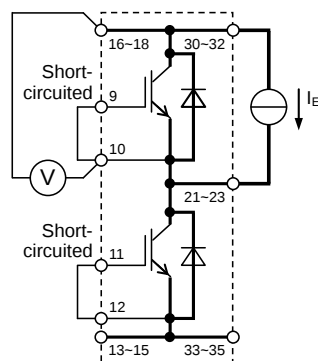
Gate-emitter GUP-EUP, GUN-EUN,
short-circuited GVP-EVP, GVN-EVN
WP / WN IGBT

 V_{CEsat} characteristics test circuit

Gate-emitter GVP-EVP, GVN-EVN,
short-circuited GWP-EWP, GWN-EWN
UP / UN DIODE



Gate-emitter GUP-EUP, GUN-EUN,
short-circuited GWP-EWP, GWN-EWN
VP / VN DIODE



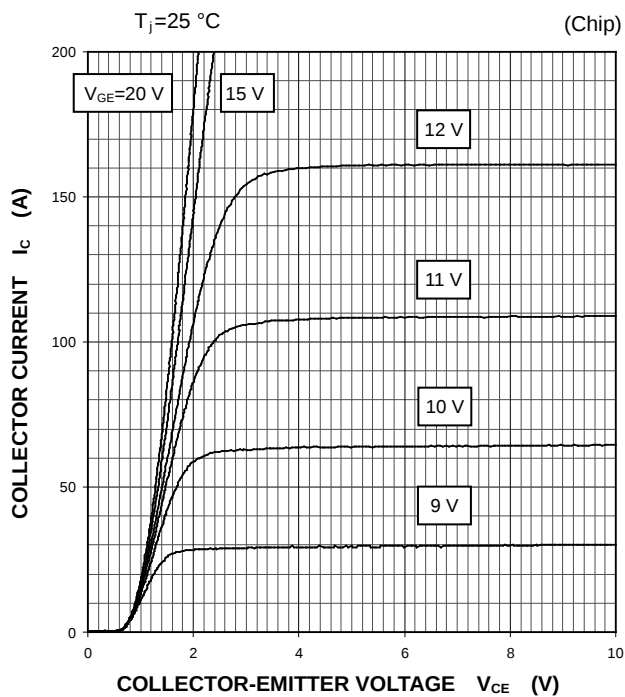
Gate-emitter GUP-EUP, GUN-EUN,
short-circuited GVP-EVP, GVN-EVN
WP / WN DIODE

 V_{EC} characteristics test circuit

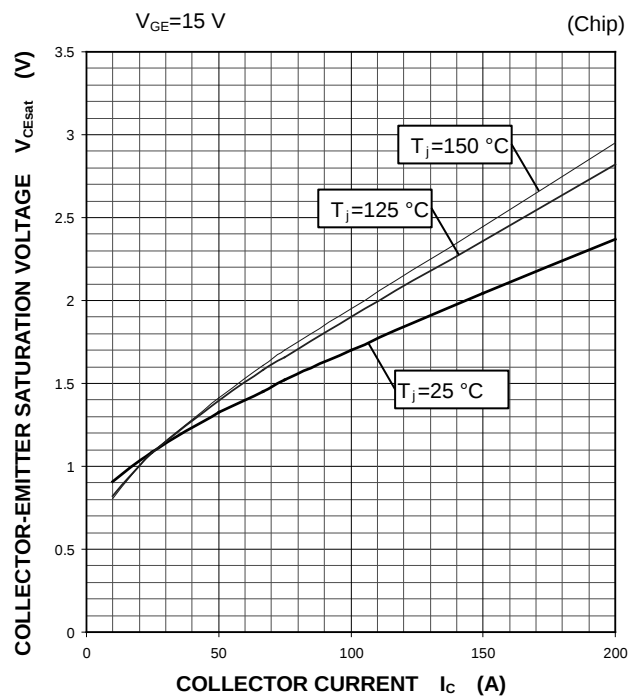
PERFORMANCE CURVES

INVERTER PART

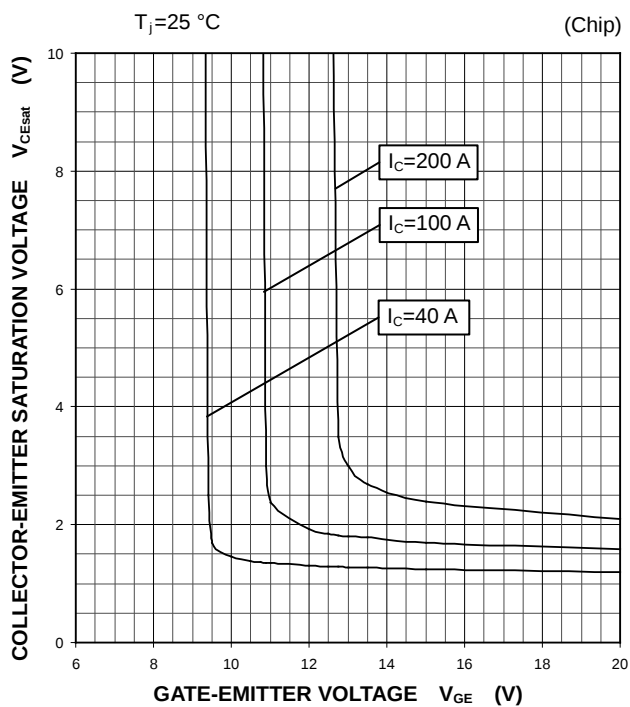
OUTPUT CHARACTERISTICS
(TYPICAL)



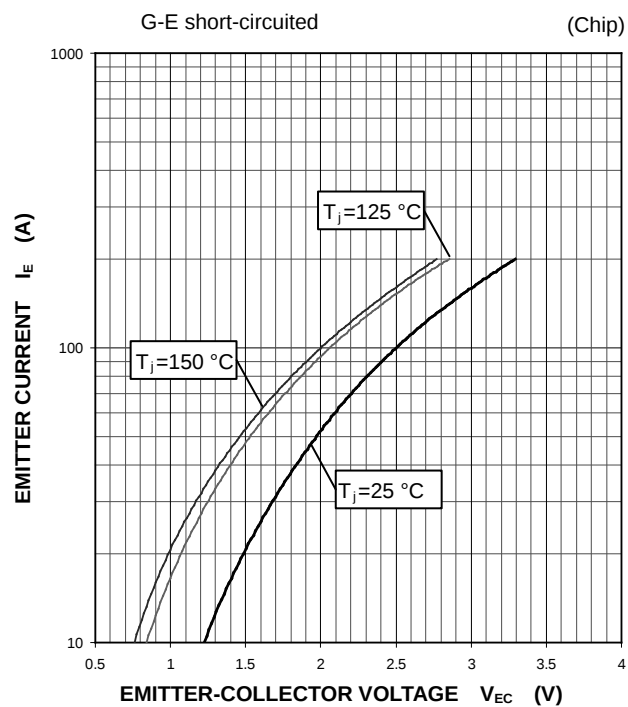
COLLECTOR-EMITTER SATURATION VOLTAGE
CHARACTERISTICS
(TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE
CHARACTERISTICS
(TYPICAL)



FREE WHEELING DIODE
FORWARD CHARACTERISTICS
(TYPICAL)

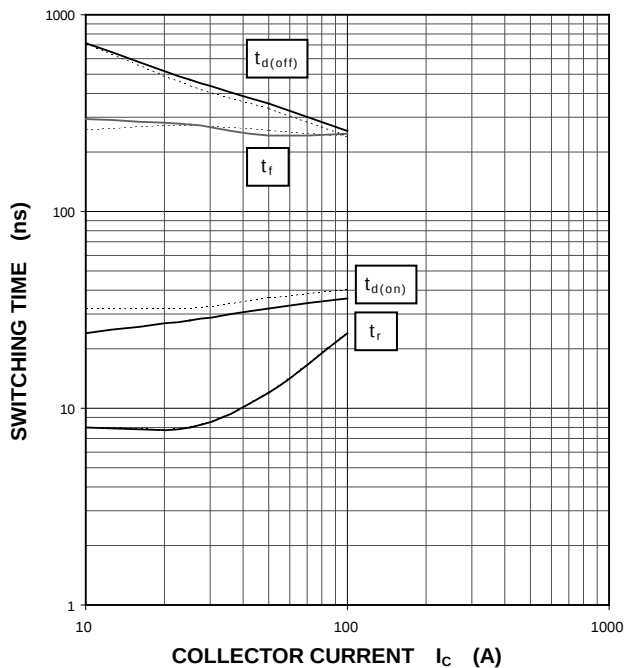


PERFORMANCE CURVES

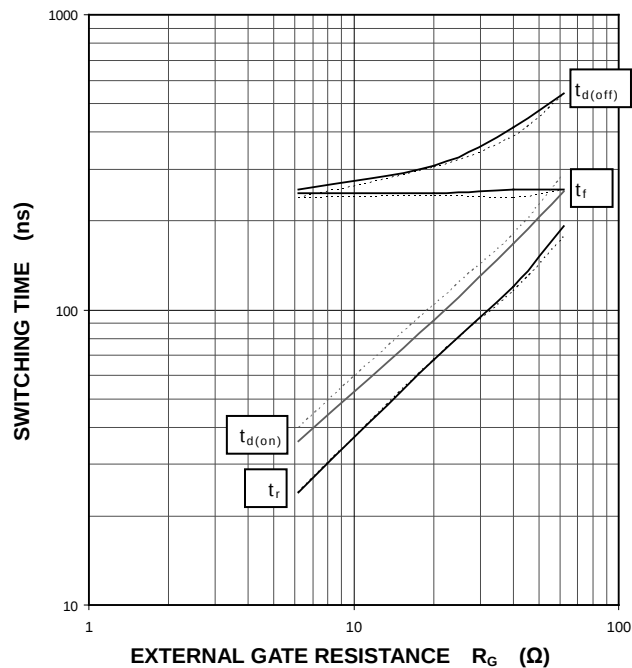
INVERTER PART

HALF-BRIDGE
SWITCHING CHARACTERISTICS
(TYPICAL)

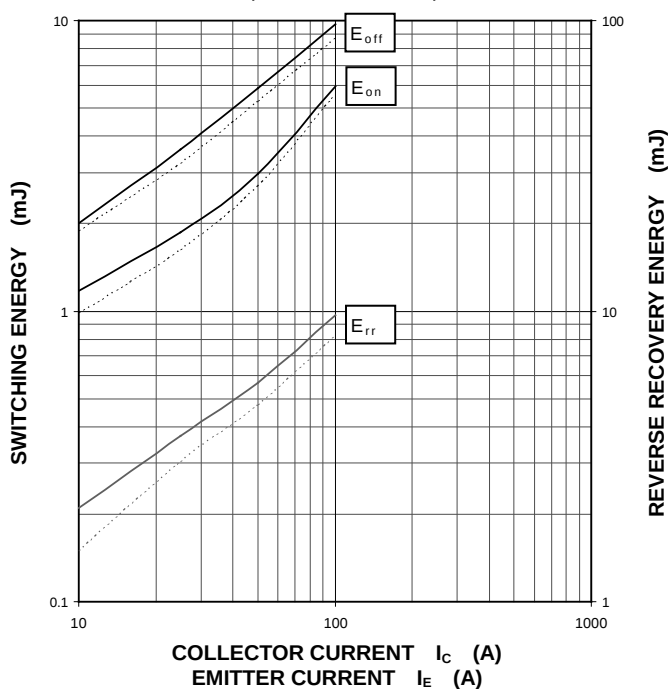
$V_{CC}=600\text{ V}$, $V_{GE}=\pm 15\text{ V}$, $R_G=6.2\ \Omega$, INDUCTIVE LOAD
 —: $T_J=150\text{ }^\circ\text{C}$, - - - -: $T_J=125\text{ }^\circ\text{C}$

HALF-BRIDGE
SWITCHING CHARACTERISTICS
(TYPICAL)

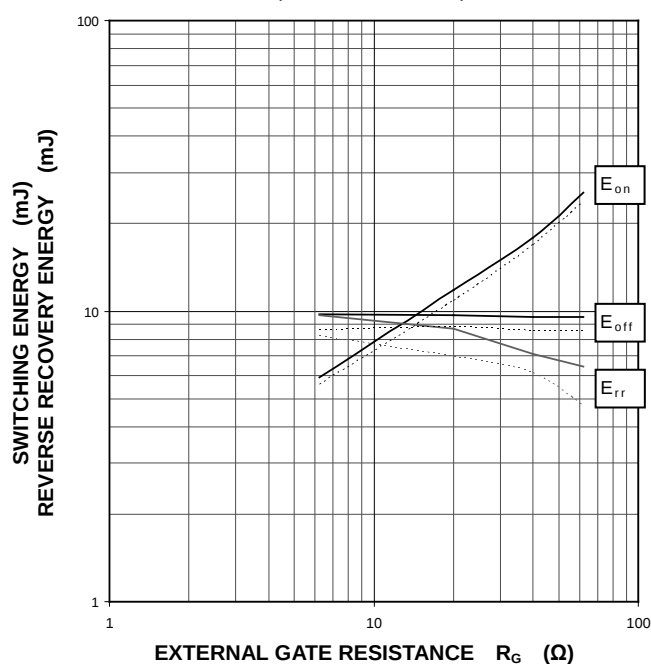
$V_{CC}=600\text{ V}$, $V_{GE}=\pm 15\text{ V}$, $I_c=100\text{ A}$, INDUCTIVE LOAD
 —: $T_J=150\text{ }^\circ\text{C}$, - - - -: $T_J=125\text{ }^\circ\text{C}$

HALF-BRIDGE
SWITCHING CHARACTERISTICS
(TYPICAL)

$V_{CC}=600\text{ V}$, $V_{GE}=\pm 15\text{ V}$, $R_G=6.2\ \Omega$,
INDUCTIVE LOAD, PER PULSE
 —: $T_J=150\text{ }^\circ\text{C}$, - - - -: $T_J=125\text{ }^\circ\text{C}$

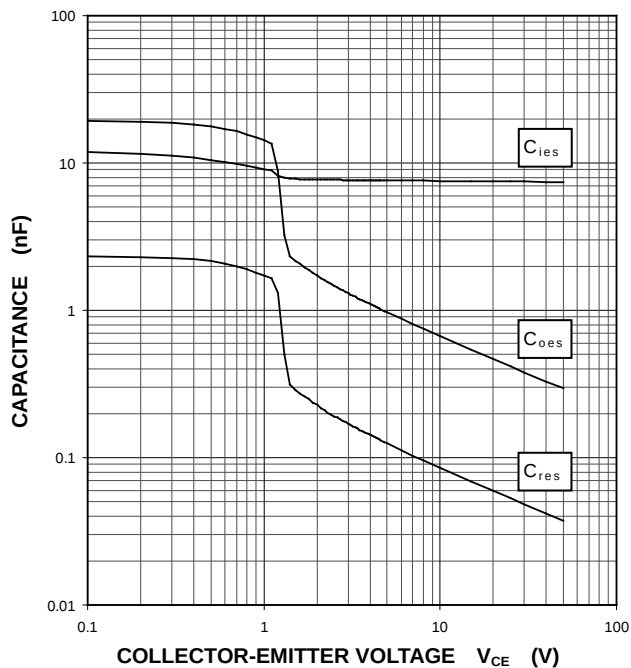
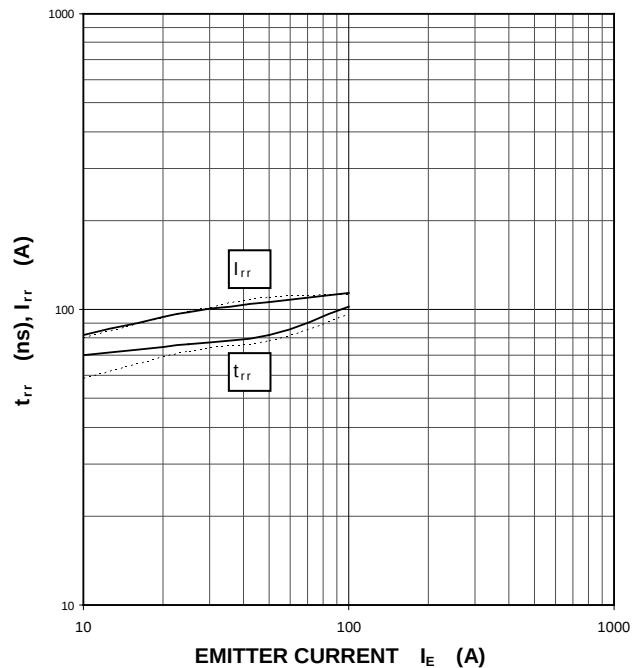
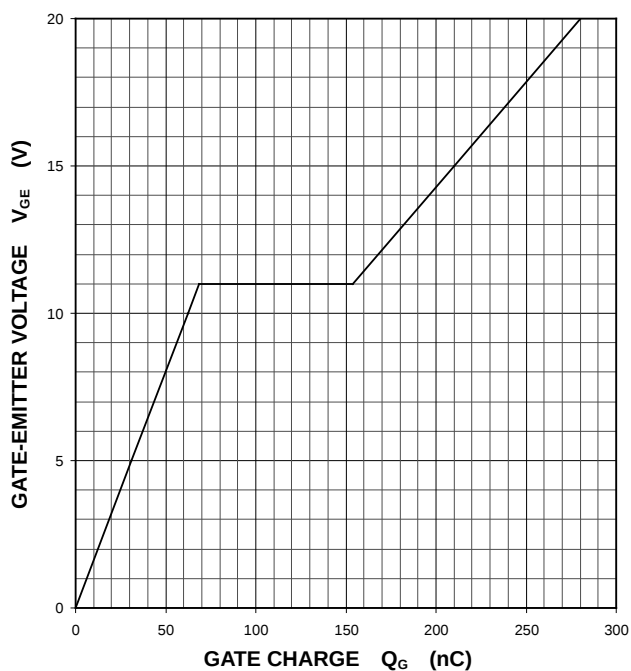
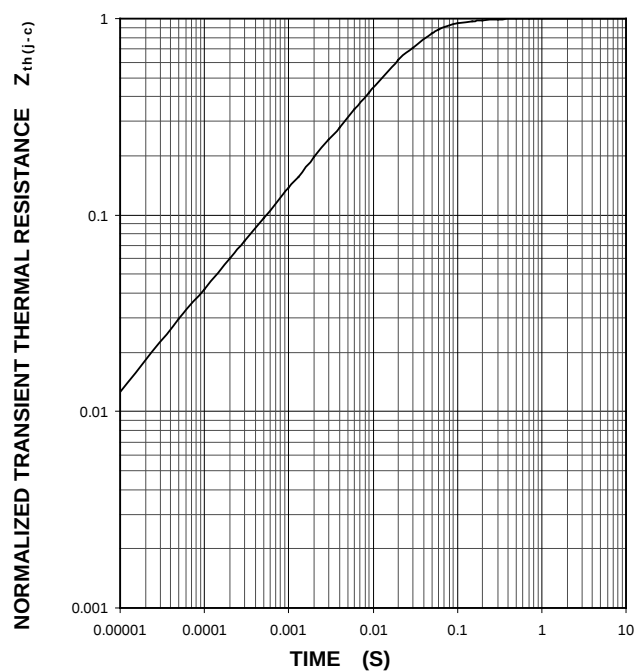
HALF-BRIDGE
SWITCHING CHARACTERISTICS
(TYPICAL)

$V_{CC}=600\text{ V}$, $V_{GE}=\pm 15\text{ V}$, $I_c/I_E=100\text{ A}$,
INDUCTIVE LOAD, PER PULSE
 —: $T_J=150\text{ }^\circ\text{C}$, - - - -: $T_J=125\text{ }^\circ\text{C}$



PERFORMANCE CURVES

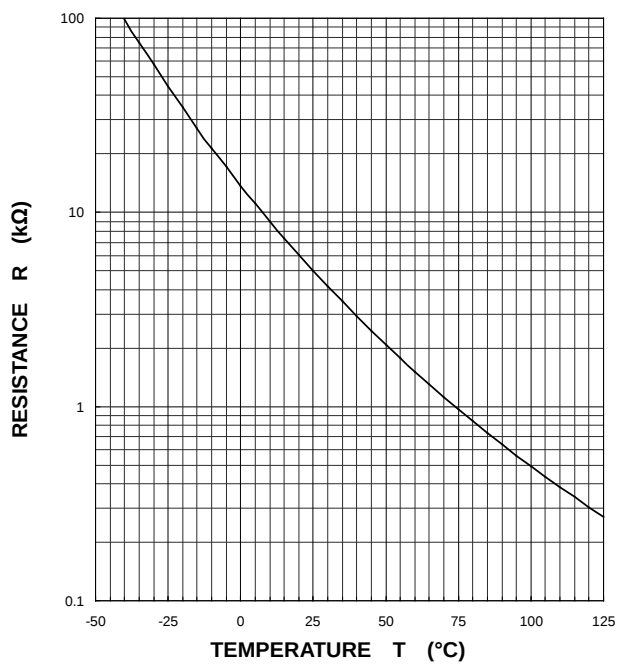
INVERTER PART

CAPACITANCE CHARACTERISTICS
(TYPICAL)G-E short-circuited, $T_J=25\text{ }^{\circ}\text{C}$ FREE WHEELING DIODE
REVERSE RECOVERY CHARACTERISTICS
(TYPICAL) $V_{CC}=600\text{ V}$, $V_{GE}=\pm 15\text{ V}$, $R_G=6.2\text{ }\Omega$, INDUCTIVE LOAD
—: $T_J=150\text{ }^{\circ}\text{C}$, - - - -: $T_J=125\text{ }^{\circ}\text{C}$ GATE CHARGE CHARACTERISTICS
(TYPICAL) $V_{CC}=600\text{ V}$, $I_C=100\text{ A}$, $T_J=25\text{ }^{\circ}\text{C}$ TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS
(MAXIMUM)Single pulse, $T_C=25\text{ }^{\circ}\text{C}$
 $R_{th(j-c)Q}=0.24\text{ K/W}$, $R_{th(j-c)D}=0.37\text{ K/W}$ 

PERFORMANCE CURVES

NTC thermistor part

TEMPERATURE CHARACTERISTICS
(TYPICAL)



Keep safety first in your circuit designs!

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