



Vishay Semiconductors

HEXFRED® Ultrafast Soft Recovery Diode, 200 A



SOT-227

| PRODUCT SUMMARY | | | | |
|--------------------------------------|----------------|--|--|--|
| V_{R} | 1200 V | | | |
| V _F (typical) | 2.7 V | | | |
| t _{rr} (typical) | 150 ns | | | |
| I _{F(DC)} at T _C | 100 A at 69 °C | | | |

FEATURES

- Fast recovery time characteristic
- · Electrically isolated base plate
- Large creepage distance between terminal
- · Simplified mechanical designs, rapid assembly
- UL approved file E78996



- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level



The dual diode series configuration (HFA200FA120P) is used for output rectification or freewheeling/clamping operation and high voltage application.

The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are intended for general applications such as HV power supplies, electronic welders, motor control and inverters.

| ABSOLUTE MAXIMUM RATINGS | | | | | |
|--|---|---|-------------|-------|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | MAX. | UNITS | |
| Cathode to anode voltage | V _R | | 1200 | V | |
| Continuous forward current | I _F | T _C = 69 °C | 100 | | |
| Single pulse forward current | I _{FSM} T _J = 25 °C | | 900 | Α | |
| Maximum repetitive forward current | I _{FRM} | Rated V _{R,} square wave, 20 kHz, T _C = 60 °C | 150 | | |
| Maximum power dissipation | В | T _C = 25 °C | 416 | W | |
| Maximum power dissipation | P _D | T _C = 100 °C | 166 | | |
| RMS isolation voltage | V _{ISOL} | Any terminal to case, t = 1 minute | 2500 | V | |
| Operating junction and storage temperature range | T _J , T _{Stg} | | - 55 to 150 | °C | |

| ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified) | | | | | | |
|--|---|---|------|------|------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNITS |
| Cathode to anode breakdown voltage | V _{BR} | I _R = 100 μA | 1200 | - | - | |
| | I _F = 100 A | - | 2.68 | 3.6 | V | |
| Forward voltage | orward voltage V _{FM} | I _F = 200 A | - | 3.37 | 4.7 | |
| | | I _F = 100 A, T _J = 150 °C | - | 2.7 | 2.9 | |
| Reverse leakage current I _{RM} | V _R = V _R rated | - | 10 | 75 | μA | |
| | $T_J = 125 ^{\circ}\text{C}, V_R = V_R \text{ rated}$ | - | 2 | - | mA | |

Not Available for New Designs, Use VS-HFA220FA120

HFA200FA120P

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Document Number: 94607

Revision: 22-Jul-10

| DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified) | | | | | | | |
|---|-------------------------|--|---|------|------|------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | MIN. | TYP. | MAX. | UNITS |
| B | + | T _J = 25 °C | I _F = 50 A dI _F /dt = - 200 A/μs | - | 150 | - | ns |
| Reverse recovery time | t _{rr} | T _J = 125 °C | | - | 237 | - | |
| Peak recovery current I _{RRM} | , | T _J = 25 °C | | - | 14 | - | ^ |
| | T _J = 125 °C | di _F /dt = - 200 A/μs V _R = 200 V | - | 21 | - | Α | |
| Reverse recovery charge | 0 | T _J = 25 °C | V _N = 200 V | - | 1050 | - | nC |
| | Q _{rr} | T _J = 125 °C | | - | 2430 | - | |

| THERMAL - MECHANICAL SPECIFICATIONS | | | | | | |
|---|-------------------|---------------------------|------|------|------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNITS |
| Junction to case, single leg conducting | 0 | | - | - | 0.3 | |
| Junction to case, both legs conducting | R _{thJC} | | - | - | 0.15 | °C/W |
| Case to heatsink | R _{thCS} | Flat, greased and surface | - | 0.05 | - | |
| Weight | | | - | 30 | - | g |
| Mounting torque | | | - | 1.3 | - | Nm |





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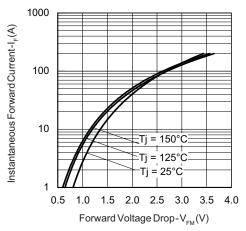


Fig. 1 - Typical Forward Voltage Drop Characteristics

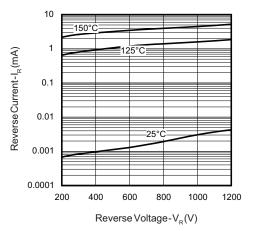


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

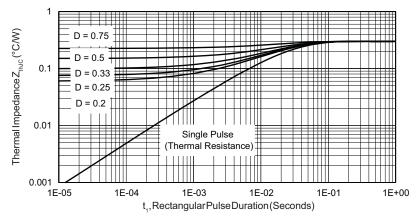


Fig. 3 - Maximum Thermal Impedance Z_{thJC} Characteristics

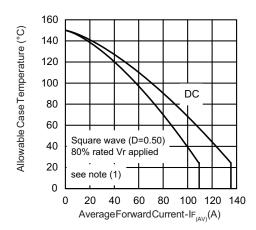


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

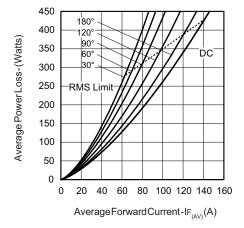


Fig. 5 - Forward Power Loss Characteristics

HFA200FA120P

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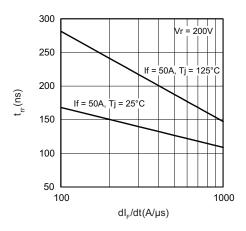


Fig. 6 - Typical Reverse Recovery Time vs. dI_F/dt

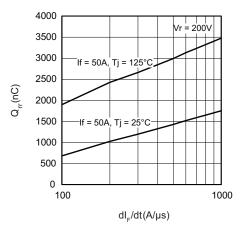


Fig. 7 - Typical Stored Charge vs. dI_F/dt

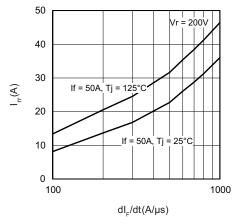


Fig. 8 - Typical Peak Recovery Current vs. dl_F/dt

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 5); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = Rated V_R$





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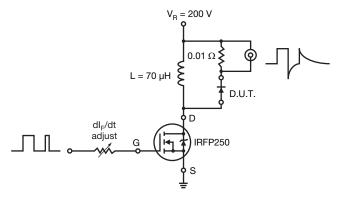
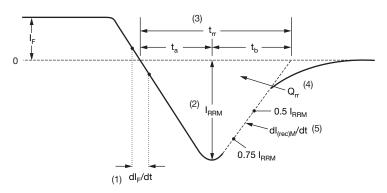


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) $\rm Q_{rr}$ area under curve defined by $\rm t_{rr}$ and $\rm I_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

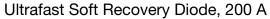
(5) $dl_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

HFA200FA120P

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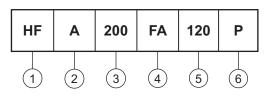


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ORDERING INFORMATION TABLE

Device code



1 - HEXFRED® family

Process designator (A = Electron irradiated)

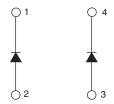
3 - Average current (200 = 200 A)

4 - Package outline (FA = SOT-227)

5 - Voltage rating (120 = 1200 V)

6 - P = Lead (Pb)-free

CIRCUIT CONFIGURATION



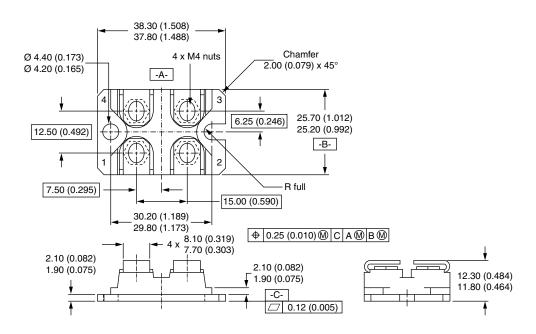
| LINKS TO RELATED DOCUMENTS | | | | |
|--|--------------------------|--|--|--|
| Dimensions <u>www.vishay.com/doc?95036</u> | | | | |
| Packaging information | www.vishay.com/doc?95037 | | | |



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SOT-227

DIMENSIONS in millimeters (inches)



Notes

- Dimensioning and tolerancing per ANSI Y14.5M-1982
- · Controlling dimension: millimeter

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