

October 2014

# **KSA1281 PNP Epitaxial Silicon Transistor**

# **Features**

- Audio Power Amplifier
- 3 W Output Application



# **Ordering Information**

Part Number	Top Mark	Package	Packing Method
KSA1281YTA	A1281 Y-	TO-92 3L	Ammo

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-Base Voltage	-50	V
V <sub>CEO</sub>	Collector-Emitter Voltage	-50	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5	٧
I <sub>C</sub>	Collector Current	-2	Α
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	-55 to +150	°C

# Thermal Characteristics(1)

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Value	Unit
D	Power Dissipation T <sub>C</sub> = 25°C	1000	mW
P <sub>D</sub>	Derate Above T <sub>A</sub> = 25°C	8.0	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	125	°C/W

# Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

# Electrical Characteristics(2)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C = -100 \text{ mA}, I_E = 0$	-50			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_C = -10 \text{ mA}, I_B = 0$	-50			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = -1 \text{ mA}, I_C = 0$	-5			V
I <sub>CBO</sub>	Collector Cut-Off Current	$V_{CB} = -50 \text{ V}, I_{E} = 0$			-100	nA
I <sub>EBO</sub>	Emitter Cut-Off Current	$V_{EB} = -5 \text{ V}, I_{C} = 0$		\	-100	nA
h <sub>FE1</sub>	DC Current Gain	$V_{CE} = -2 \text{ V}, I_{C} = -500 \text{ mA}$	70		240	
h <sub>FE2</sub>	DC Current Gain	$V_{CE} = -2 \text{ V}, I_{C} = -1.5 \text{ A}$	40			
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	$I_C = -1 A$ , $I_B = -0.05 A$			-1.2	V
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	$I_C = -1 A$ , $I_B = -0.05 A$			-0.5	V
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = -10 V, I <sub>E</sub> = 0, f = 1 MHz		40		pF
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = -2 \text{ V}, I_{C} = -500 \text{ mA}$		100		MHz

# Note:

2. Pulse test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2.0%.

# **h**<sub>FE</sub> Classification

Classification	0	Y
h <sub>FE1</sub>	70 ~ 140	120 ~ 240

# **Typical Performance Characteristics**

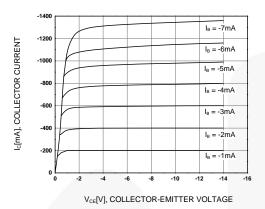


Figure 1. Static Characteristic

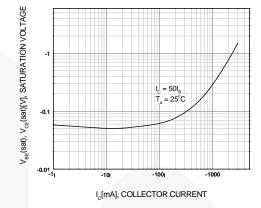


Figure 2. Base-Emitter Saturation Voltage

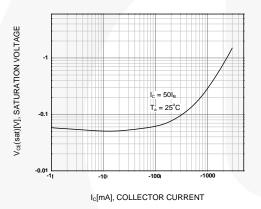


Figure 3. Collector-Emitter Saturation Voltage

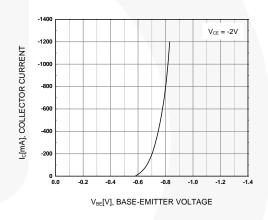


Figure 4. Base-Emitter On Voltage

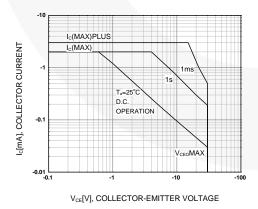


Figure 5. Safe Operating Area

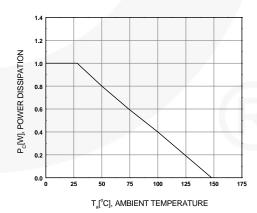
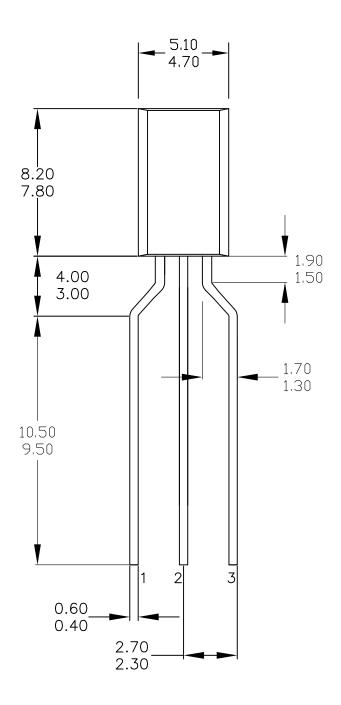
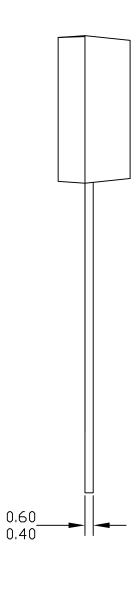
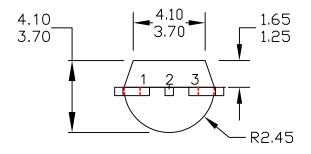


Figure 6. Power Derating







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Definition of Terms				
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