# MEST2G-025-10-CM32



## **Pin Diode Switch Element**

Rev. V1

#### **Features**

- Power Handling 25 W @ 4 GHz or less @ 100 mA
- Low Insertion Loss: <0.4 dB @ 1 4 GHz
- Medium Isolation: >10 dB @ 2.0 GHz
- RoHS\* Compliant

## **Description**

The MEST2G-025-10-CM32 is a thermal to ground series diode switch element (EST2G) in an Aluminum Nitride package. This part is designed to handle up to 25 watts. Usable up to 4.0 GHz.



(CM32) non-hermetic

## Electrical Specifications: $T_C = +25^{\circ}C$ (unless otherwise specified)

| Parameter                                       | Test Conditions  | Units | Min.     | Тур.         | Max.         |
|---|--|-------|----------|--------------|--------------|
| Breakdown Voltage (V <sub>BR</sub> )            | I <sub>R</sub> = 10 μA   | V     | 200      | _            | _            |
| Leakage Current (I <sub>R</sub> )               | V <sub>R</sub> = 100 V   | nA    | _        | 40           | 100          |
| Forward Voltage (V <sub>F</sub> )               | I <sub>F</sub> = 100 mA  | V     | _        | 980          | _            |
| Series Resistance (R <sub>S</sub> )             | I <sub>F</sub> = 100 mA, 100 MHZ                                 | Ω     |          | 1.25         | _            |
| Total Capacitance (C <sub>T</sub> )             | V <sub>R</sub> = -50 V, 1 MHz                                    | pF    |          | 0.22         | _            |
| Lifetime (t)                                    | I <sub>F</sub> = 10 mA, I <sub>R</sub> = 6 mA, 50%               | ns    |          | 2000         | _            |
| I-Region (w)                                    | I-Layer  | μm    | _        | 140          | _            |
| Input / Output Return Loss (I/OR <sub>L</sub> ) | I <sub>F</sub> = 100 mA, 2 GHz<br>I <sub>F</sub> = 100 mA, 4 GHz | dB    | 27<br>20 | 32<br>26     | _            |
| Insertion Loss (I <sub>L</sub> )                | I <sub>F</sub> = 100 mA, 2 GHz<br>I <sub>F</sub> = 100 mA, 4 GHz | dB    | _        | 0.06<br>0.07 | 0.15<br>0.15 |
| Isolation (I <sub>SO</sub> )                    | I <sub>F</sub> = 400 mA, 2 GHz<br>I <sub>F</sub> = 400 mA, 4 GHz | dB    | 8<br>—   | 10<br>6      | _            |

<sup>\*</sup> Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.



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## **Absolute Maximum Ratings**<sup>1,2</sup>

| Parameter                                | Absolute Maximum              |  |  |
|--|-------------------------------|--|--|
| Breakdown Voltage (V <sub>R</sub> )      | 200 V                         |  |  |
| Forward Current (I <sub>FDC</sub> )      | 200 mA                        |  |  |
| Thermal Resistance (θ <sub>JC</sub> )    | 25°C/W                        |  |  |
| Junction Temperature (T <sub>J</sub> )   | -40°C to 175°C                |  |  |
| Storage Temperature (T <sub>STG</sub> )  | -55°C to +150°C               |  |  |
| Mounting Temperature (T <sub>MTG</sub> ) | +260°C per<br>JEDEC STD-J-20C |  |  |

- 1. Exceeding any one or combination of these limits may cause permanent damage to this device.
- MACOM does not recommend sustained operation near these survivability limits.

## **Handling Procedures**

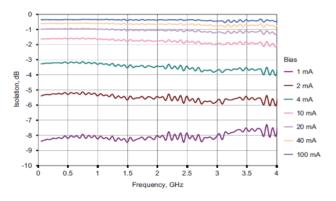
Please observe the following precautions to avoid damage:

## **Static Sensitivity**

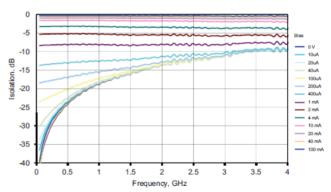
These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 0 (HBM) devices.

## Typical Performance Curves: $T_A = 25^{\circ}C$ , $Z_O = 50 \Omega$ , -10 dBm Small Signal

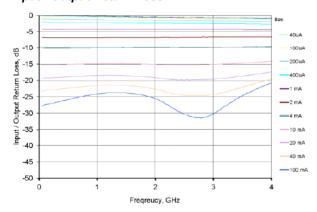
#### Insertion Loss



### Isolation



#### Input / Output Return Loss

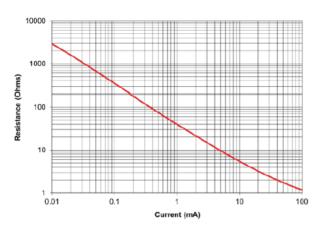




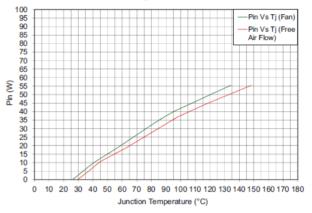
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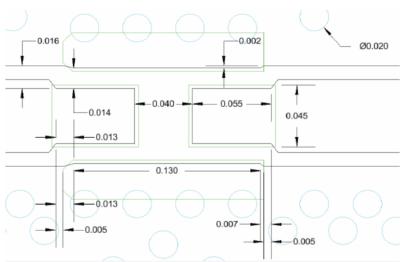
#### Resistance vs. Bias Current, 100 MHz



# Junction Temperature vs. Input Power Mounted on Heat Sink @ $T_A$ = 25°C, 1.3 GHz



## **PCB Layout**



Plated through, filled and plated over vias

Solder mask should provide 60um clearance between copper pad and solder mask. Rounded pkg pads should have matching rounded solder mask openings.

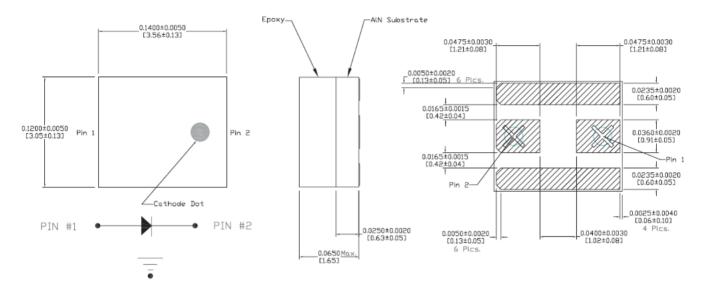
Use circles or squares for the thermal land stencil design such that only get  $60\ to\ 80\%$  solder paste coverage.



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## Outline (CM32)



Pin function for Silicon PIN diode.

- 1. Anode
- 2. Cathode

#### Notes:

1. Metallization: 250-350  $\mu^{\prime\prime}$  Cu / 60-100  $\mu^{\prime\prime}$  Ni / 15-45  $\mu^{\prime\prime}$  Au

2. Dimensions in mils [mm]

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