



PMF63UN

20 V, single N-channel Trench MOSFET

Rev. 1 — 22 March 2012

Product data sheet

1. Product profile

1.1 General description

N-channel enhancement mode Field-Effect Transistor (FET) in a SOT323 (SC-70) small Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

1.2 Features and benefits

- Low threshold voltage
- Trench MOSFET technology
- Very fast switching

1.3 Applications

- Relay driver
- Low-side loadswitch
- High-speed line driver
- Switching circuits

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{DS}	drain-source voltage	$T_j = 25\text{ }^{\circ}\text{C}$	-	-	20	V
V_{GS}	gate-source voltage		-8	-	8	V
I_D	drain current	$V_{GS} = 4.5\text{ V}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$; $t \leq 5\text{ s}$	[1]	-	1.9	A
Static characteristics						
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 4.5\text{ V}$; $I_D = 1.8\text{ A}$; $T_j = 25\text{ }^{\circ}\text{C}$	-	63	74	m Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	SOT323 (SC-70)	017aaa253
2	S	source		
3	D	drain		



3. Ordering information

Table 3. Ordering information

Type number	Package		Version
	Name	Description	
PMF63UN	SC-70	plastic surface-mounted package; 3 leads	SOT323

4. Marking

Table 4. Marking codes

Type number	Marking code ^[1]
PMF63UN	V8%

[1] % = placeholder for manufacturing site code

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

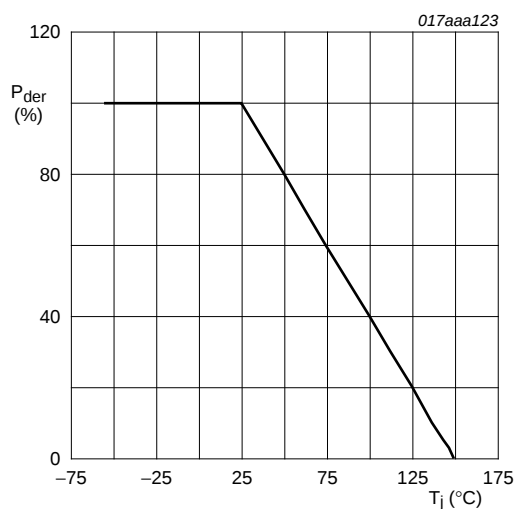
Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage	$T_j = 25\text{ }^{\circ}\text{C}$	-	20	V
V_{GS}	gate-source voltage		-8	8	V
I_D	drain current	$V_{GS} = 4.5\text{ V}; T_{amb} = 25\text{ }^{\circ}\text{C}; t \leq 5\text{ s}$ ^[1]	-	1.9	A
		$V_{GS} = 4.5\text{ V}; T_{amb} = 25\text{ }^{\circ}\text{C}$ ^[1]	-	1.8	A
		$V_{GS} = 4.5\text{ V}; T_{amb} = 100\text{ }^{\circ}\text{C}$ ^[1]	-	1.1	A
I_{DM}	peak drain current	$T_{amb} = 25\text{ }^{\circ}\text{C}$; single pulse; $t_p \leq 10\text{ }\mu\text{s}$	-	7.2	A
P_{tot}	total power dissipation	$T_{amb} = 25\text{ }^{\circ}\text{C}$ ^[2]	-	275	mW
		^[1]	-	350	mW
		$T_{sp} = 25\text{ }^{\circ}\text{C}$	-	1785	mW
T_j	junction temperature		-55	150	$^{\circ}\text{C}$
T_{amb}	ambient temperature		-55	150	$^{\circ}\text{C}$
T_{stg}	storage temperature		-65	150	$^{\circ}\text{C}$

Source-drain diode

I_S	source current	$T_{amb} = 25\text{ }^{\circ}\text{C}$ ^[1]	-	0.8	A
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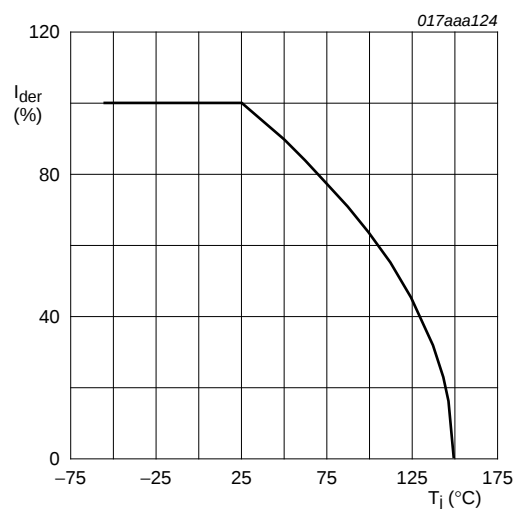
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



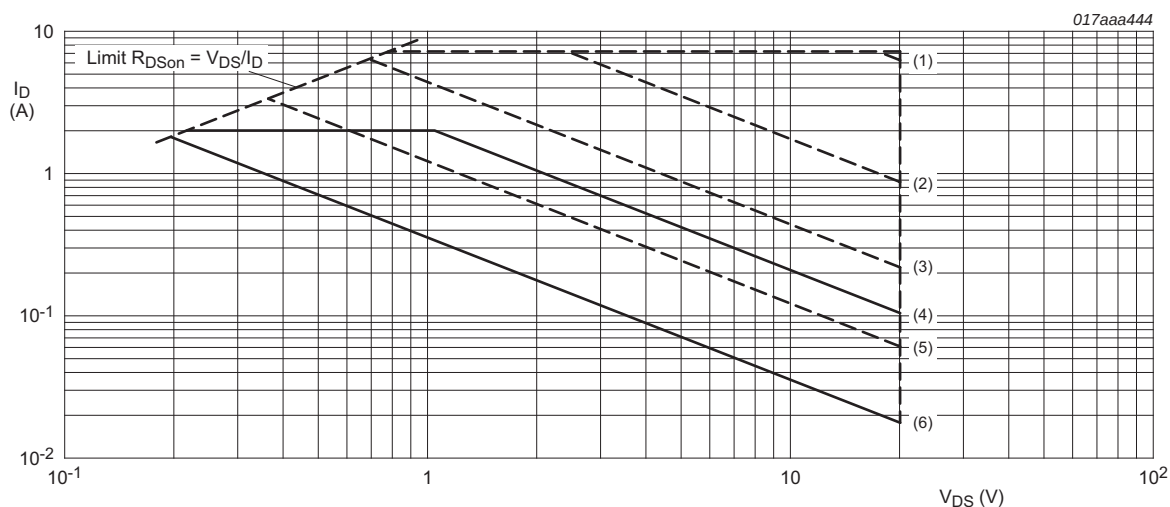
$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}\text{C})}} \times 100 \%$$

Fig 1. Normalized total power dissipation as a function of junction temperature



$$I_{der} = \frac{I_D}{I_{D(25^{\circ}\text{C})}} \times 100 \%$$

Fig 2. Normalized continuous drain current as a function of junction temperature



I_{DM} = single pulse

(1) $t_p = 100 \mu\text{s}$

(2) $t_p = 1 \text{ ms}$

(3) $t_p = 10 \text{ ms}$

(4) DC; $T_{sp} = 25^{\circ}\text{C}$

(5) $t_p = 100 \text{ ms}$

(6) DC; $T_{amb} = 25^{\circ}\text{C}$; drain mounting pad 6 cm^2

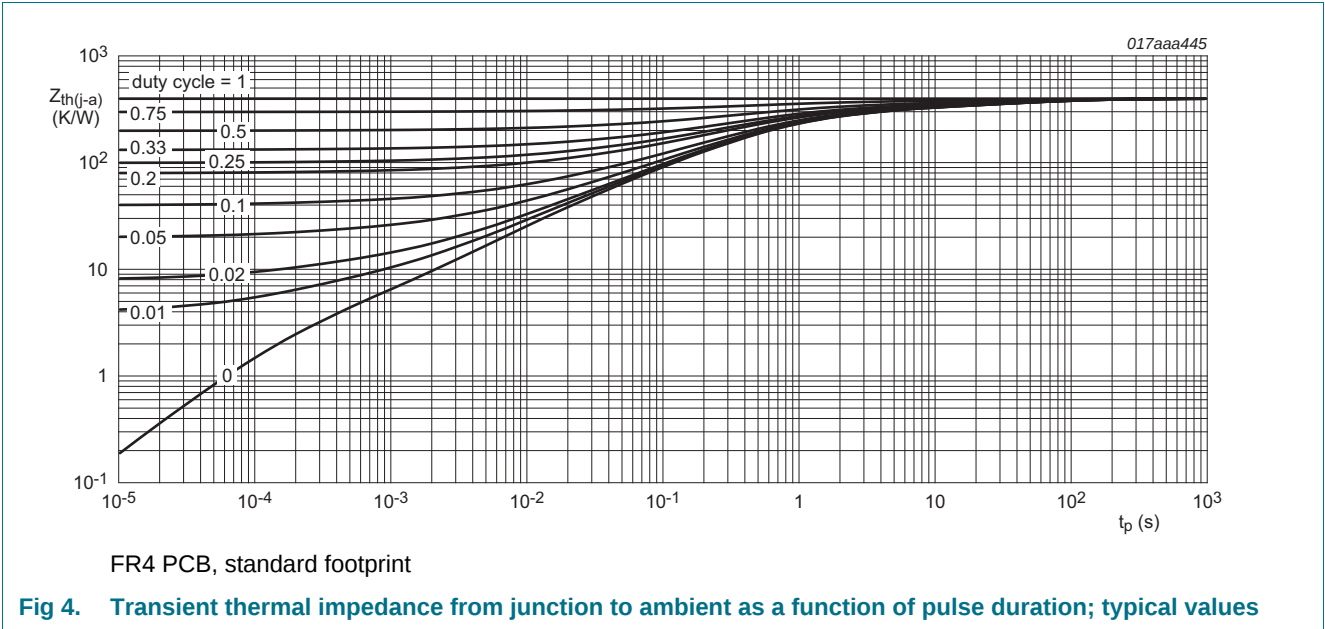
Fig 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	395	455 K/W
			[2]	-	308	355 K/W
			[3]	-	263	305 K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		-	60	70	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm², t ≤ 5 s.



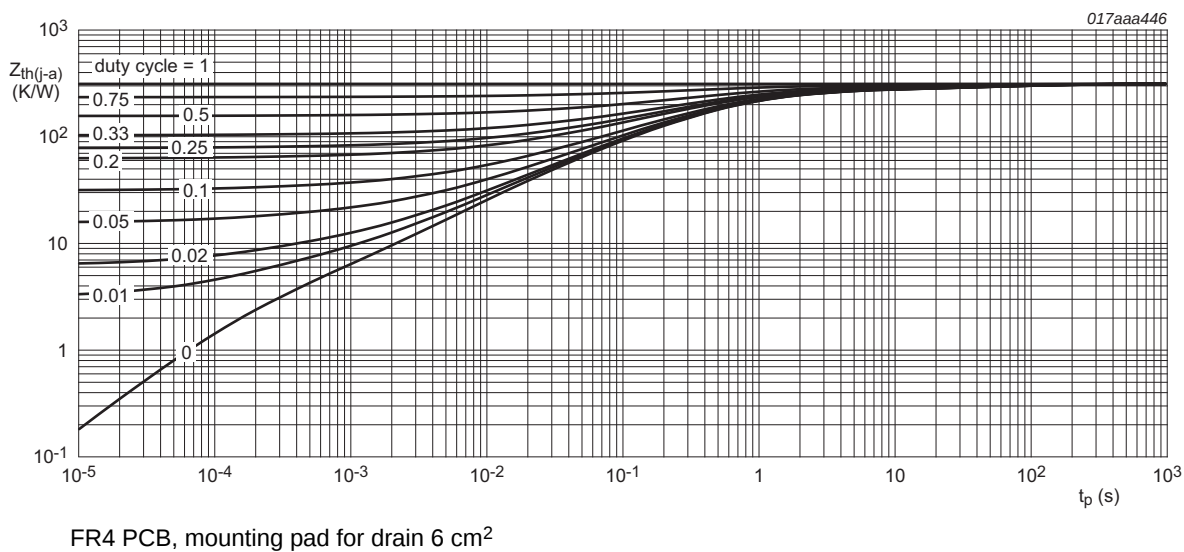


Fig 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

7. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250\ \mu A$; $V_{GS} = 0\ V$; $T_j = 25\ ^\circ C$	20	-	-	V
V_{GSth}	gate-source threshold voltage	$I_D = 250\ \mu A$; $V_{DS} = V_{GS}$; $T_j = 25\ ^\circ C$	0.4	0.7	1	V
I_{DSS}	drain leakage current	$V_{DS} = 20\ V$; $V_{GS} = 0\ V$; $T_j = 25\ ^\circ C$	-	-	1	μA
		$V_{DS} = 20\ V$; $V_{GS} = 0\ V$; $T_j = 150\ ^\circ C$	-	-	20	μA
I_{GSS}	gate leakage current	$V_{GS} = 8\ V$; $V_{DS} = 0\ V$; $T_j = 25\ ^\circ C$	-	-	100	nA
		$V_{GS} = -8\ V$; $V_{DS} = 0\ V$; $T_j = 25\ ^\circ C$	-	-	100	nA
R_{DSon}	drain-source on-state resistance	$V_{GS} = 4.5\ V$; $I_D = 1.8\ A$; $T_j = 25\ ^\circ C$	-	63	74	m Ω
		$V_{GS} = 4.5\ V$; $I_D = 1.8\ A$; $T_j = 150\ ^\circ C$	-	92	108	m Ω
		$V_{GS} = 2.5\ V$; $I_D = 1.6\ A$; $T_j = 25\ ^\circ C$	-	77	96	m Ω
		$V_{GS} = 1.8\ V$; $I_D = 0.8\ A$; $T_j = 25\ ^\circ C$	-	114	162	m Ω
g_{fs}	forward transconductance	$V_{DS} = 10\ V$; $I_D = 1.8\ A$; $T_j = 25\ ^\circ C$	-	8	-	S
Dynamic characteristics						
$Q_{G(tot)}$	total gate charge	$V_{DS} = 10\ V$; $I_D = 1.8\ A$; $V_{GS} = 4.5\ V$; $T_j = 25\ ^\circ C$	-	2.2	3.3	nC
Q_{GS}	gate-source charge		-	0.36	-	nC
Q_{GD}	gate-drain charge		-	0.55	-	nC
C_{iss}	input capacitance	$V_{DS} = 10\ V$; $f = 1\ MHz$; $V_{GS} = 0\ V$; $T_j = 25\ ^\circ C$	-	185	-	pF
C_{oss}	output capacitance		-	53	-	pF
C_{rss}	reverse transfer capacitance		-	27	-	pF
$t_{d(on)}$	turn-on delay time	$V_{DS} = 10\ V$; $I_D = 1.8\ A$; $V_{GS} = 4.5\ V$; $R_{G(ext)} = 6\ \Omega$; $T_j = 25\ ^\circ C$	-	8	-	ns
t_r	rise time		-	27	-	ns
$t_{d(off)}$	turn-off delay time		-	31	-	ns
t_f	fall time		-	17	-	ns
Source-drain diode						
V_{SD}	source-drain voltage	$I_S = 0.8\ A$; $V_{GS} = 0\ V$; $T_j = 25\ ^\circ C$	-	0.8	1.2	V

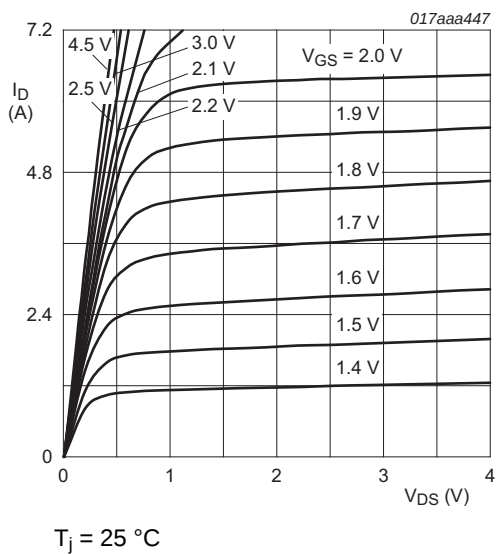


Fig 6. Output characteristics: drain current as a function of drain-source voltage; typical values

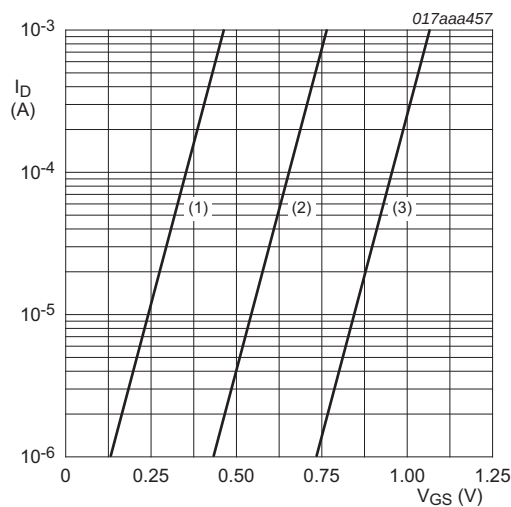


Fig 7. Sub-threshold drain current as a function of gate-source voltage

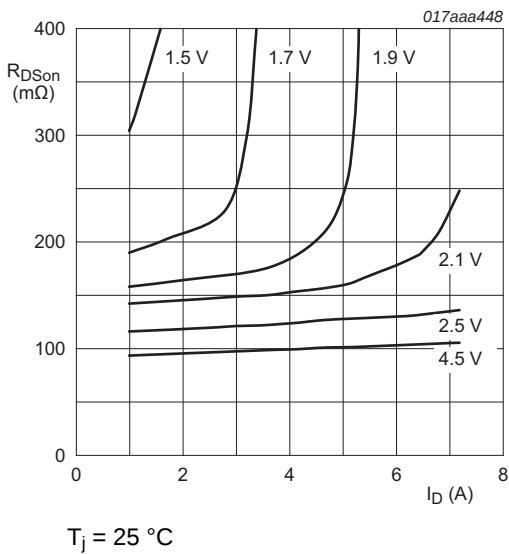


Fig 8. Drain-source on-state resistance as a function of drain current; typical values

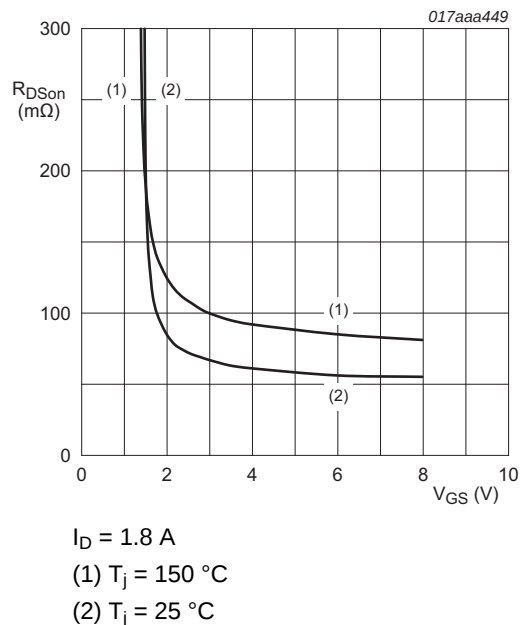


Fig 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

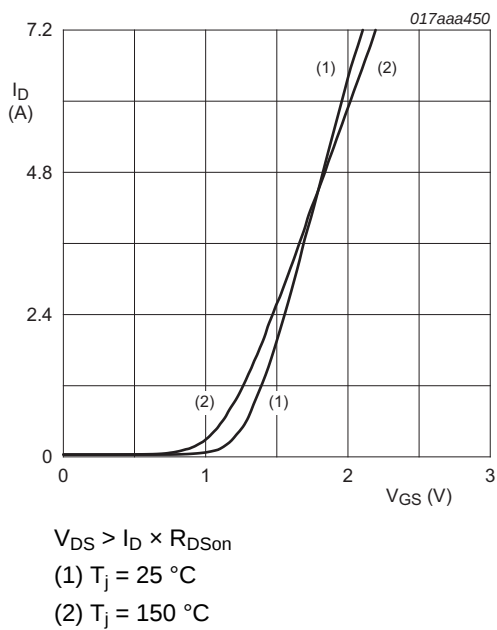


Fig 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

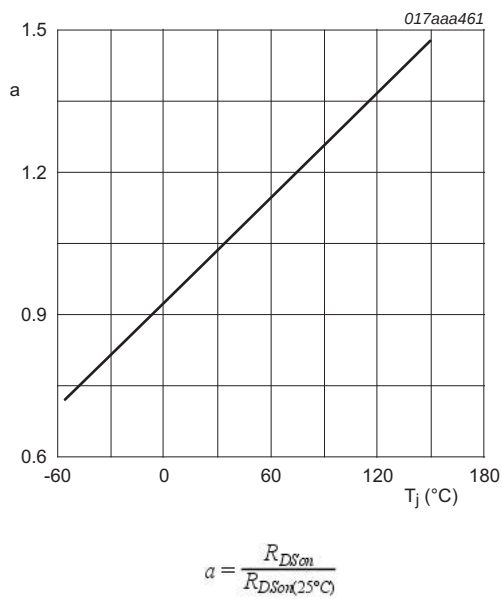


Fig 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

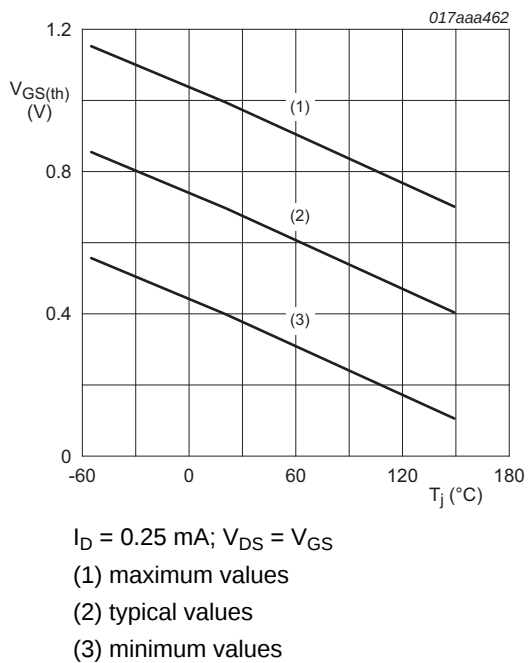


Fig 12. Gate-source threshold voltage as a function of junction temperature

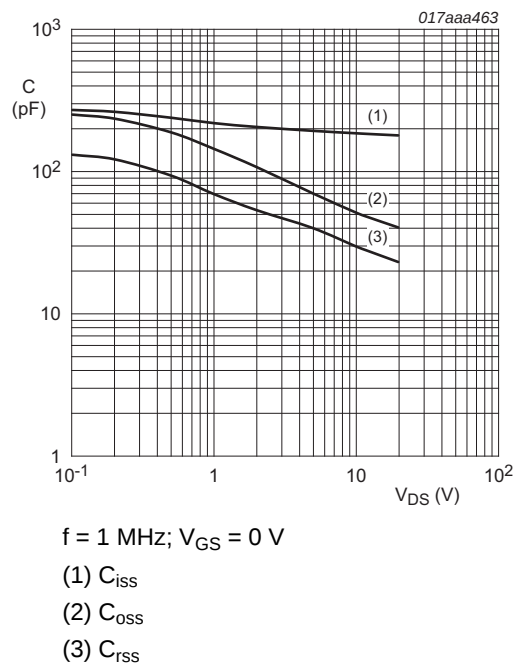
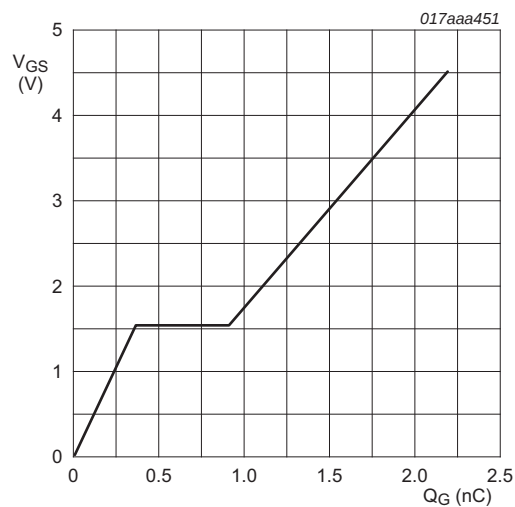


Fig 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



$I_D = 1.8$ A; $V_{DS} = 10$ V; $T_{amb} = 25$ °C

Fig 14. Gate-source voltage as a function of gate charge; typical values

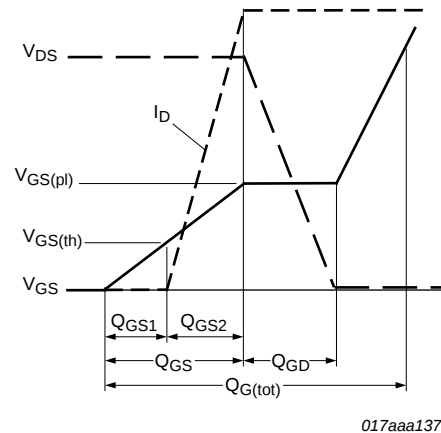
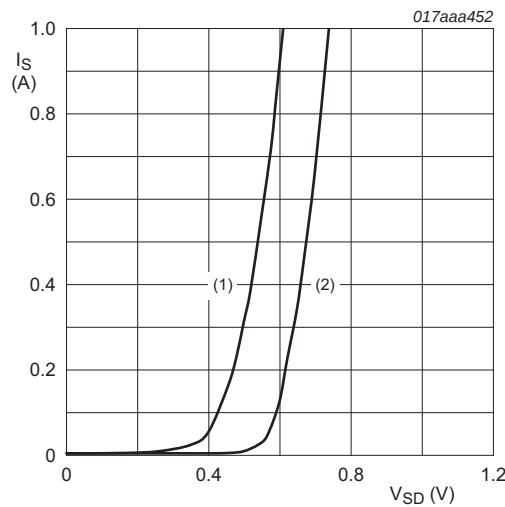


Fig 15. Gate charge waveform definitions



$V_{GS} = 0$ V
(1) $T_j = 150$ °C
(2) $T_j = 25$ °C

Fig 16. Source current as a function of source-drain voltage; typical values

8. Test information

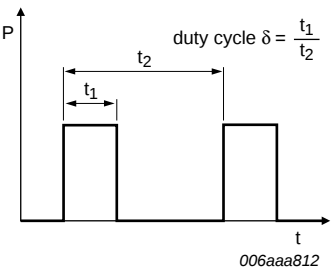


Fig 17. Duty cycle definition

9. Package outline

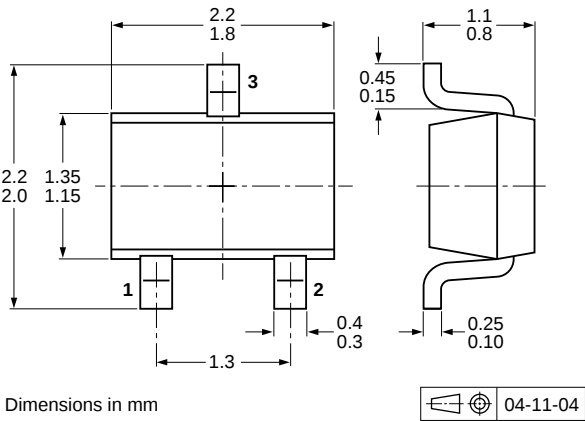
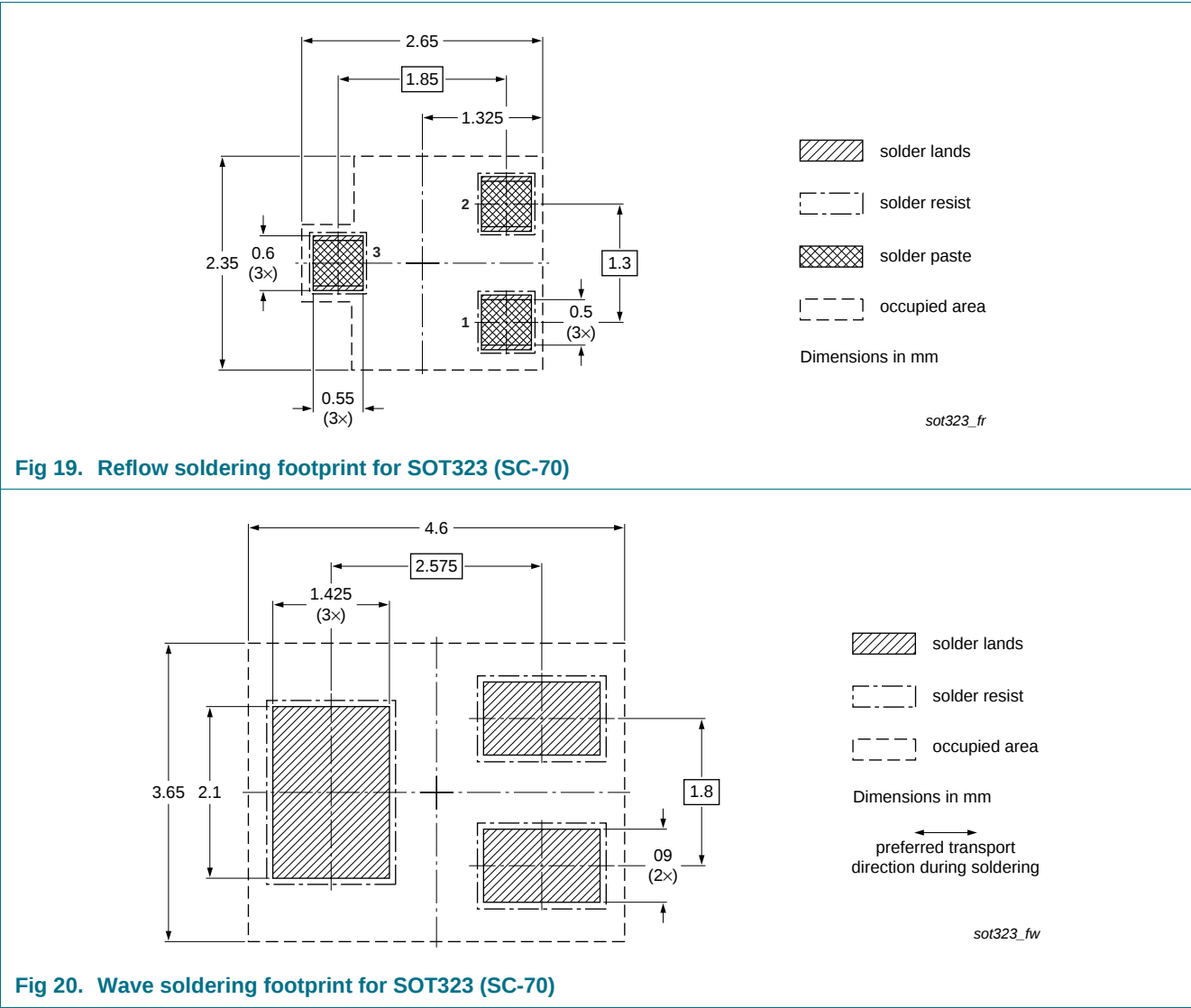


Fig 18. Package outline SOT323 (SC-70)

10. Soldering



11. Revision history

Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PMF63UN v.1	20120322	Product data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status ^{[1] [2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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